参考資料

COSEL

TEST DATA OF YS1512A

(100V　INPUT)

Regulated DC Power Supply

Date : Mar.25. 1999

Approved by : _____
Design Manager

Prepared by : _____
Design Engineer

コーセル株式会社

COSEL CO.,LTD.

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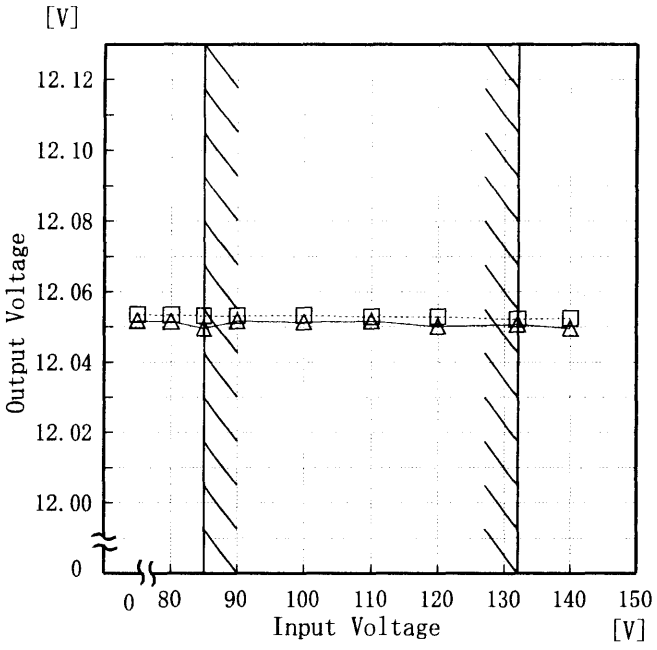
Model	YS1512A
Item	Line Regulation 静的入力変動
Object	+12.0V1.30A

Temperature 25℃
Testing Circuitry Figure A

1. Graph

□ Load 50%
△ Load 100%



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Load 50% Output Volt. [V]	Load 100% Output Volt. [V]
75	12.054	12.052
80	12.053	12.052
85	12.053	12.050
90	12.053	12.052
100	12.053	12.051
110	12.053	12.052
120	12.053	12.050
132	12.052	12.051
140	12.052	12.050

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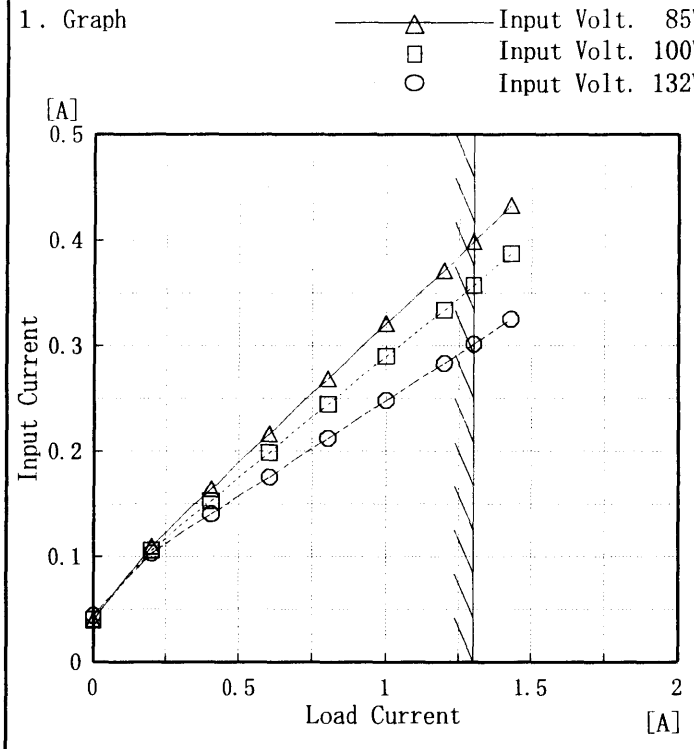
COSEL

Model	YS1512A
Item	Input Current (by Load Current) 入力電流 (負荷特性)
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

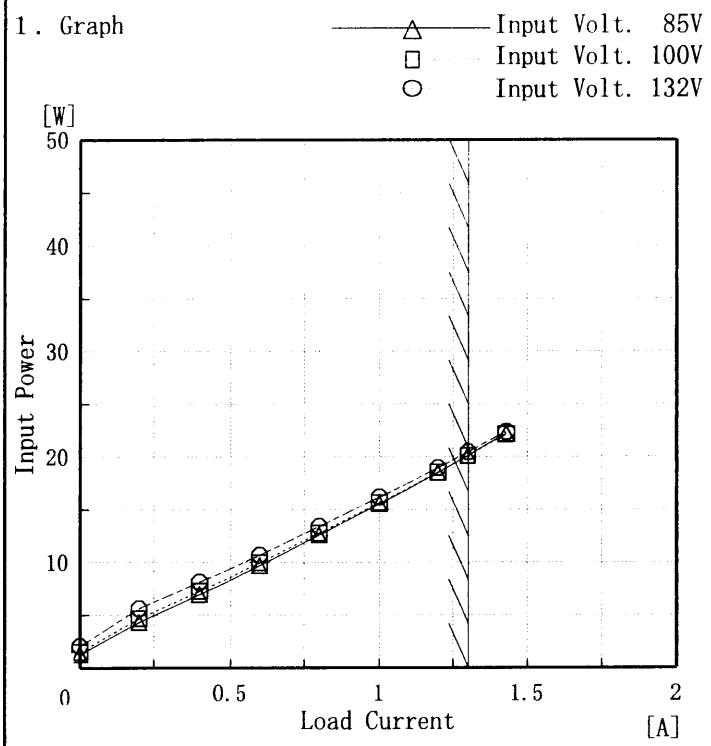
Load Current [A]	Input Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	0.039	0.041	0.044
0.20	0.109	0.106	0.103
0.40	0.164	0.153	0.140
0.60	0.216	0.199	0.175
0.80	0.269	0.244	0.212
1.00	0.321	0.290	0.248
1.20	0.371	0.334	0.283
1.30	0.398	0.357	0.302
1.43	0.432	0.387	0.325
—	—	—	—
—	—	—	—
—	—	—	—

COSEL

Model	YS1512A
Item	Input Power (by Load Current) 入力電力（負荷特性）
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

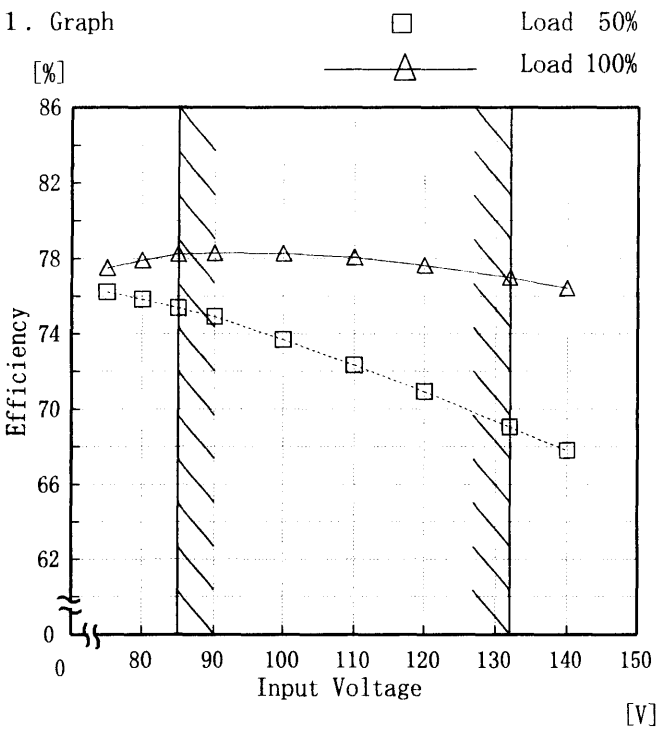
Load Current [A]	Input Power [W] Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	1.27	1.50	2.04
0.20	4.29	4.66	5.62
0.40	6.94	7.25	8.14
0.60	9.68	9.93	10.67
0.80	12.57	12.75	13.39
1.00	15.54	15.65	16.17
1.20	18.51	18.53	18.96
1.30	20.13	20.11	20.45
1.43	22.18	22.13	22.38
—	—	—	—
—	—	—	—
—	—	—	—

COSEL

Model	YS1512A
Item	Efficiency (by Input Voltage) 効率（入力電圧特性）
Object	————————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Load 50% Efficiency [%]	Load 100% Efficiency [%]
75	76.2	77.5
80	75.8	77.9
85	75.4	78.3
90	74.9	78.3
100	73.7	78.3
110	72.4	78.1
120	70.9	77.6
132	69.0	77.0
140	67.8	76.4

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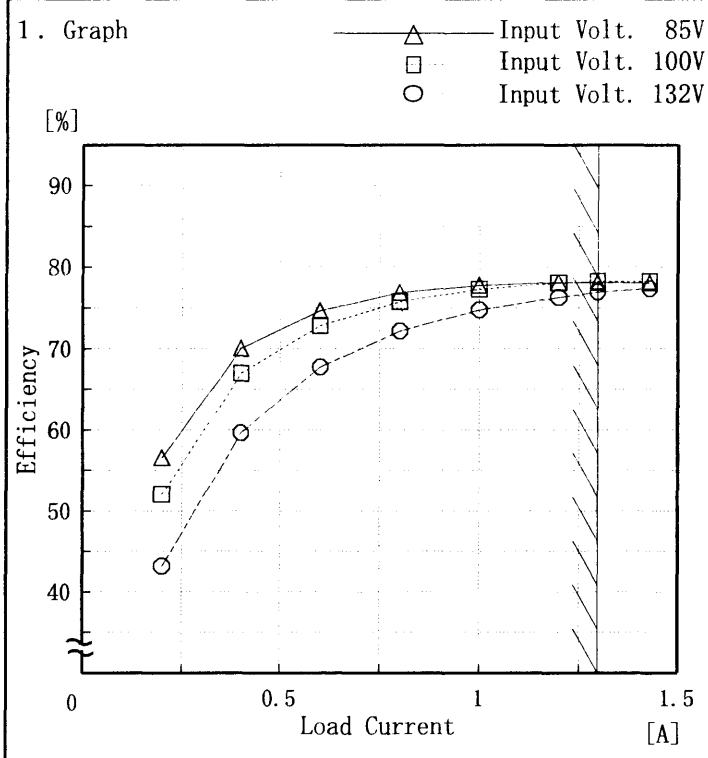
Model	YS1512A
Item	Efficiency (by Load Current) 効率 (負荷電流特性)
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Efficiency [%] Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.20	56.5	52.1	43.1
0.40	70.0	66.9	59.6
0.60	74.7	72.8	67.7
0.80	76.9	75.8	72.1
1.00	77.8	77.3	74.8
1.20	78.1	78.1	76.3
1.30	78.1	78.2	76.9
1.43	78.1	78.3	77.4
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

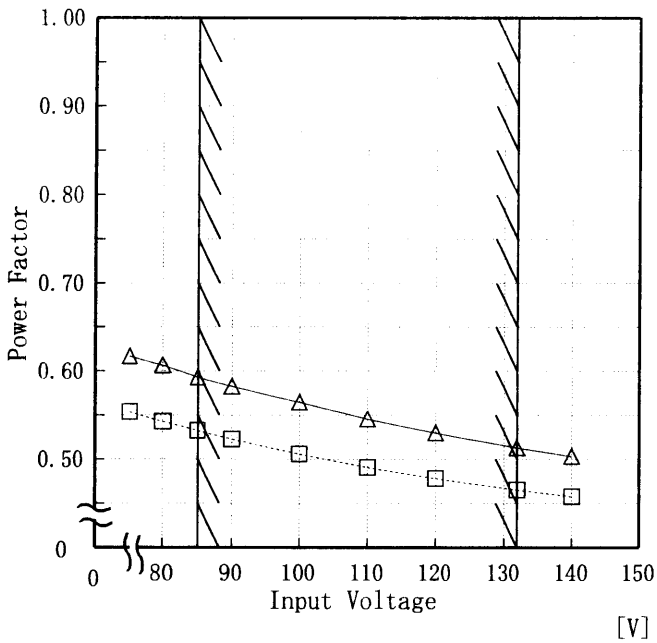
COSEL

Model	YS1512A
Item	Power Factor (by Input Voltage) 力率（入力電圧特性）
Object	————————————

Temperature 25℃
Testing Circuitry Figure A

1. Graph

□ load 50%
△ load 100%



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

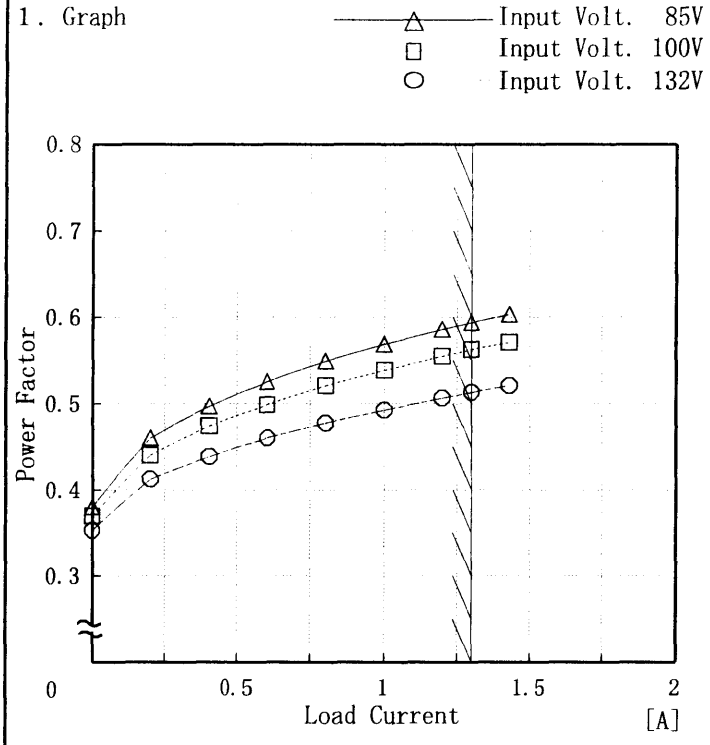
Input Voltage [V]	load 50% Power Factor	load 100% Power Factor
75	0.55	0.62
80	0.54	0.61
85	0.53	0.59
90	0.52	0.58
100	0.51	0.56
110	0.49	0.55
120	0.48	0.53
132	0.47	0.51
140	0.46	0.50

COSEL

Model	YS1512A
Item	Power Factor (by Load Current) 力率（負荷電流特性）
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

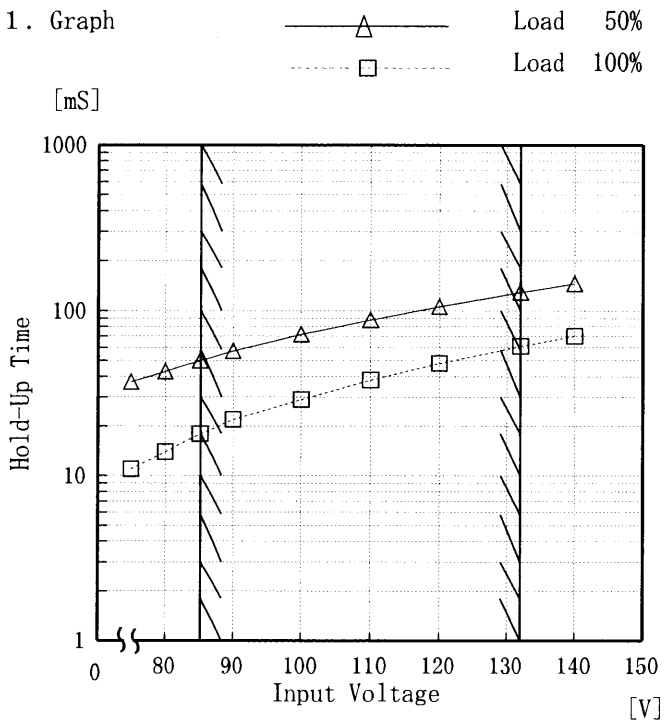
Load Current [A]	Power Factor Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	0.38	0.37	0.35
0.20	0.46	0.44	0.41
0.40	0.50	0.47	0.44
0.60	0.53	0.50	0.46
0.80	0.55	0.52	0.48
1.00	0.57	0.54	0.49
1.20	0.59	0.55	0.51
1.30	0.59	0.56	0.51
1.43	0.60	0.57	0.52
—	—	—	—
—	—	—	—
—	—	—	—

COSEL

Model	YS1512A
Item	Hold-Up Time　出力保持時間
Object	+12.0V1.30A

Temperature　25℃
Testing Circuitry　Figure A

1. Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated input voltage.

出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。

(注)斜線は定格入力電圧範囲を示す。

2. Values

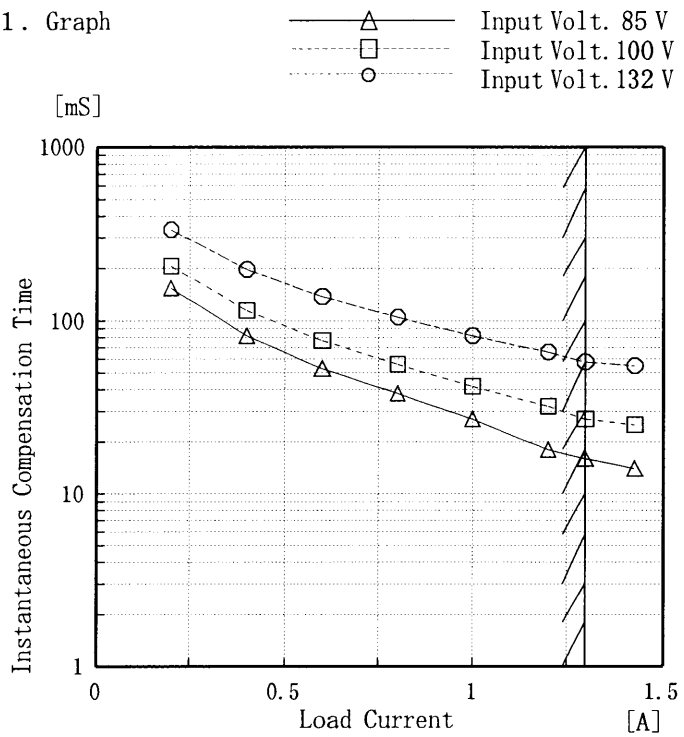
Input Voltage [V]	Load 50% Hold-Up Time [mS]	Load 100% Hold-Up Time [mS]
75	37	11
80	43	14
85	50	18
90	57	22
100	72	29
110	88	38
120	106	48
132	129	61
140	146	70

COSEL

Model	YS1512A
Item	Instantaneous Interruption Compensation 瞬時停電保障
Object	+12.0V1.30A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.
Note:Slanted line shows the range of the rated load current.

瞬時停電保障時間とは、出力電圧が定電圧精度の規格範囲を保持している瞬時停電時間をいう。
(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
	Time [mS]		
0.00	—	—	—
0.20	154	207	334
0.40	82	115	198
0.60	53	77	138
0.80	38	56	105
1.00	27	42	82
1.20	18	32	66
1.30	16	27	58
1.43	14	25	55
—	—	—	—
—	—	—	—

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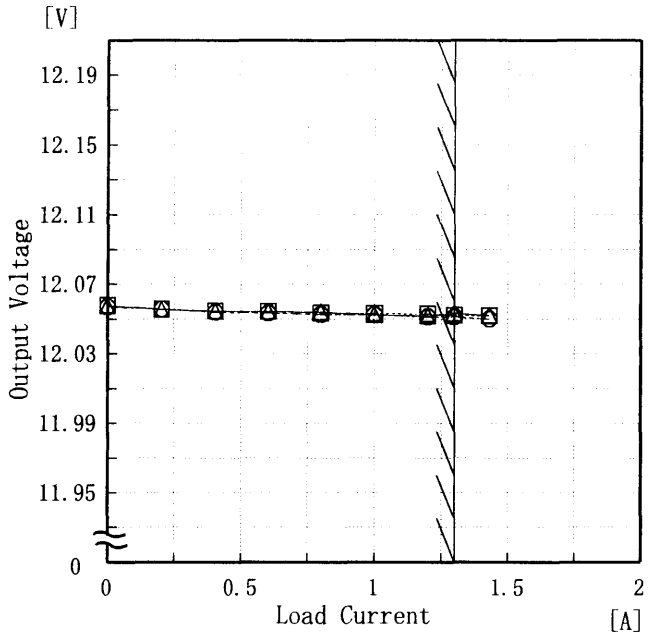
Model	YS1512A
Item	Load Regulation　静的負荷変動
Object	+12.0V1.30A

Temperature　25℃
Testing Circuitry　Figure A

1. Graph

△ Input Volt. 85V
□ Input Volt. 100V
○ Input Volt. 132V



Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Input Volt. 85[V] Output Volt. [V]	Input Volt. 100[V] Output Volt. [V]	Input Volt. 132[V] Output Volt. [V]
0.00	12.058	12.058	12.057
0.20	12.056	12.056	12.056
0.40	12.055	12.055	12.054
0.60	12.054	12.054	12.054
0.80	12.054	12.054	12.053
1.00	12.053	12.053	12.053
1.20	12.052	12.053	12.051
1.30	12.053	12.052	12.051
1.43	12.052	12.052	12.050
—	—	—	—

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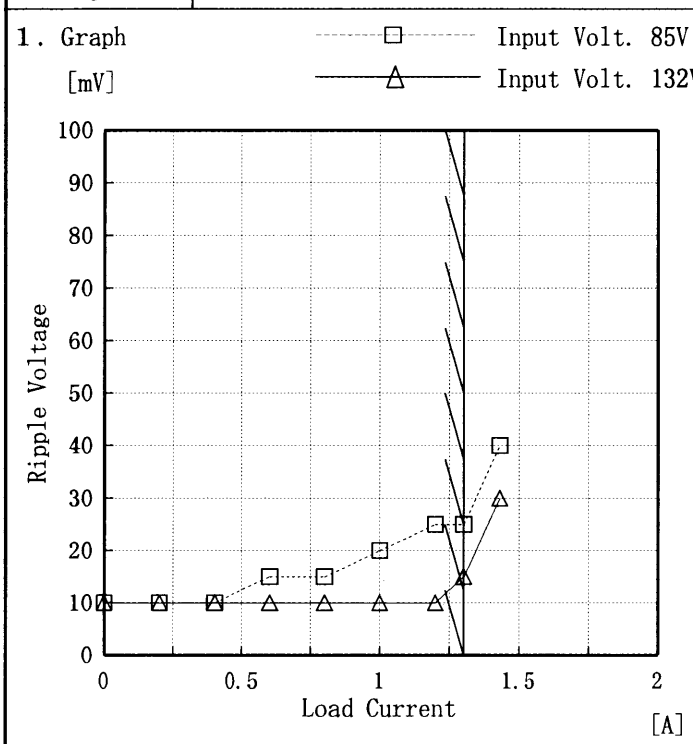
Model	YS1512A
Item	Ripple Voltage(by Load Current) リップル電圧(負荷電流特性)
Object	+12.0V1.30A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Ripple Voltage is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

リップル電圧は、下図ｐ－ｐ値で示される。

(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
ｽｲｯﾁﾝｸﾞ周期

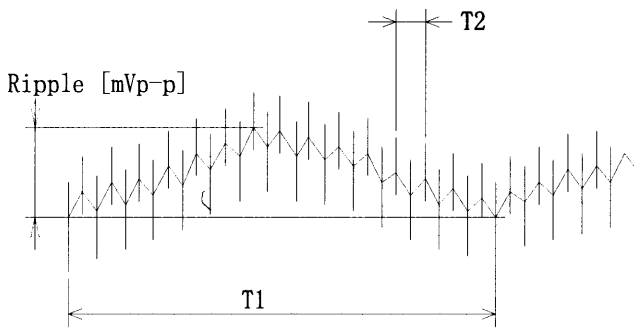


Fig. Complex Ripple Wave Form
図 リップル波形詳細図

2. Values

Load Current [A]	Input Volt. 85 [V] Ripple Output Volt. [mV]	Input Volt. 132 [V] Ripple Output Volt. [mV]
0.00	10	10
0.20	10	10
0.40	10	10
0.60	15	10
0.80	15	10
1.00	20	10
1.20	25	10
1.30	25	15
1.43	40	30
—	—	—
—	—	—

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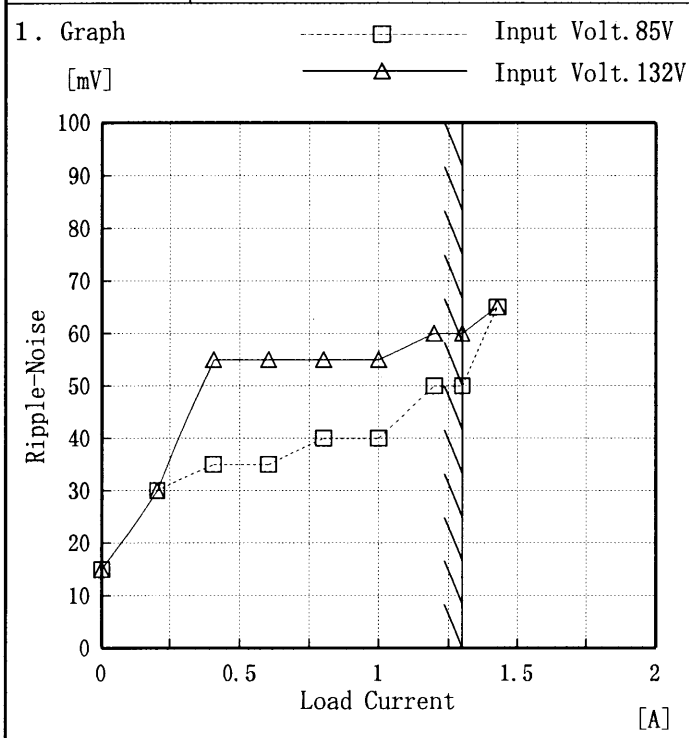
COSEL

Model	YS1512A
Item	Ripple-Noise　リップルノイズ
Object	+12.0V1.30A

Temperature　25℃
Testing Circuitry　Figure A

1. Graph



Ripple-Noise is shown as p-p in the figure below.
Note: Slanted line shows the range of the rated load current.

リップルノイズは、下図ｐ－ｐ値で示される。
(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
ｽｲｯﾁﾝｸﾞ周期

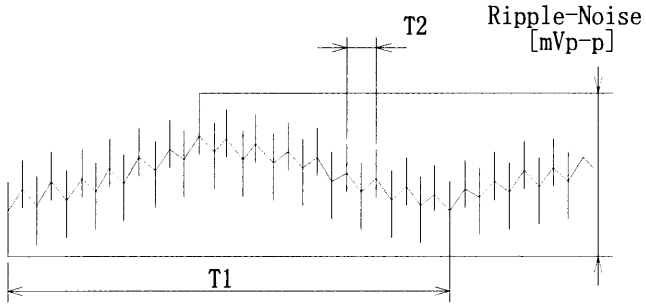


Fig. Complex Ripple Wave Form
図　リップル波形詳細図

2. Values

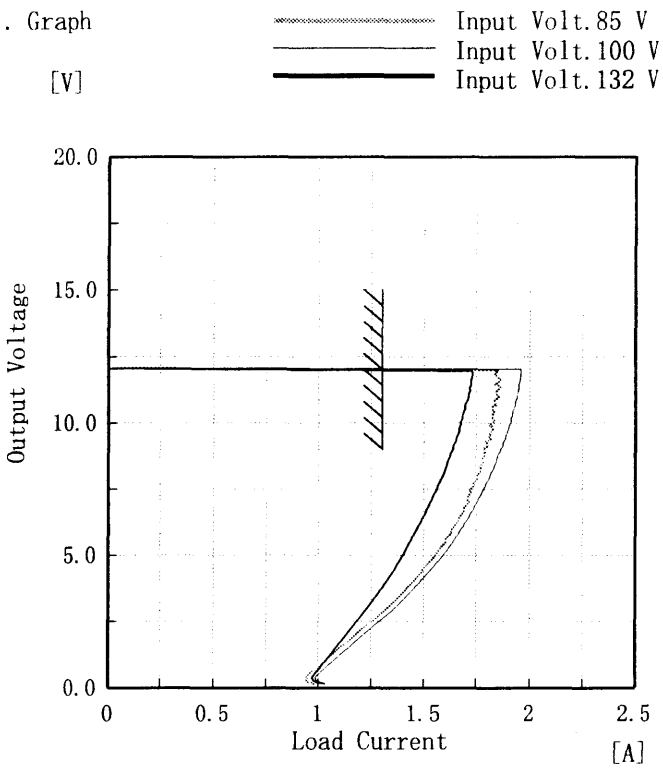
Load current [A]	Input Volt. 85 [V] Ripple-Noise [mV]	Input Volt. 132 [V] Ripple-Noise [mV]
0.00	15	15
0.20	30	30
0.40	35	55
0.60	35	55
0.80	40	55
1.00	40	55
1.20	50	60
1.30	50	60
1.43	65	65
—	—	—
—	—	—

COSEL

Model	YS1512A
Item	Overcurrent Protection 過電流保護
Object	+12.0V1.30A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Output Voltage [V]	Input Volt. 85[V] Load Current [A]	Input Volt. 100[V] Load Current [A]	Input Volt. 132[V] Load Current [A]
12.00	1.85	1.96	1.73
11.40	1.86	1.95	1.72
10.80	1.84	1.93	1.71
9.60	1.82	1.89	1.66
8.40	1.78	1.84	1.60
7.20	1.72	1.77	1.54
6.00	1.64	1.68	1.47
4.80	1.53	1.58	1.39
3.60	1.40	1.44	1.29
2.40	1.24	1.28	1.17
1.20	1.07	1.11	1.05
0.00	0.99	1.03	1.04

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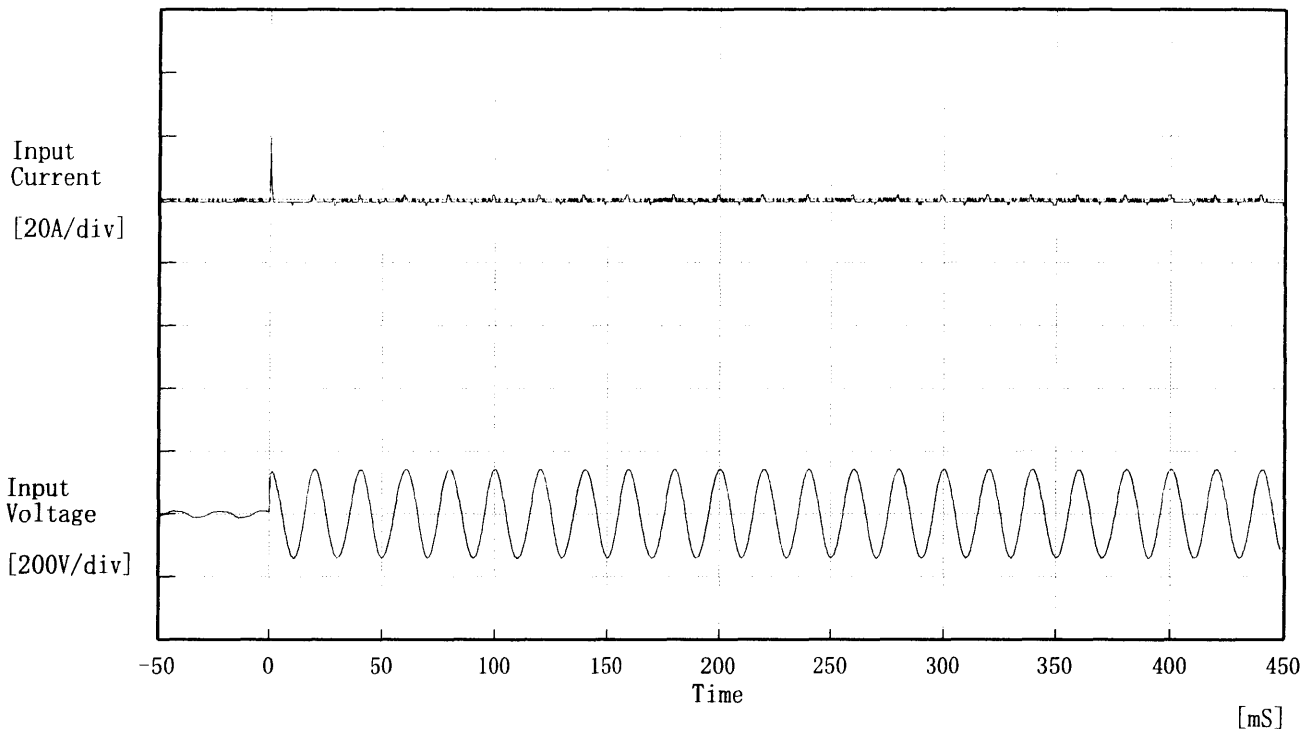
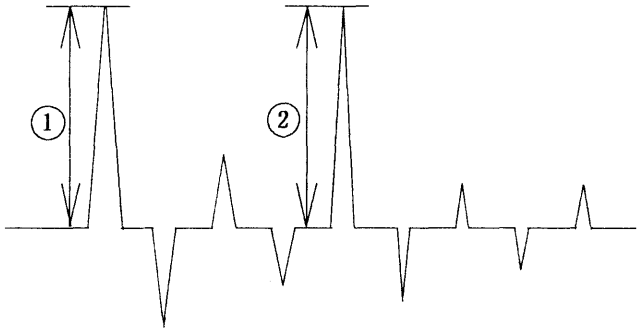
Model	YS1512A	Temperature	25℃
Item	Inrush Current 突入電流	Testing Circuitry	Figure A
Object	————————		



Input Voltage 100 V
Frequency 50 Hz
Load 100 %
Inrush Current
① 19.41 [A]
② 1.90 [A]



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Model	YS1512A	
Item	Dynamic Load Responce 動的負荷変動	Temperature 25℃ Testing Circuitry Figure A
Object	+12.0V1.30A	

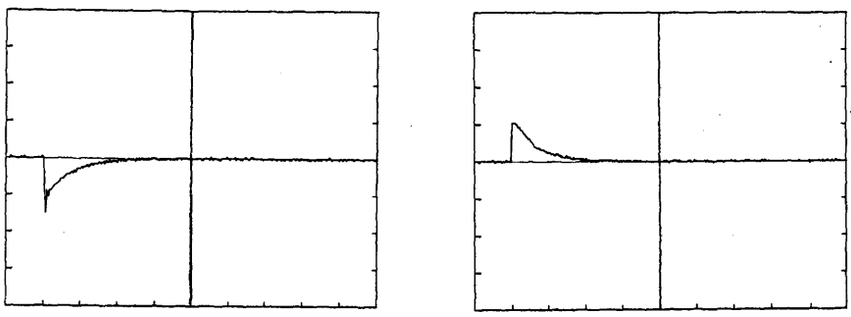
Input Volt. 100 V

Cycle 200 mS

Load Current

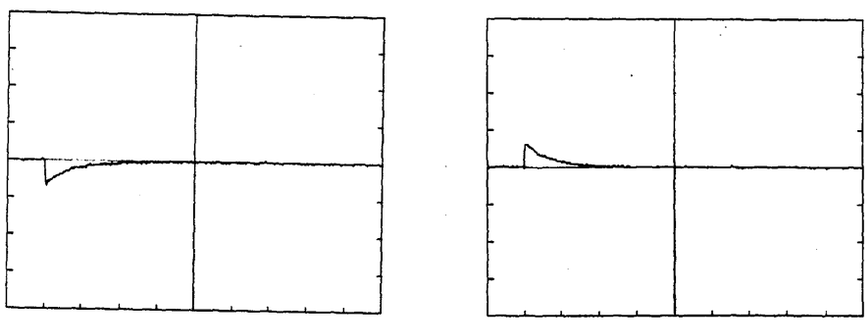
Load 0% ⟷

Load 100 %



Load 0% ⟷

Load 50 %



500 mV/div

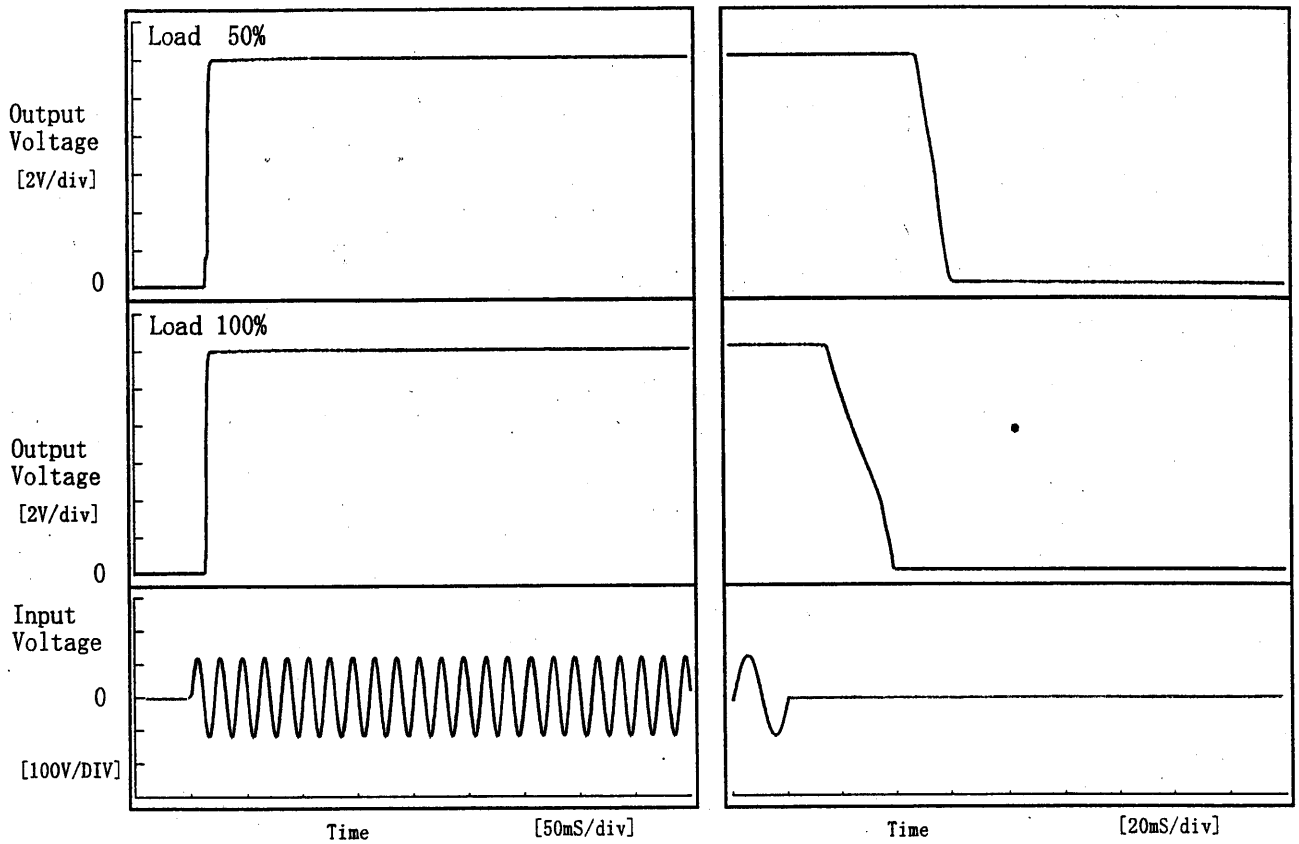
1 mS/div

COSEL

Model	YS1512A		
Item	Rise and Fall Time 立上り、立下り時間	Temperature	25℃
		Testing Circuitry	Figure A
Object	+12.0V1.30A		

1. Graph

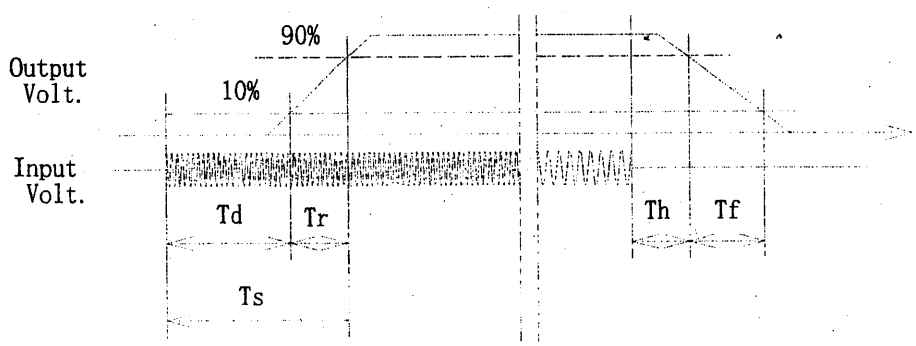
Input Volt. 85 V



2. Values

[mS]

Load \ Time	T d	T r	T s	T h	T f
50 %	12.8	4.0	16.8	49.7	9.8
100 %	12.8	2.3	15.0	18.0	20.6



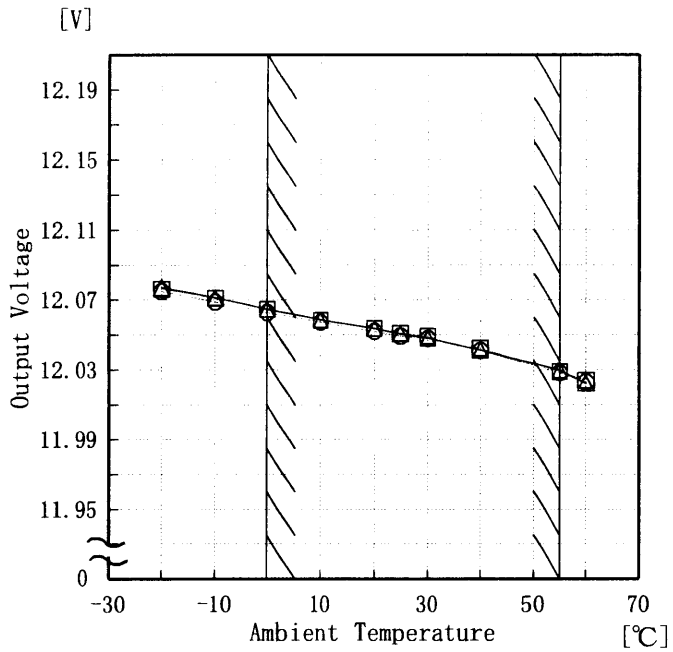
COSEL

Model	YS1512A
Item	Ambient Temperature Drift 周囲温度変動
Object	+12.0V1.30A

Testing Circuitry Figure A

1. Graph

△ Input Volt. 85V
□ Input Volt. 100V
○ Input Volt. 132V



Load 100%

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

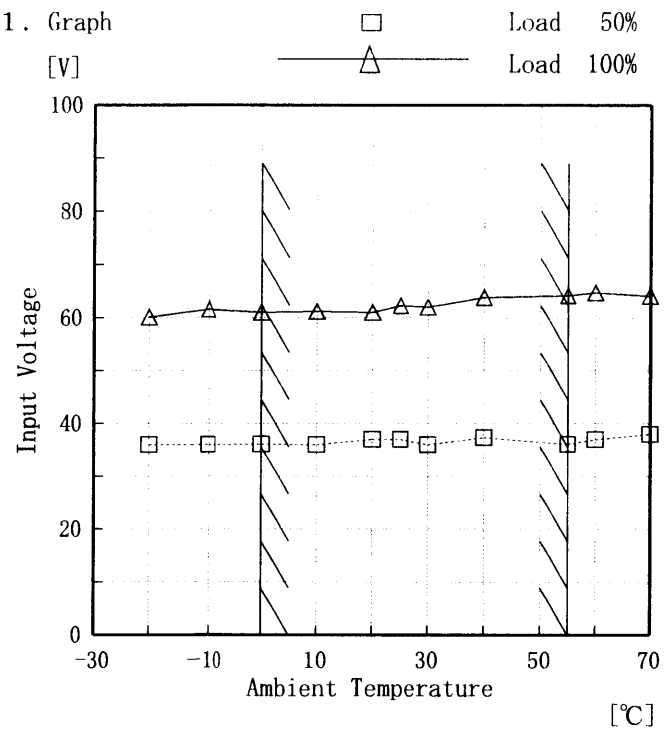
Temperature [℃]	Input Volt. 85[V] Output Volt. [V]	Input Volt. 100[V] Output Volt. [V]	Input Volt. 132[V] Output Volt. [V]
-20	12.077	12.076	12.075
-10	12.071	12.071	12.069
0	12.065	12.065	12.063
10	12.059	12.058	12.057
20	12.054	12.053	12.052
25	12.050	12.051	12.049
30	12.048	12.049	12.048
40	12.041	12.042	12.041
55	12.030	12.029	12.028
60	12.022	12.024	12.023
70	12.011	12.012	12.011

COSEL

Model	YS1512A
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+ 12.0V 1.30A

Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

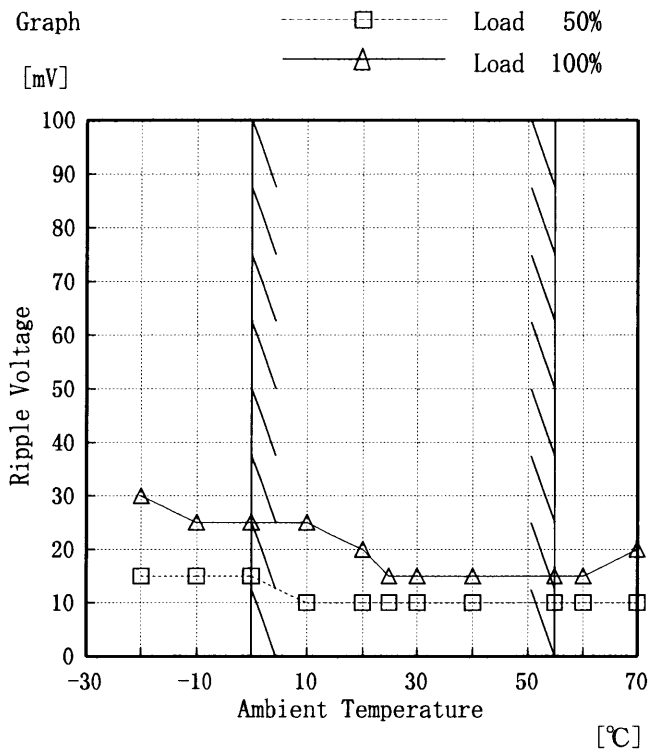
Ambient Temp. [℃]	Load 50% Input Volt. [V]	Load 100% Input Volt. [V]
-20	36	60
-10	36	62
0	36	61
10	36	61
20	37	61
25	37	62
30	36	62
40	37	64
55	36	64
60	37	65
70	38	64

COSEL

Model	YS1512A
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)
Object	+12.0V1.30A

Testing Circuitry Figure A

1. Graph



Input Volt. 100 V

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

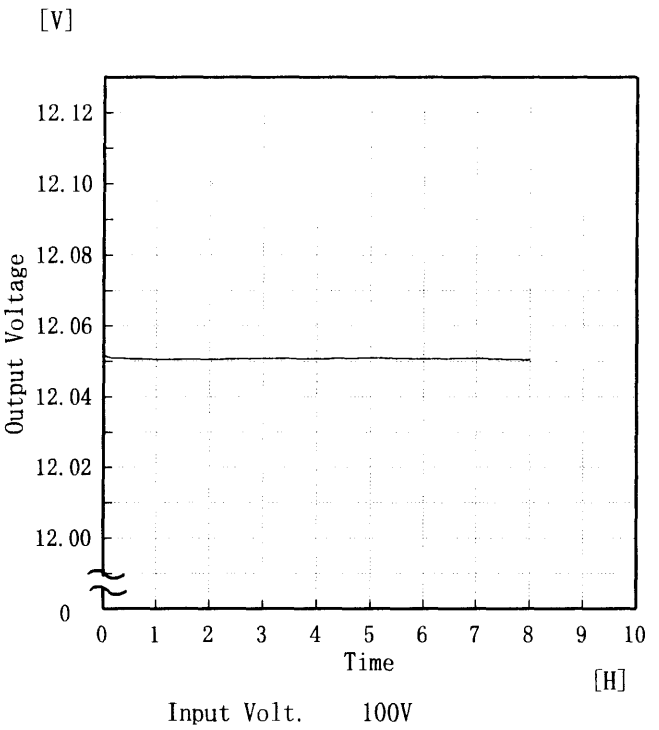
2. Values

Ambient Temp. [℃]	Load 50% Ripple Output Volt. [mV]	Load 100% Ripple Output Volt. [mV]
-20	15	30
-10	15	25
0	15	25
10	10	25
20	10	20
25	10	15
30	10	15
40	10	15
55	10	15
60	10	15
70	10	20

COSEL

Model	YS1512A	
Item	Time Lapse Drift 経時ドリフト	Temperature 25 ℃ Testing Circuitry Figure A
Object	+12.0V1.30A	

1. Graph



Input Volt. 100V

Load 100%

2. Values

Time since start [H]	Output Voltage [V]
0.0	12.056
0.5	12.051
1.0	12.050
2.0	12.051
3.0	12.051
4.0	12.051
5.0	12.051
6.0	12.051
7.0	12.051
8.0	12.050

COSEL

Model	YS1512A	
Item	Output Voltage Accuracy　定電圧精度	Testing Circuitry　Figure A
Object	+12.0V1.30A	

Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature　: 0～55 ℃
Input Voltage : 85～132 V
Load Current　: 0.00～1.30 A

* Output Voltage Accuracy = ±(Maximum of Output Voltage　－ Minimum of Output Voltage)／2

$$\text{* Output Voltage Accuracy (Ration)} = \frac{\text{Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

定電圧精度

周囲温度、入力電圧、負荷を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度　0～55 ℃
入力電圧　85～132 V
負荷電流　0.00～1.30 A

* 定電圧精度(変動値) ＝ ±(出力電圧の最高値－出力電圧の最低値)／2

$$\text{* 定電圧精度(変動率)} = \frac{\text{変動値}}{\text{定格出力電圧}} \times 100$$

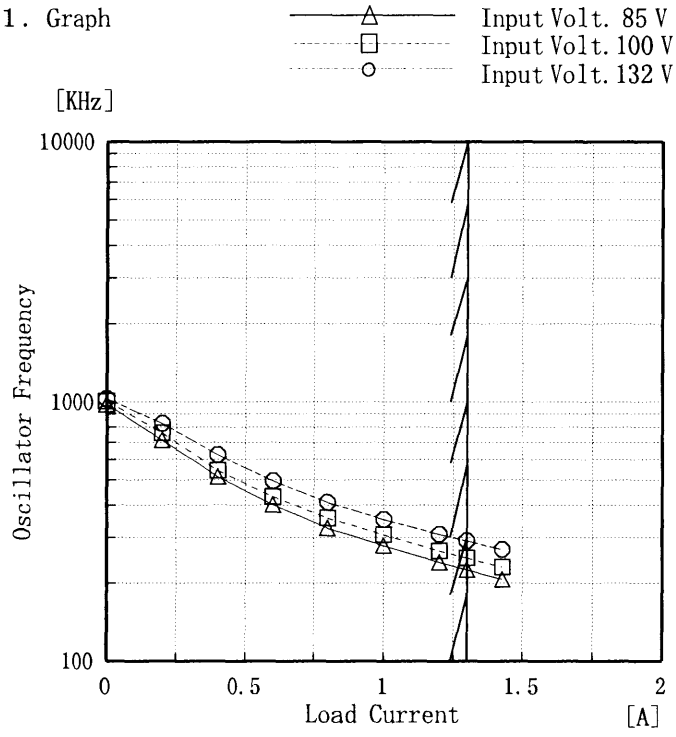
Item	Temperature [℃]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy(Ration) [%]
Maximum Voltage	0	132	0.00	12.072	±23	±0.2
Minimum Voltage	55	132	1.30	12.027		

COSEL

Model	YS1512A
Item	Oscillator Frequency 発振周波数
Object	+12.0V1.30A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note:Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
	Oscillator Frequency [KHz]		
0.00	980	1010	1030
0.20	714	761	827
0.40	516	545	625
0.60	402	433	496
0.80	327	358	411
1.00	280	308	352
1.20	241	267	309
1.30	225	250	291
1.43	207	231	270
—	—	—	—
—	—	—	—

BC−3202

COSEL

Model	YS1512A	
Item	Condensation 結露特性	Testing Circuitry Figure A
Object	+12.0V1.30A	

1. Condensation test

Testing procedure is as follows.

① Keeping and cooling the unit in a tank at -10℃ for an hour with the input off.

② Taking it out of the tank and dewing itself in a room where the temperature is 25℃ and the humidity is 40%RH.

③ Testing electrical characteristics of the unit to confirm there be no fault.

1．結露特性試験

入力を切った状態で、恒温槽で－１０℃に冷却しておき、約１時間後に恒温槽から取り出し、室温２５℃、湿度４０%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

2．Values

Item	Data	Testing Conditions
Output Voltage [V]	12.052	Input Volt.: 100V, Load Current:1.30A
Line Regulation [mV]	1	Input Volt.: 85～132V, Load Current:1.30A
Load Regulation [mV]	6	Input Volt.: 100V, Load Current:0.0～1.30A

COSEL

Model	YS1512A		
Item	Leakage Current 漏洩電流	Temperature Testing Circuitry	25℃ Figure B
Object	————————		

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DENTORI	0.11	0.13	0.18
(B) IEC60950	0.11	0.13	0.17

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B) IEC60950	—	—	—

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力の両相について測定し、その大きい方を漏洩電流測定値とする。

COSEL

Model	YS1512A		
Item	Line Noise Tolerance 入力雑音耐量	Temperature Testing Circuitry	25℃ Figure C
Object	+12.0V1.30A		

1. Results

Pulse Width [nS]	MODE	No protection failure should occur 保護回路の誤動作がない	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation
1000	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation

Conditions

Input Voltage :100 V
Pulse Voltage :2000 V
Pulse Cycle :10 mS
Pulse Input Duration:1 min. or more
Load :100 %

COSEL

Model	YS1512A	
Item	Conducted Emission 雑音端子電圧	Testing Circuitry Figure D
Object	————————	

1. Graph

Remarks

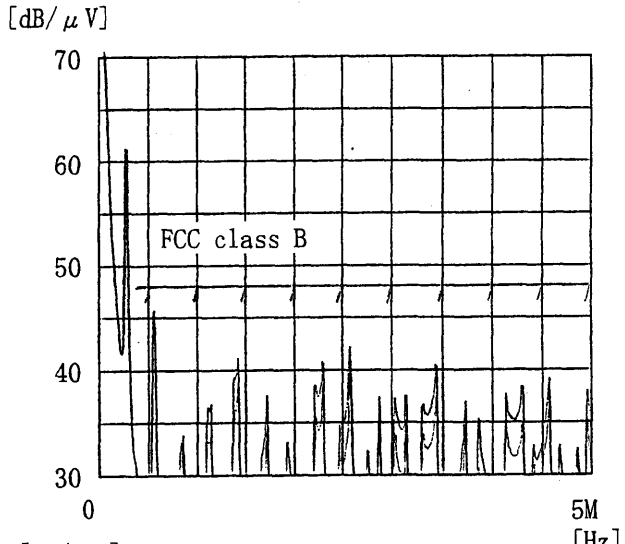
Input Volt. 120 V

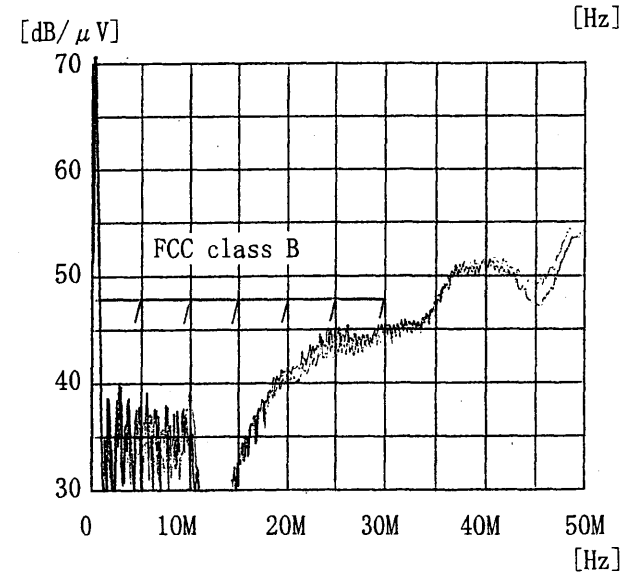
Load 100 %

Note: Slanted line shows the range of Tolerance.

(注)斜線は許容値を示す。

NO	Standards	Standards Complied	Frequency [MHz]	Tolerance [dB/μV]
1	FCC class A		0.45～1.6	60
			1.6～30	69.5
2	FCC class B	○	0.45～30	48
3	VCCI class A		0.15～0.5	79
			0.5～30	73
4	VCCI class B		0.15～0.5	66-56
			0.5～5	56
			5～30	60
5	CISPR Pub.22 class A (EN55022)		0.15～0.5	79
			0.5～30	73
6	CISPR Pub.22 class B (EN55022)		0.15～0.5	66-56
			0.5～5	56
			5～30	60





COSEL

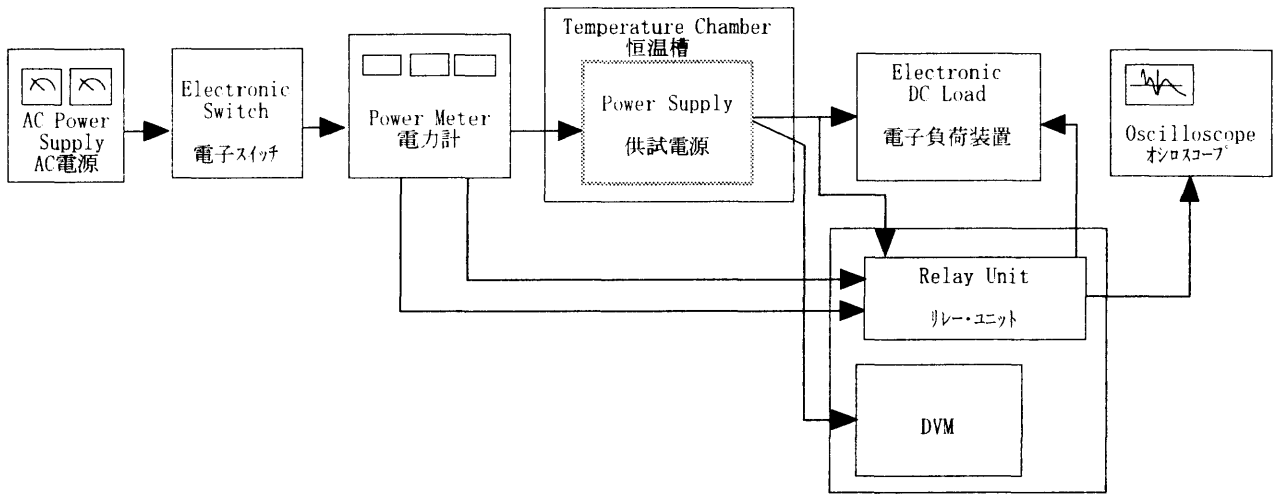


Figure A

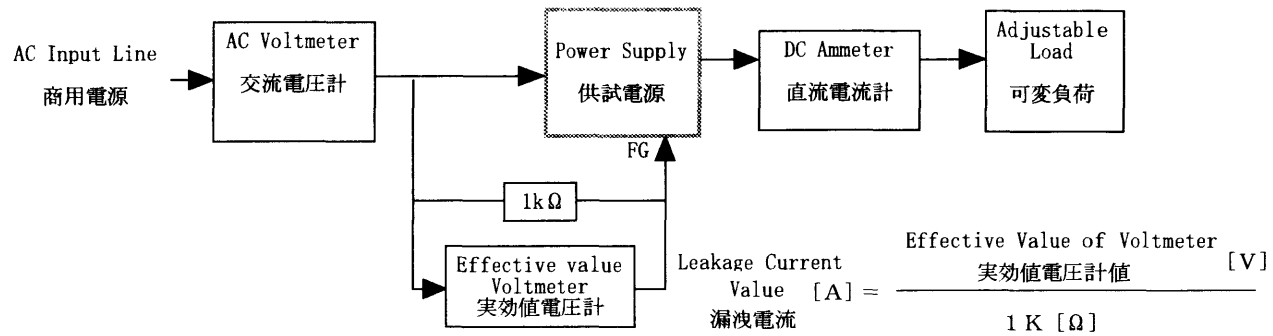


Figure B(DENTORI)

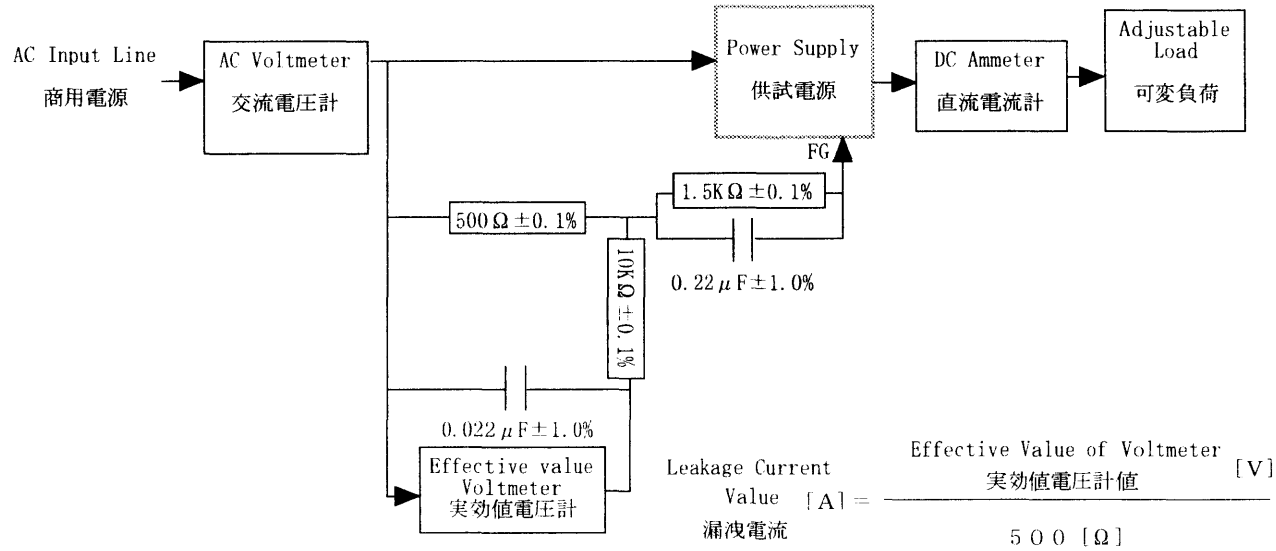


Figure B (IEC60950)

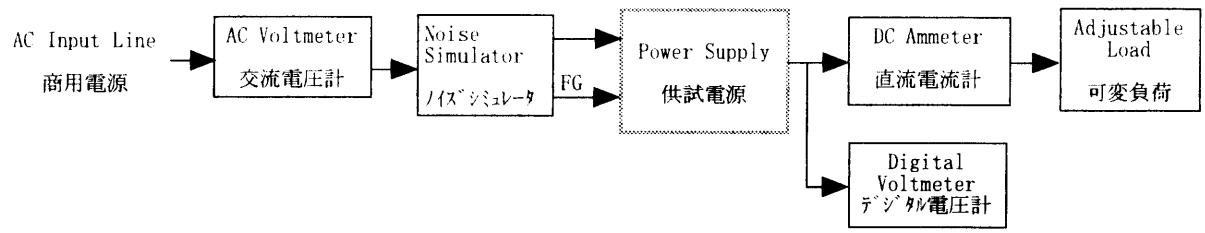


Figure C

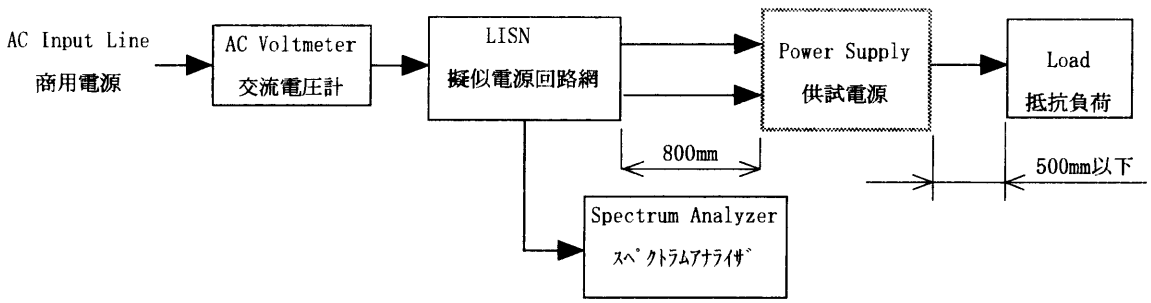


Figure D

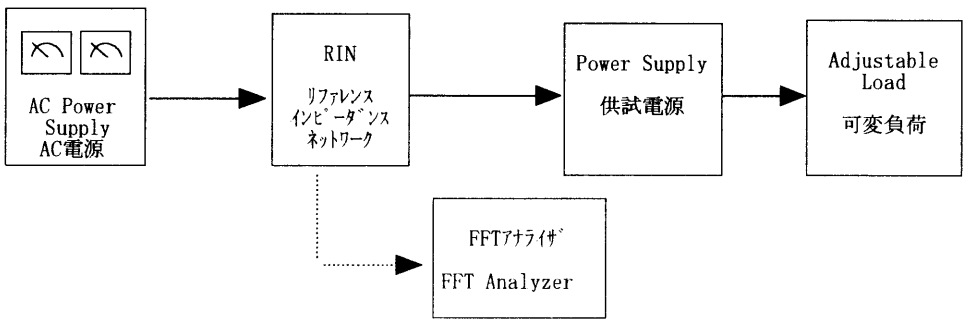


Figure E