

TEST DATA OF WDA60F-12

Regulated DC Power Supply
August 17, 2022

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Design Manager

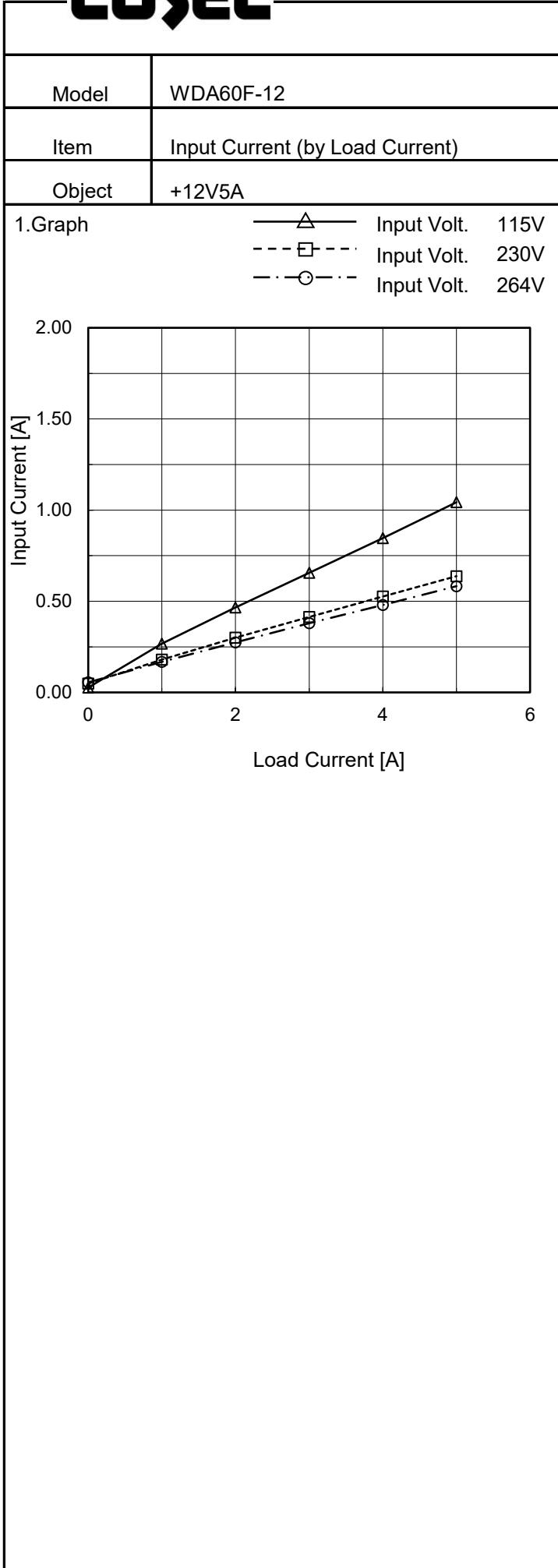
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COSEL CO.,LTD.

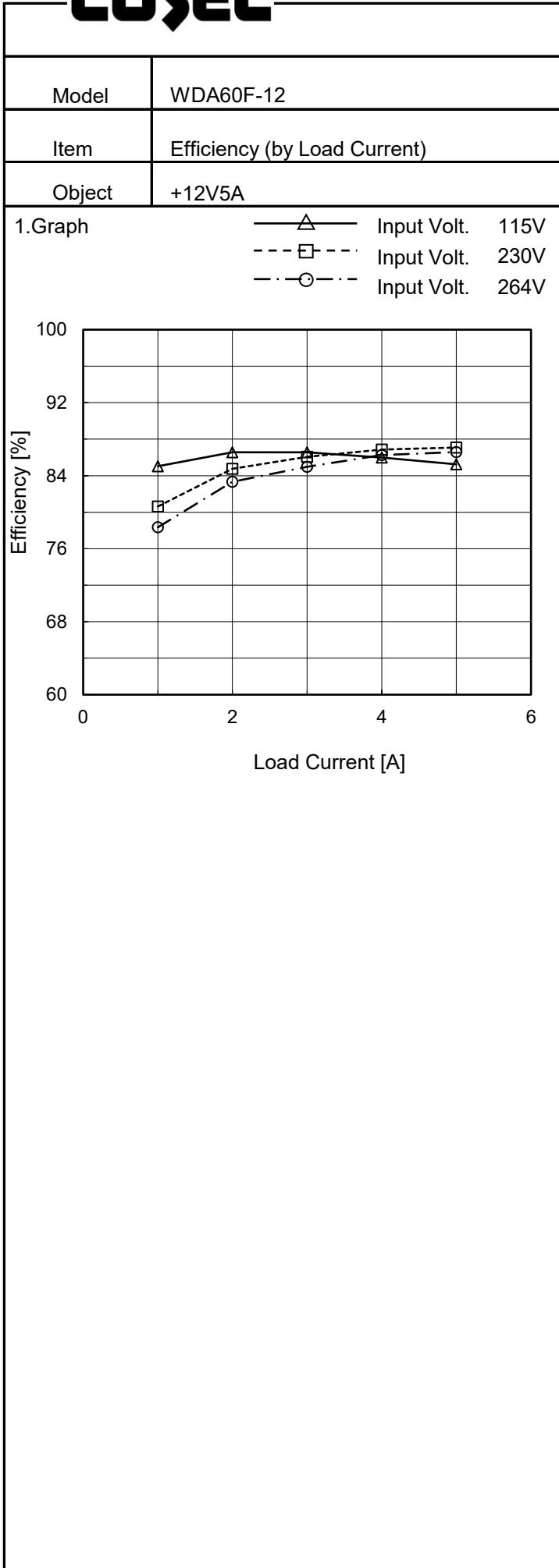
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Temperature 25°C
Testing Circuitry Figure A

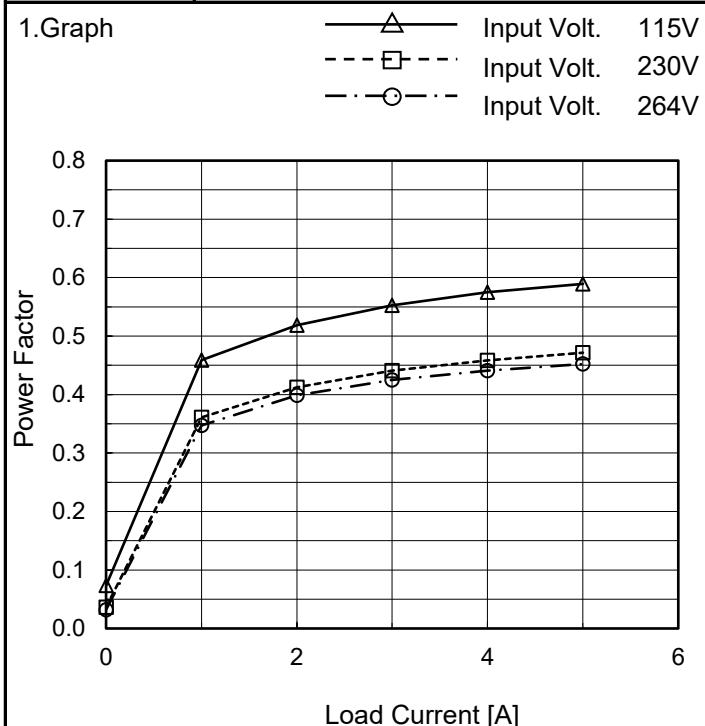


Temperature 25°C
Testing Circuitry Figure A

2.Values

Load Current [A]	Efficiency [%]		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
0	-	-	-
1	85.0	80.6	78.3
2	86.6	84.8	83.3
3	86.6	86.1	85.0
4	86.0	86.9	86.3
5	85.2	87.1	86.6
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

Model	WDA60F-12
Item	Power Factor (by Load Current)
Object	+12V5A



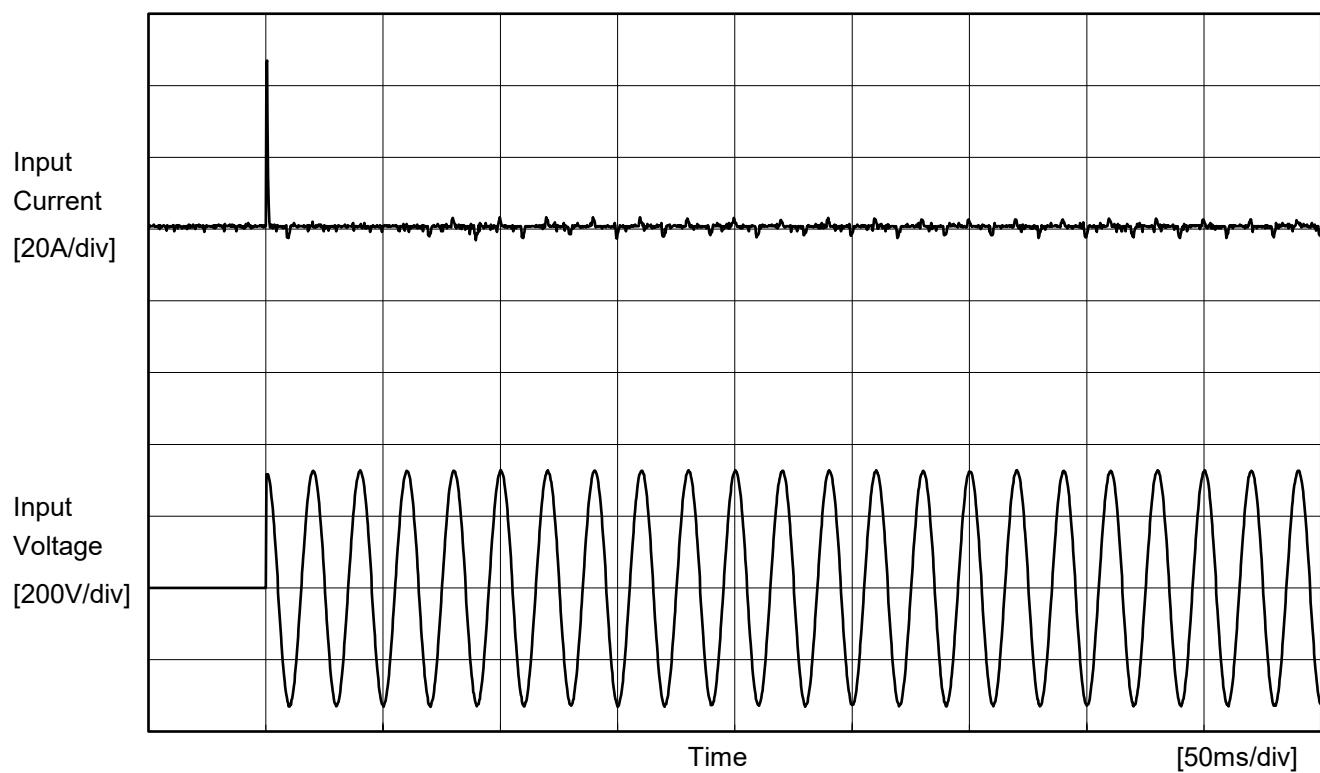
Temperature 25°C
Testing Circuitry Figure A

2.Values

Load Current [A]	Power Factor		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
0	0.073	0.036	0.032
1	0.459	0.361	0.347
2	0.518	0.413	0.399
3	0.553	0.441	0.425
4	0.575	0.459	0.441
5	0.589	0.472	0.452
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

Model	WDA60F-12
Item	Inrush Current
Object	+12V5A

Temperature 25°C
Testing Circuitry Figure A



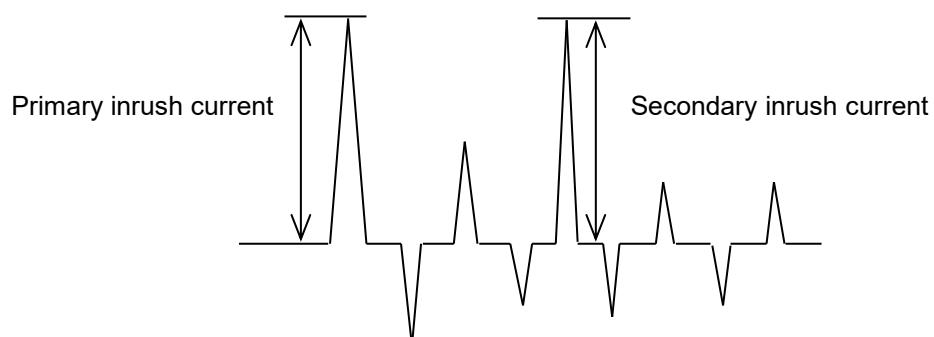
Input Voltage 230 V

Frequency 50 Hz

Load 100 %

Primary inrush current 46.6 A

Secondary inrush current 0.0 A



Model	WDA60F-12	Temperature Testing Circuitry Figure C
Item	Leakage Current	
Object	+12V5A	

1. Results

[mA]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			115 [V]	240 [V]	264 [V]	
DEN-AN	Figure C-1	Both phases	0.14	0.32	0.35	Operation
		One of phases	0.26	0.58	0.64	Stand by
IEC62368-1	Figure C-2	Both phases	0.14	0.30	0.33	Operation
		One of phases	0.25	0.58	0.60	Stand by
	Figure C-3	Both phases	0.13	0.29	0.33	Operation
		One of phases	0.24	0.54	0.60	Stand by

The value for "One of phases" is the reference value only.

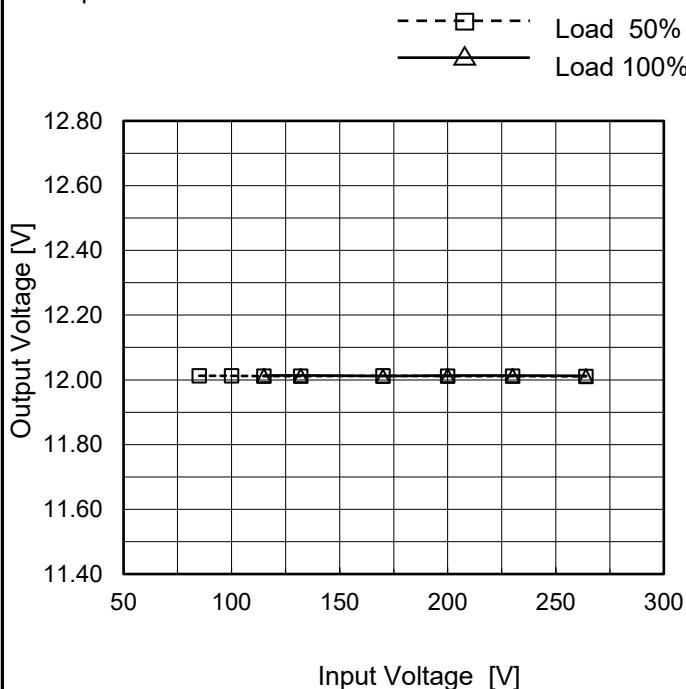
2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

Model	WDA60F-12
Item	Line Regulation
Object	+12V5A

Temperature 25°C
Testing Circuitry Figure A

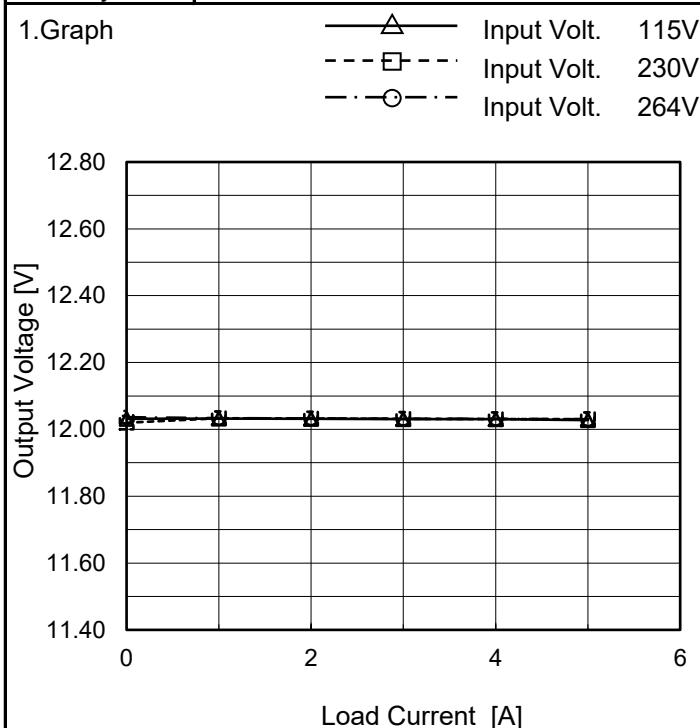
1.Graph



2.Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
85	12.013	-
100	12.012	-
115	12.012	12.014
132	12.012	12.014
170	12.012	12.012
200	12.012	12.014
230	12.011	12.013
264	12.011	12.013
--	-	-

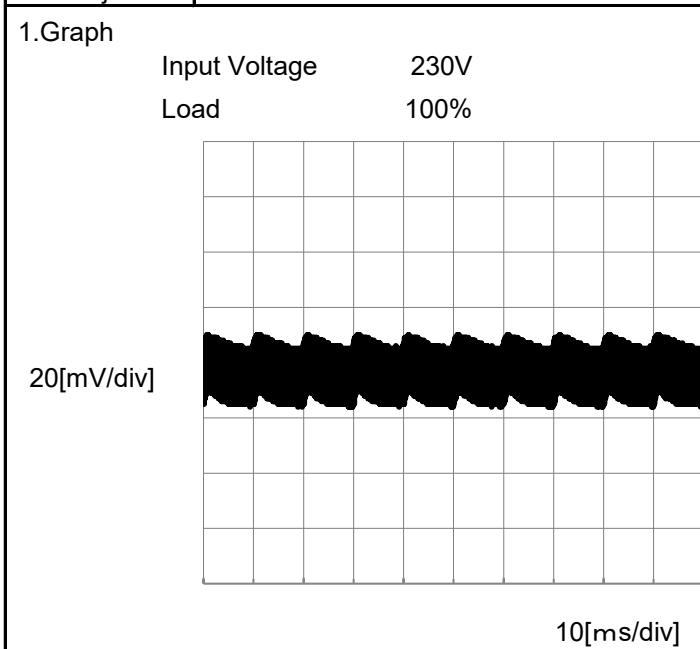
Model	WDA60F-12
Item	Load Regulation
Object	+12V5A


 Temperature 25°C
 Testing Circuitry Figure A

2.Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
0	12.032	12.020	12.037
1	12.032	12.033	12.034
2	12.032	12.033	12.033
3	12.031	12.032	12.032
4	12.030	12.031	12.032
5	12.028	12.031	12.030
--	--	--	--
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Item	Ripple-Noise
Object	+12V5A

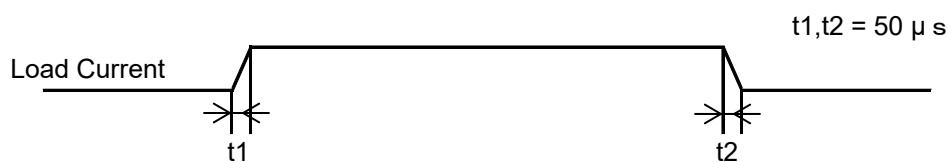
 Temperature 25°C
 Testing Circuitry Figure B


Model	WDA60F-12
Item	Dynamic Load Response
Object	+12V5A

Temperature 25°C
Testing Circuitry Figure A

Input Volt. 230 V

Cycle 1000 ms

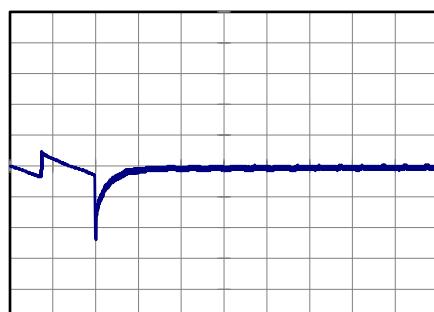


Min.Load (0A)↔
Load 100% (5A)

100 mV/div

20 ms/div

20 ms/div

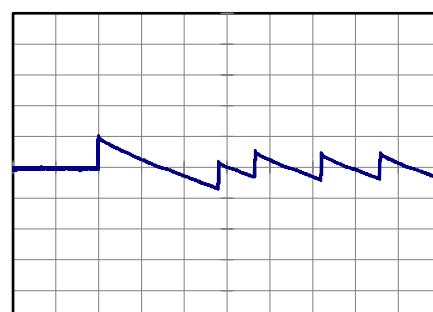
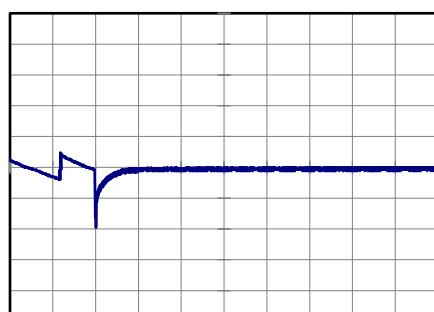


Min.Load (0A)↔
Load 50% (2.5A)

100 mV/div

20 ms/div

20 ms/div

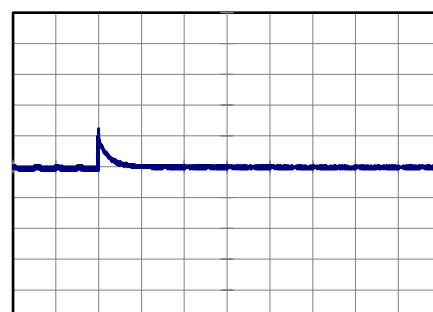
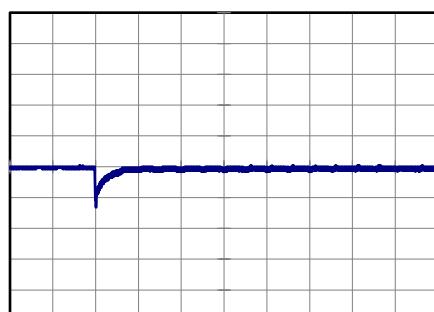


Load 50% (2.5A)↔
Load 100% (5A)

100 mV/div

20 ms/div

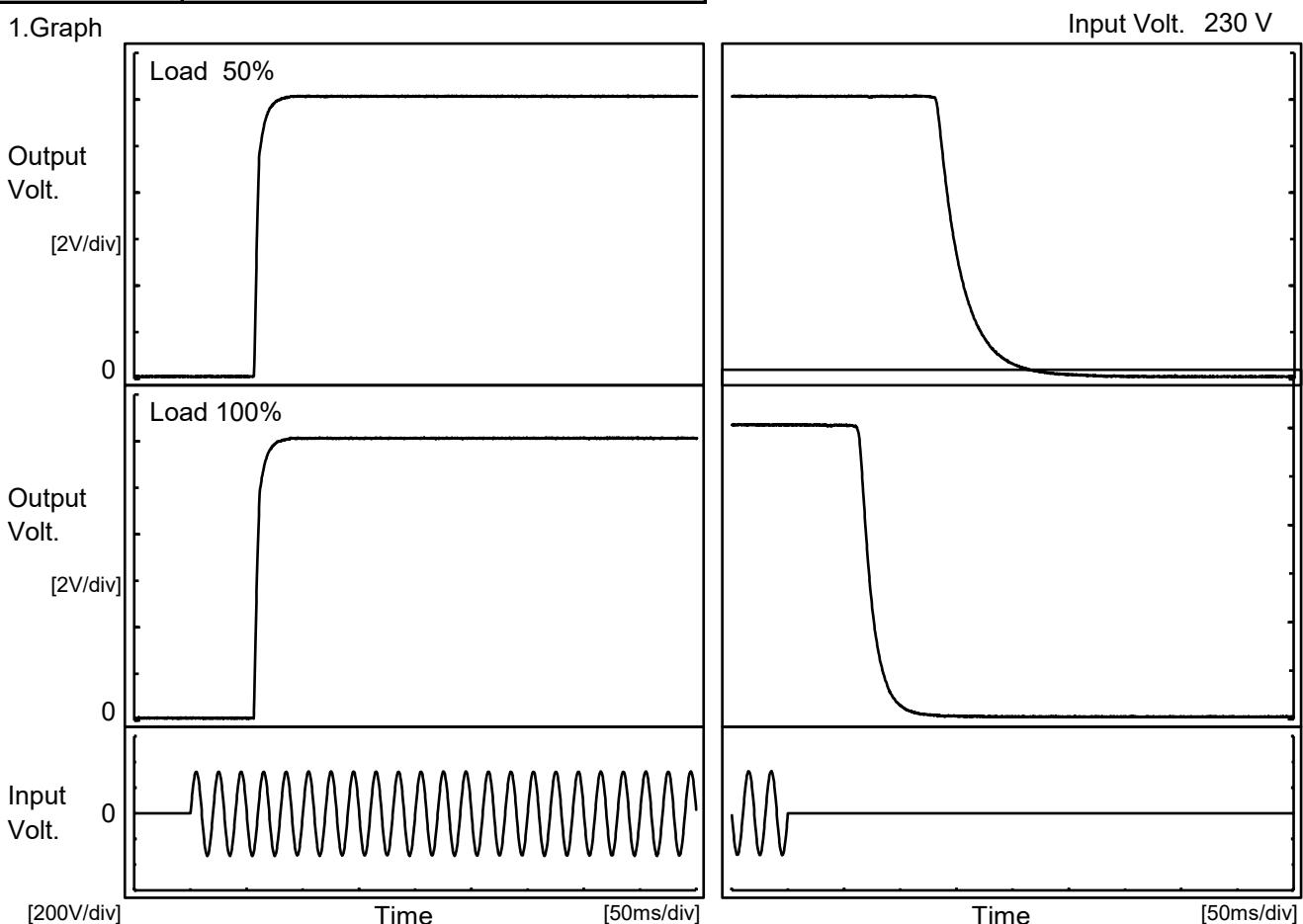
20 ms/div



Model	WDA60F-12
Item	Rise and Fall Time
Object	+12V5A

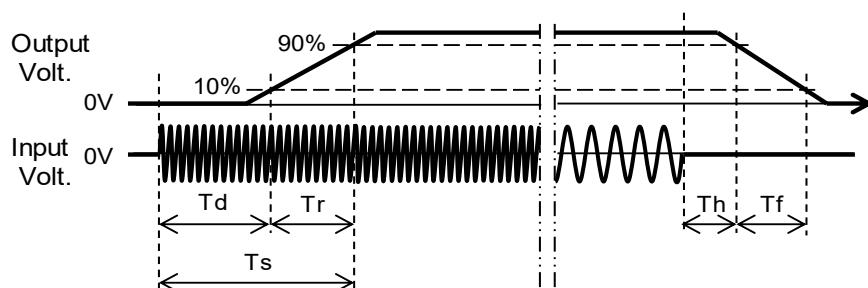
Temperature 25°C
Testing Circuitry Figure A

1. Graph



2. Values

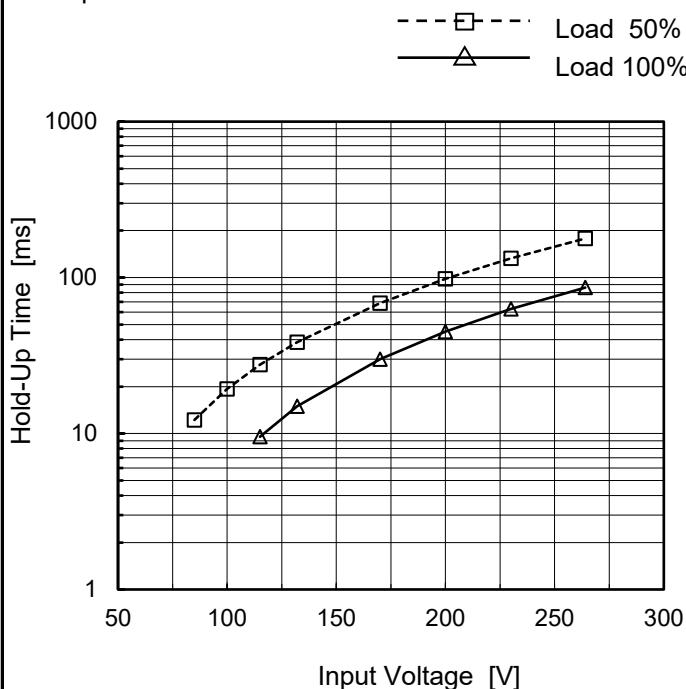
Load	Time	Td	Tr	Ts	Th	Tf	[ms]
50 %		57.3	8.0	65.3	134.5	47.0	
100 %		57.0	8.3	65.3	64.8	24.3	



Model	WDA60F-12
Item	Hold-Up Time
Object	+12V5A

Temperature 25°C
Testing Circuitry Figure A

1. Graph

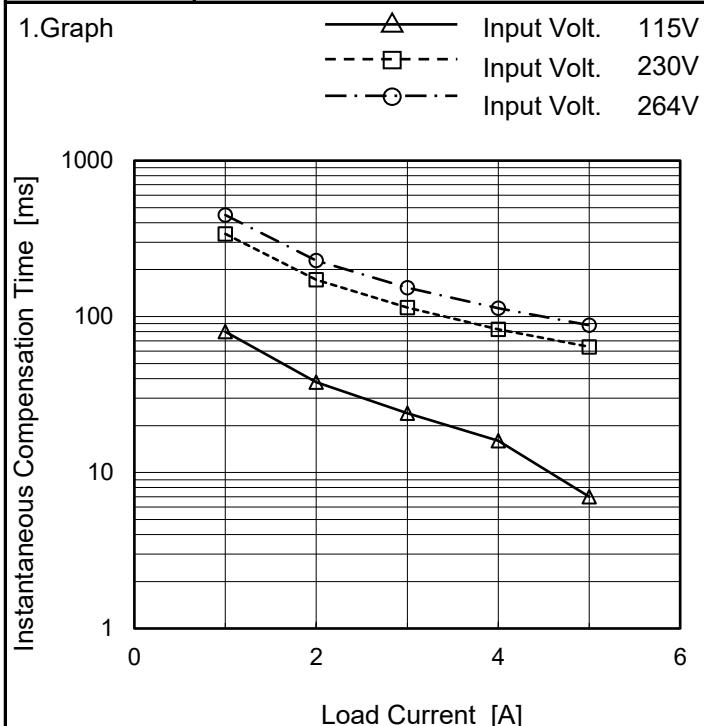


2. Values

Input Voltage [V]	Hold-Up Time [ms]	
	Load 50%	Load 100%
85	12	-
100	19	-
115	28	10
132	39	15
170	69	30
200	98	45
230	133	63
264	178	86
--	-	-

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

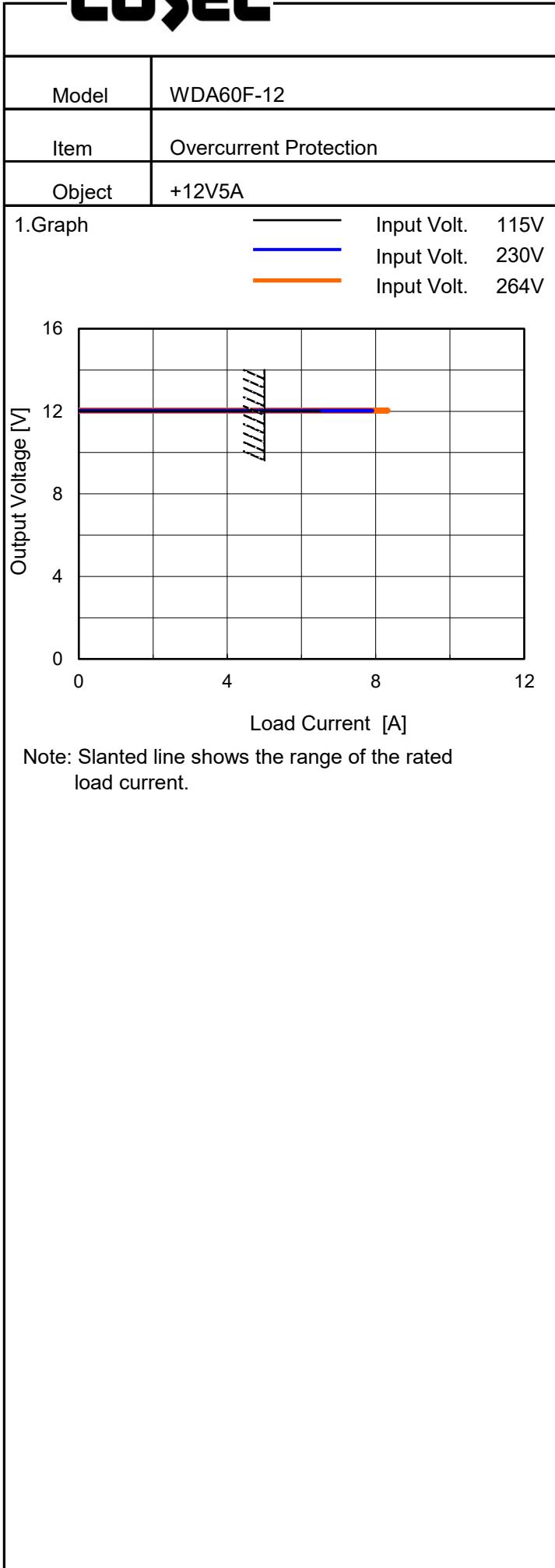
Model	WDA60F-12
Item	Instantaneous Interruption Compensation
Object	+12V5A



Temperature 25°C
Testing Circuitry Figure A

2.Values

Load Current [A]	Time [ms]		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
0	-	-	-
1	80	339	448
2	38	172	229
3	24	114	153
4	16	83	113
5	7	64	88
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-



Temperature 25°C
Testing Circuitry Figure A

2. Values

Output Voltage [V]	Load Current [A]		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
12	6.50	7.90	8.31
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

Model	WDA60F-12	Testing Circuitry Figure A
Item	Ambient Temperature Drift	
Object	+12V5A	

1.Values

Load 100%

Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 115V	Input Volt. 230V	Input Volt. 264V
-20	11.970	11.973	11.973
25	12.035	12.038	12.037
40	12.053	12.053	12.051

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+12V5A	

1.Values

Ambient Temperature[°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	45	81
25	45	79
40	45	79

Item	Overvoltage Protection	Testing Circuitry Figure A
Object	+12V5A	

1.Values

Load 0%

Ambient Temperature[°C]	Operating Point [V]	
	Input Volt. 115V	Input Volt. 264V
-20	15.89	15.89
25	16.26	16.19
40	16.33	16.33

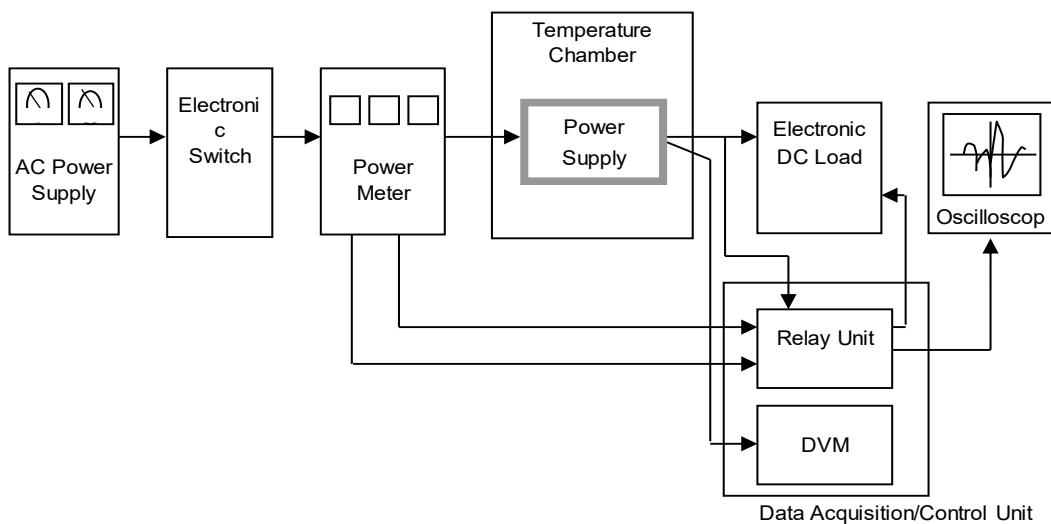
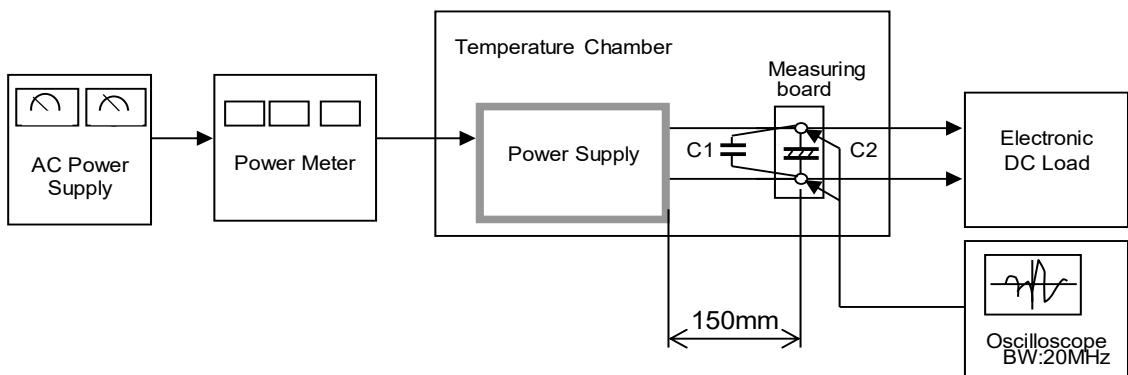


Figure A


 $C_1 = 0.1 \mu F$
 (Ceramic capacitor)

 $C_2 = 47 \mu F$
 (Electrolytic capacitor)

Figure B

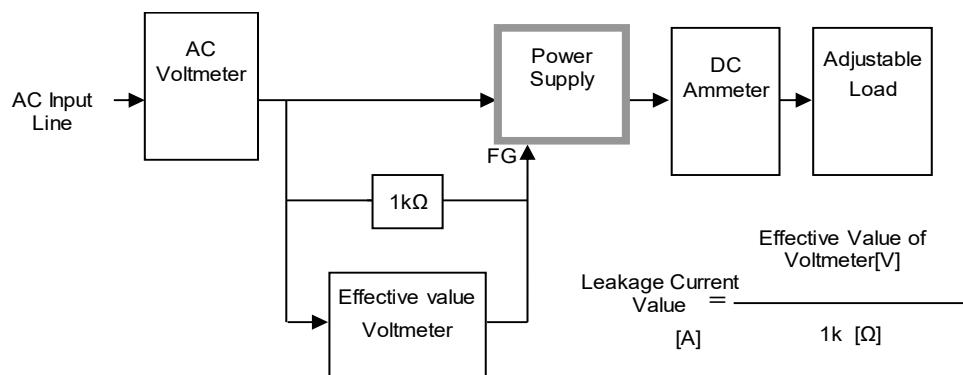


Figure C-1 (DEN-AN)

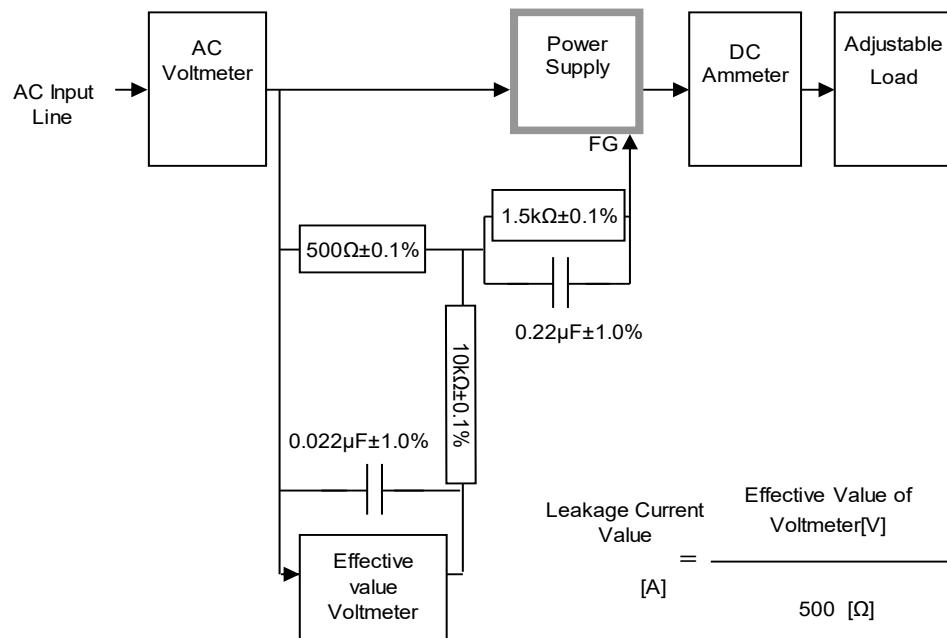


Figure C-2 (IEC62368-1 refer to IEC60990 Fig.4)

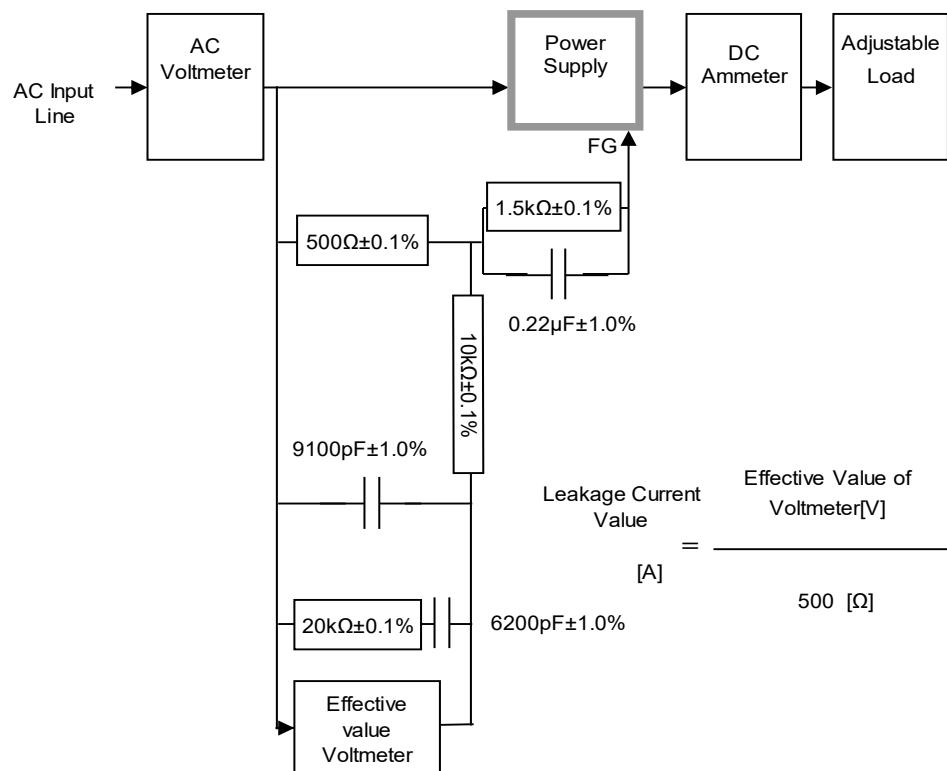


Figure C-3 (IEC62368-1 refer to IEC60990 Fig.5)