

TEST DATA OF TUNS1200F12

Regulated DC Power Supply
July 21, 2020

Approved by : Junichi Hatagishi
Junichi Hatagishi Design Manager

Prepared by : Shunsuke Sawai
Shunsuke Sawai Design Engineer

COSEL CO.,LTD.

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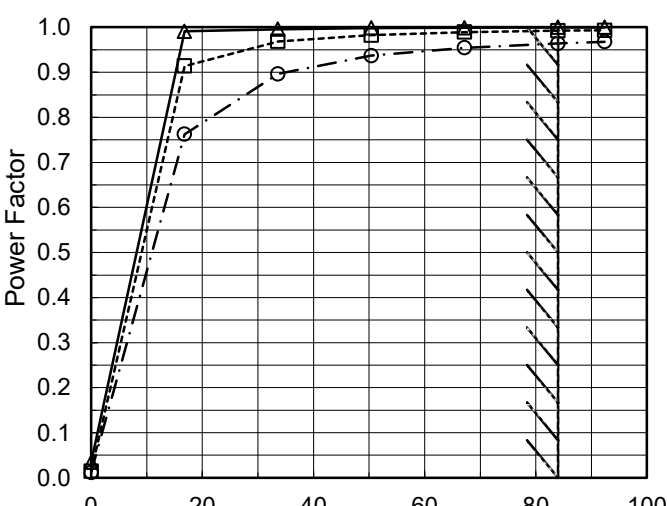
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Model		TUNS1200F12		Temperature 25°C																																																				
Item		Input Current (by Load Current)		Testing Circuitry Figure A																																																				
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1.Graph		<div><div><div>—△—</div><div>Input Volt.</div><div>100V</div></div><div><div>---□---</div><div>Input Volt.</div><div>200V</div></div><div><div>-·-○-·-</div><div>Input Volt.</div><div>277V</div></div></div> <div><p>Note: Slanted line shows the range of the rated load current.</p></div>		2.Values																																																				
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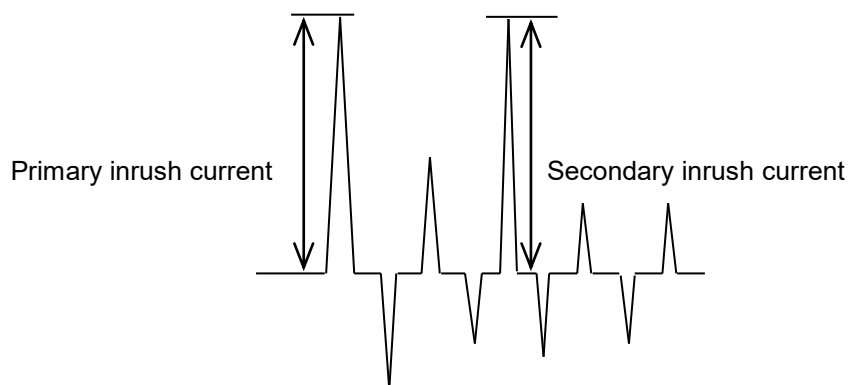
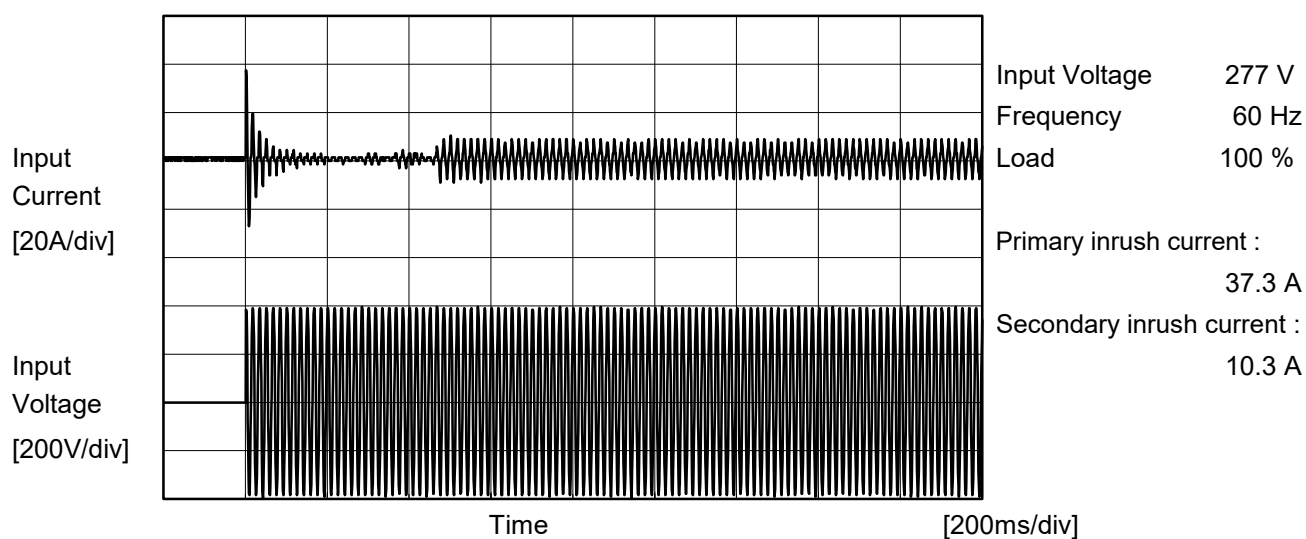
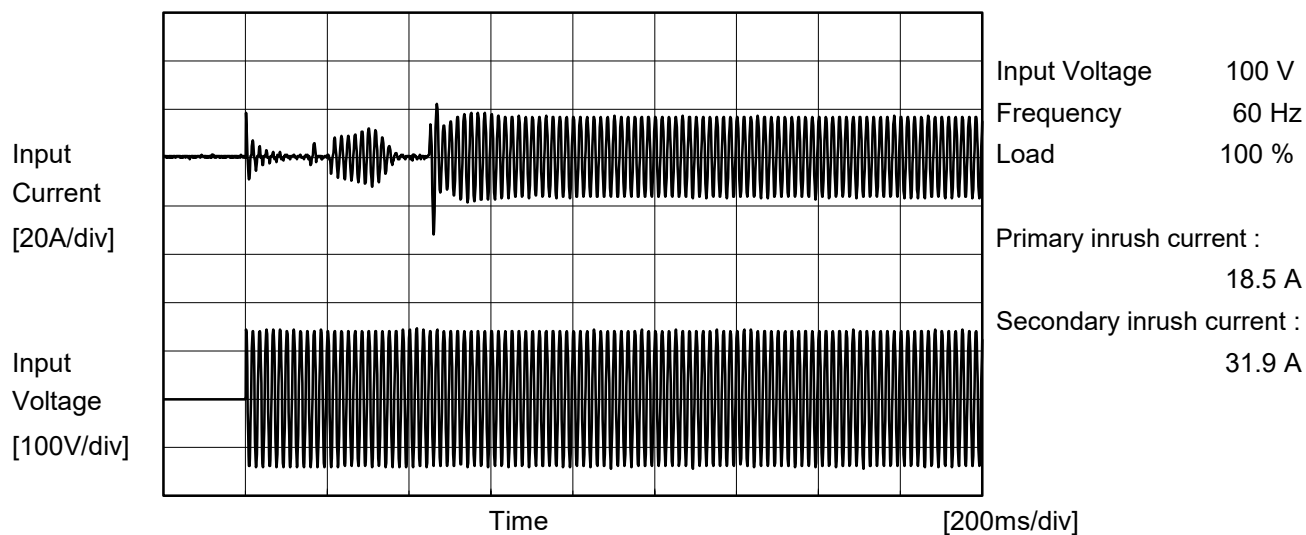


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COSEL

Model	TUNS1200F12	Temperature	25°C
Item	Inrush Current	Testing Circuitry	Figure A
Object	_____		





COSEL		Temperature 25°C Testing Circuitry Figure B
Model	TUNS1200F12	
Item	Leakage Current	
Object	_____	

1.Results

[mA]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	200 [V]	240 [V]	
IEC60601-1	Figure B	Both phases	0.16	0.35	0.44	Operation
		One of phases	0.30	0.62	0.77	Stand by

The value for "One of phases" is the reference value only.

2.Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

Model		TUNS1200F12	
Item		Line Regulation	
Object		+12V84A	
1.Graph		2.Values	

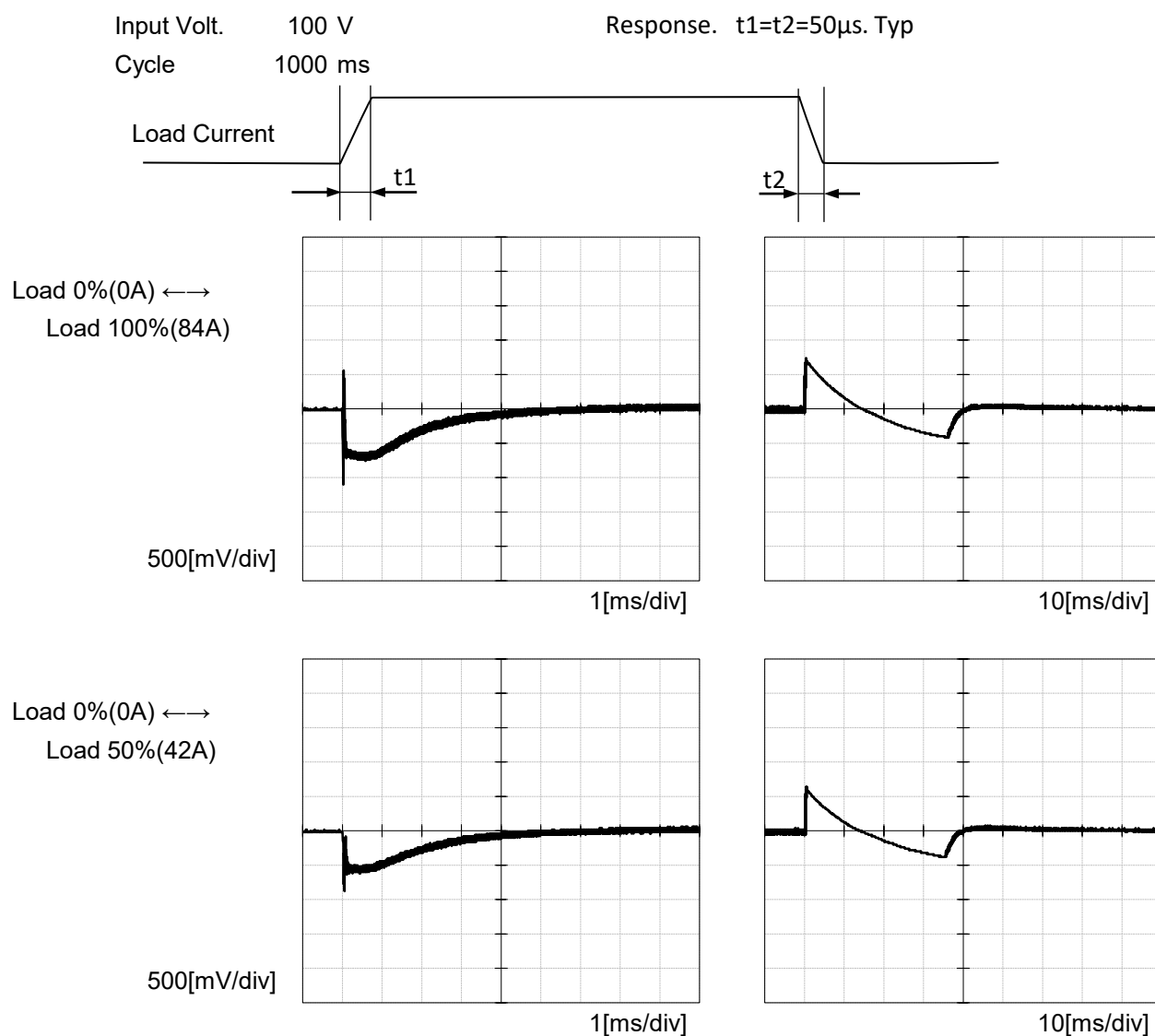
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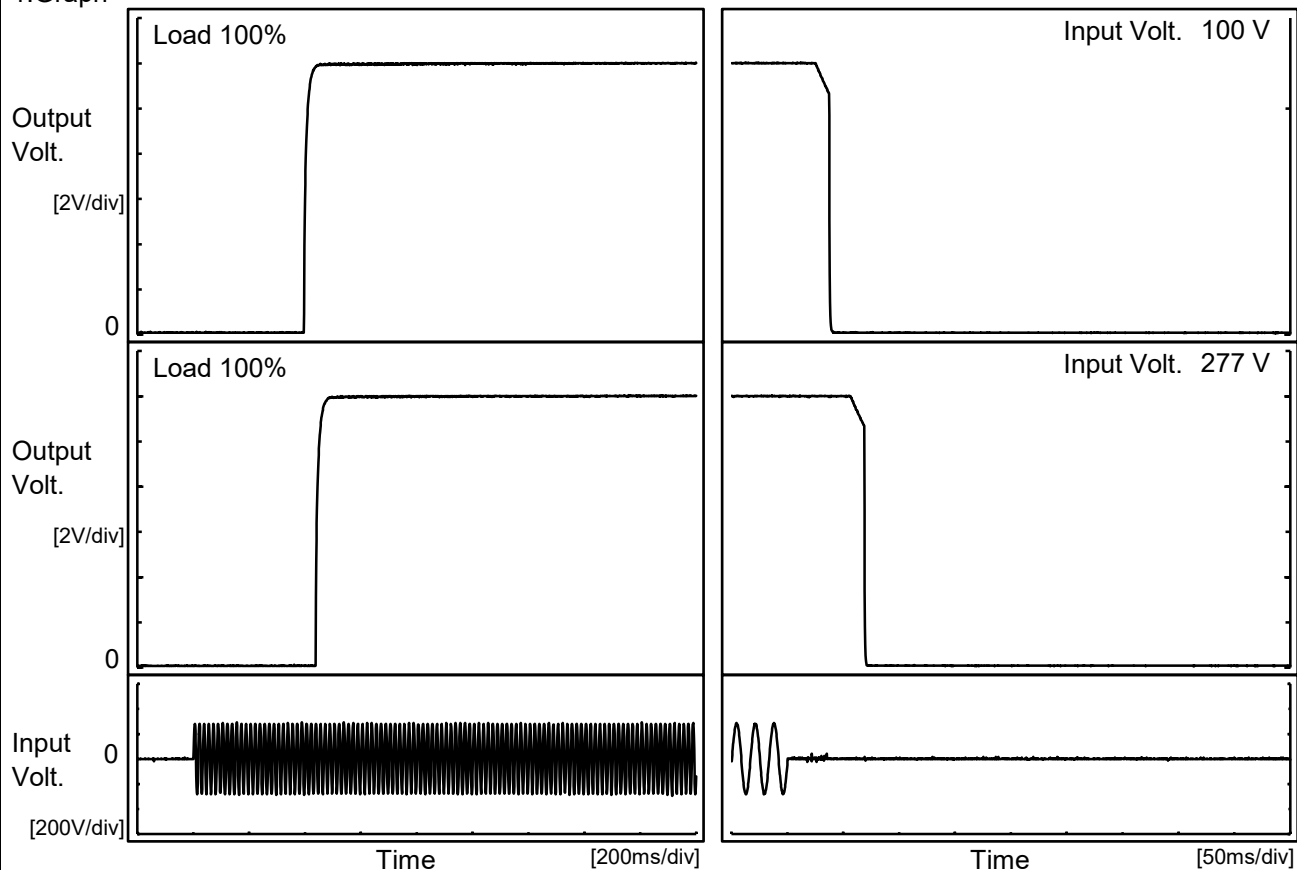
Model	TUNS1200F12	Temperature 25°C Testing Circuitry Figure A
Item	Dynamic Load Response	
Object	+12V84A	





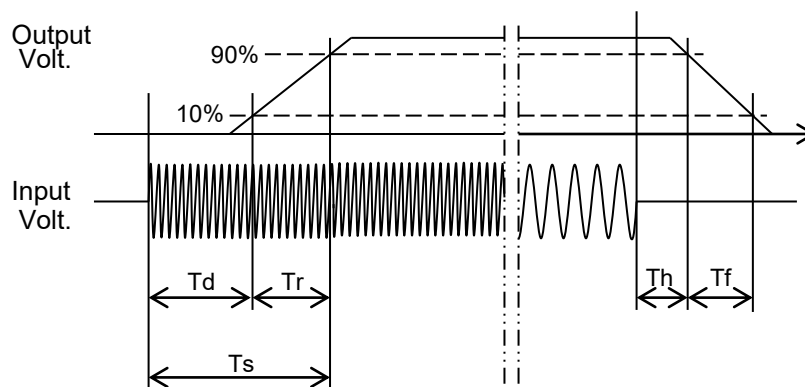
Model	TUNS1200F12	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+12V84A		

1.Graph



2.Values

Input Volt.	Time	Td	Tr	Ts	Th	Tf
100 V		397.0	20.0	417.0	35.3	3.3
277 V		437.0	21.0	458.0	66.8	3.0



Model		TUNS1200F12	
Item		Hold-Up Time	
Object		+12V84A	
1.Graph		2.Values	

1000

100

10

1

50

100

150

200

250

300

Hold-Up Time [ms]

Input Voltage [V]

□

Load 50%

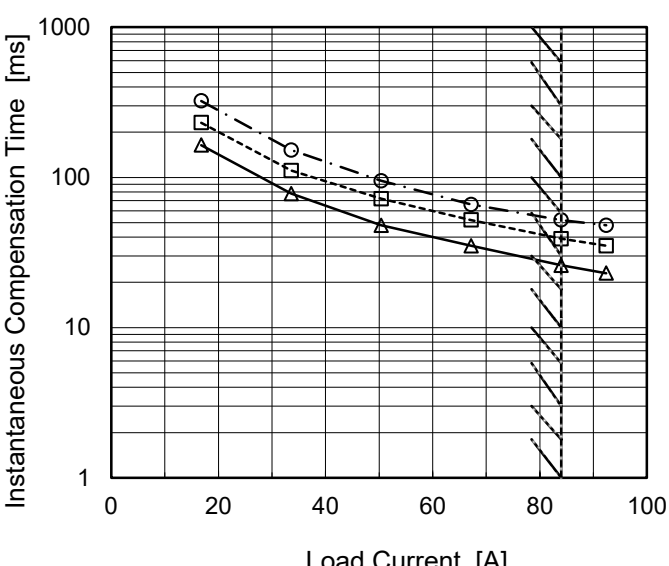
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Load 100%

Input Voltage [V]	Hold-Up Time [ms]	
	Load 50%	Load 100%
80	61	26
85	61	26
100	61	26
120	61	26
200	88	40
230	88	40
277	124	58
283	124	57
305	124	57

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated input voltage.

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Item		Instantaneous Interruption Compensation		Testing Circuitry Figure A																																																				
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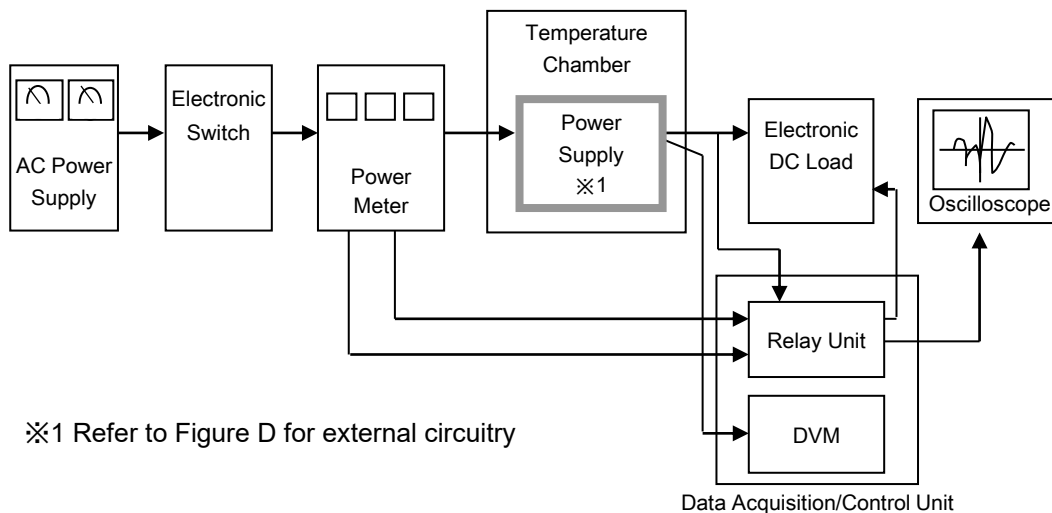


Figure A

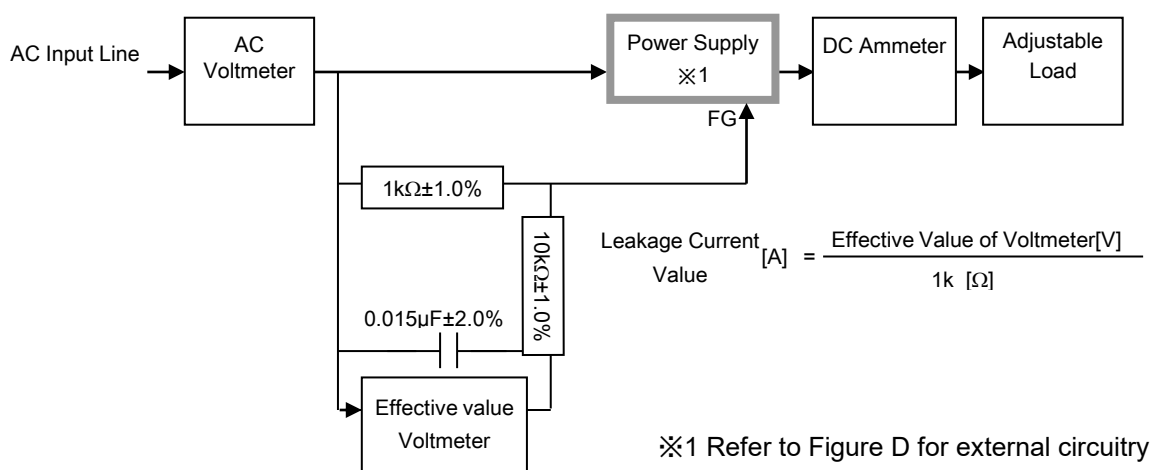


Figure B (IEC60601-1)

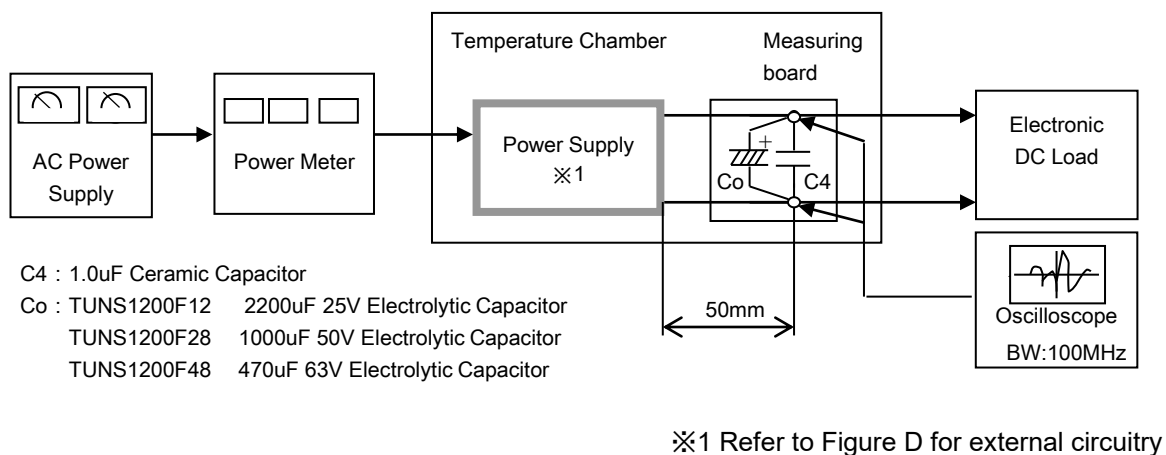
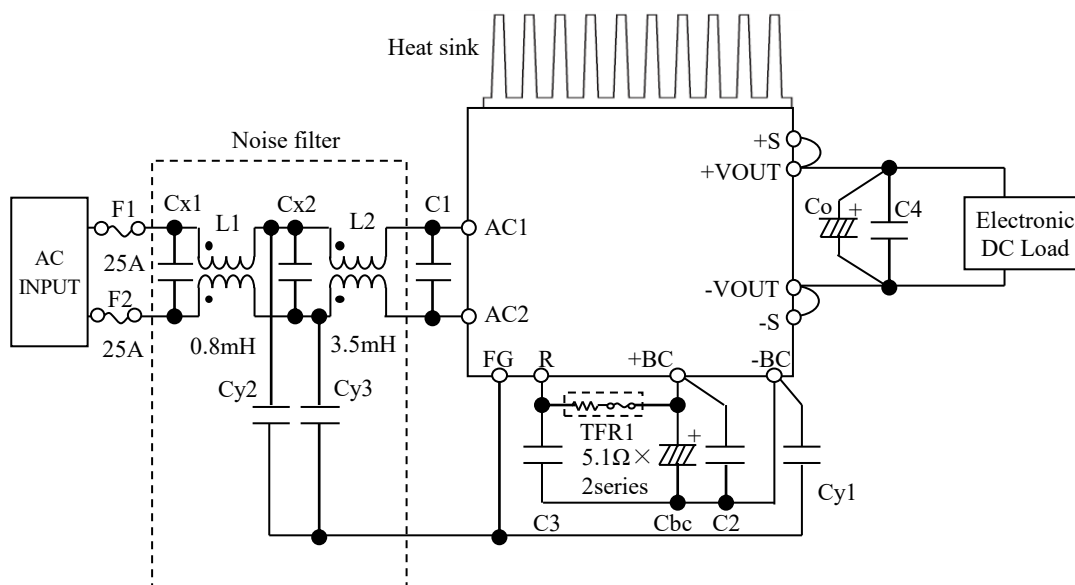


Figure C



L1	: SCR25-200-1R7A008JH	
L2	: SC15-E350H	
Cx1,Cx2	: 1.5uF 310V Film Capacitor	
Cy1	: 2200pF 400V	
Cy2,Cy3	: 1500pF 400V	
C1	: 1.5uF 310V Film Capacitor × 2parallel	
C2,C3	: 1.0uF 630V Film Capacitor × 2parallel	
C4	: 1.0uF Ceramic Capacitor	
Cbc	: 470uF 450V Electrolytic Capacitor × 3parallel	(0 ≤ Ta ≤ 85°C)
	: 470uF 450V Electrolytic Capacitor × 6parallel	(-40 ≤ Ta < 0°C)
Co	: TUNS1200F12	2200uF 25V Electrolytic Capacitor (0 ≤ Ta ≤ 85°C)
		2200uF 25V Electrolytic Capacitor × 3parallel (-40 ≤ Ta < 0°C)
	: TUNS1200F28	1000uF 50V Electrolytic Capacitor (0 ≤ Ta ≤ 85°C)
		1000uF 50V Electrolytic Capacitor × 3parallel (-40 ≤ Ta < 0°C)
	: TUNS1200F48	470uF 63V Electrolytic Capacitor (0 ≤ Ta ≤ 85°C)
		470uF 63V Electrolytic Capacitor × 3parallel (-40 ≤ Ta < 0°C)

Ta : Ambient Temp.

Figure D