



TEST DATA OF TUNS1200F12

Regulated DC Power Supply

July 21, 2020

Approved by :

Junichi Hatagishi
Junichi Hatagishi Design Manager

Prepared by :

Shunsuke Sawai
Shunsuke Sawai Design Engineer

COSEL CO.,LTD.



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Model	TUNS1200F12																																																					
Item	Input Current (by Load Current)																																																					
Object	_____																																																					
1.Graph	—△— Input Volt. 100V - -□--- Input Volt. 200V - ·○--- Input Volt. 277V																																																					
<p>The graph shows the relationship between Input Current [A] on the Y-axis (0.0 to 20.0) and Load Current [A] on the X-axis (0 to 100). Three curves are plotted for different input voltages: 100V (solid triangles), 200V (dashed squares), and 277V (dotted circles). All curves show a positive linear relationship. A slanted line is drawn across the graph, starting from approximately (0, 0.5) and ending at (85, 13.5), representing the rated load current range.</p> <table border="1"> <thead> <tr> <th>Load Current [A]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 277[V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>0.261</td><td>0.454</td><td>0.610</td></tr> <tr><td>16.8</td><td>2.419</td><td>1.290</td><td>1.117</td></tr> <tr><td>33.6</td><td>4.720</td><td>2.381</td><td>1.849</td></tr> <tr><td>50.4</td><td>7.060</td><td>3.515</td><td>2.645</td></tr> <tr><td>67.2</td><td>9.450</td><td>4.680</td><td>3.478</td></tr> <tr><td>84.0</td><td>11.910</td><td>5.860</td><td>4.320</td></tr> <tr><td>92.4</td><td>13.150</td><td>6.460</td><td>4.750</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> </tbody> </table>	Load Current [A]	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 277[V]	0.0	0.261	0.454	0.610	16.8	2.419	1.290	1.117	33.6	4.720	2.381	1.849	50.4	7.060	3.515	2.645	67.2	9.450	4.680	3.478	84.0	11.910	5.860	4.320	92.4	13.150	6.460	4.750	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-	Temperature 25°C Testing Circuitry Figure A					
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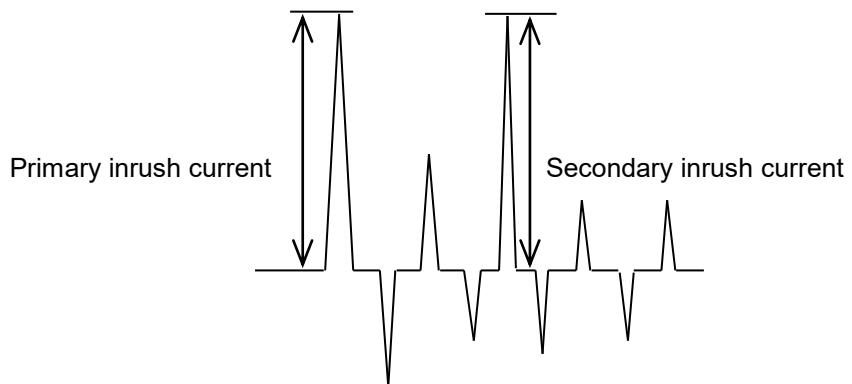
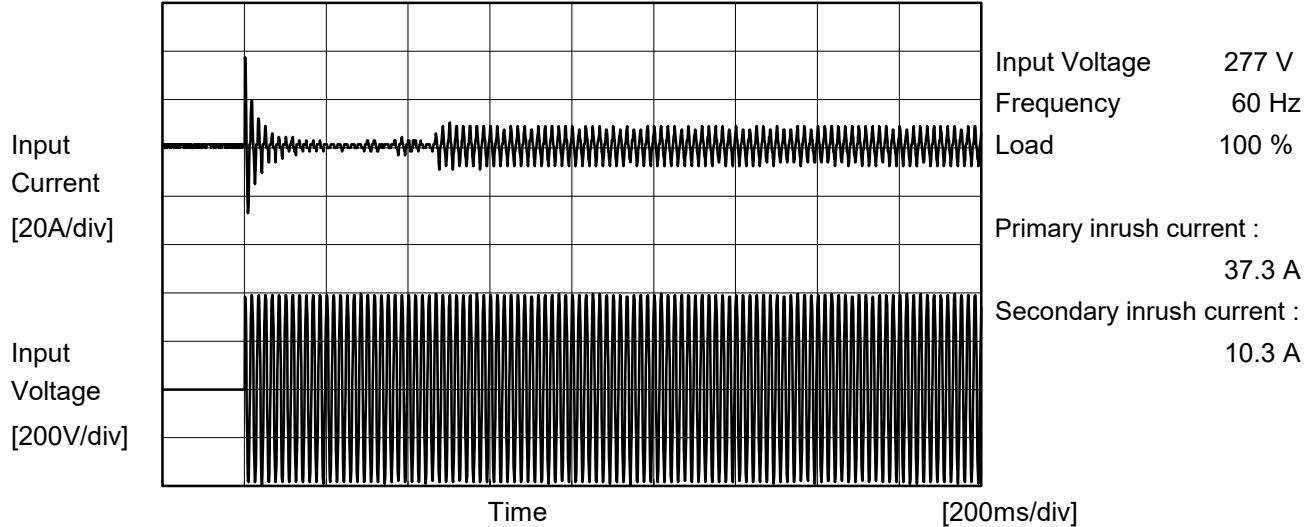
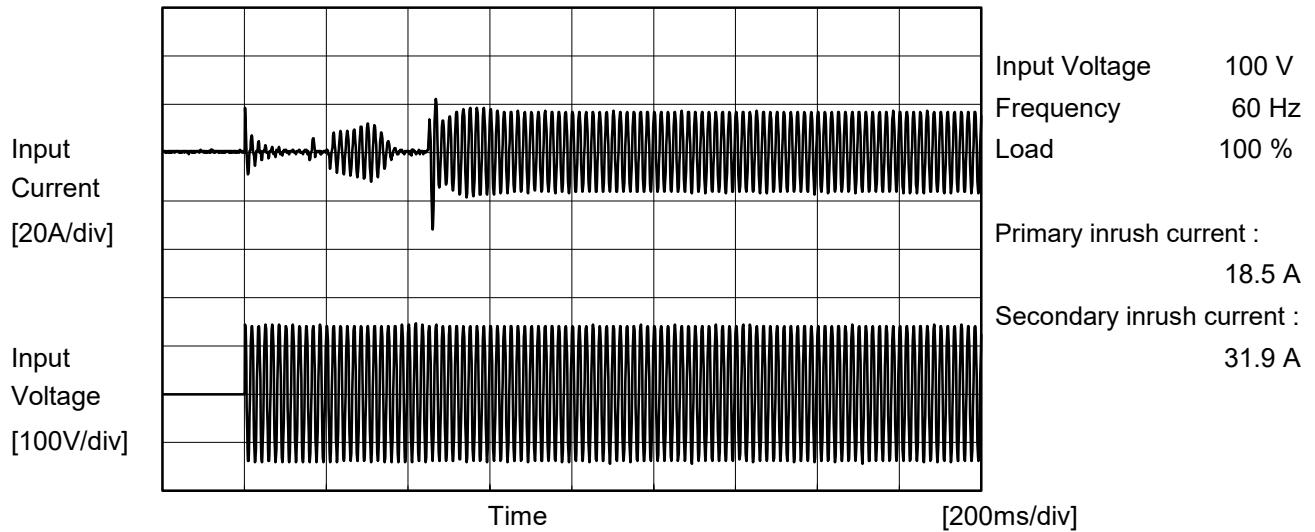
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Model	TUNS1200F12	Temperature Testing Circuitry Figure A
Item	Inrush Current	
Object	_____	





Model	TUNS1200F12	Temperature	25°C
Item	Leakage Current	Testing Circuitry	Figure B
Object	_____		

1. Results

[mA]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	200 [V]	240 [V]	
IEC60601-1	Figure B	Both phases	0.16	0.35	0.44	Operation
		One of phases	0.30	0.62	0.77	Stand by

The value for "One of phases" is the reference value only.

2. Condition

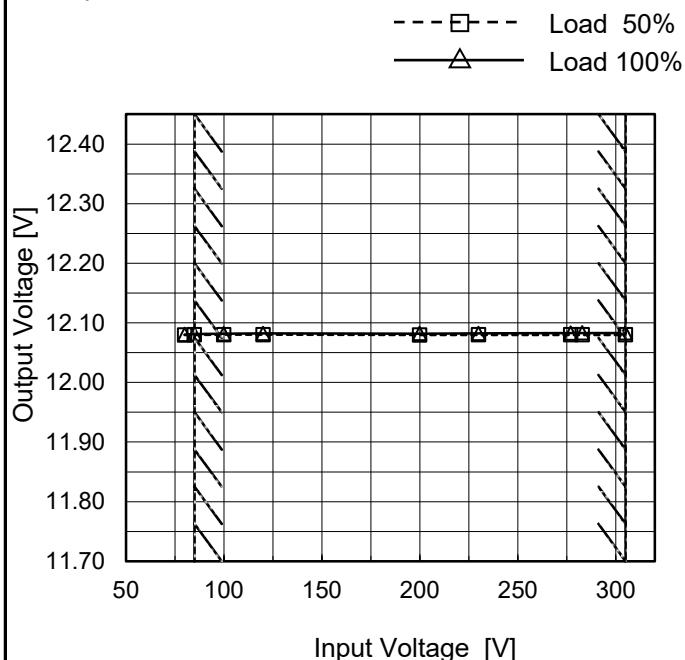
Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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Model	TUNS1200F12
Item	Line Regulation
Object	+12V84A

Temperature 25°C
 Testing Circuitry Figure A

1.Graph

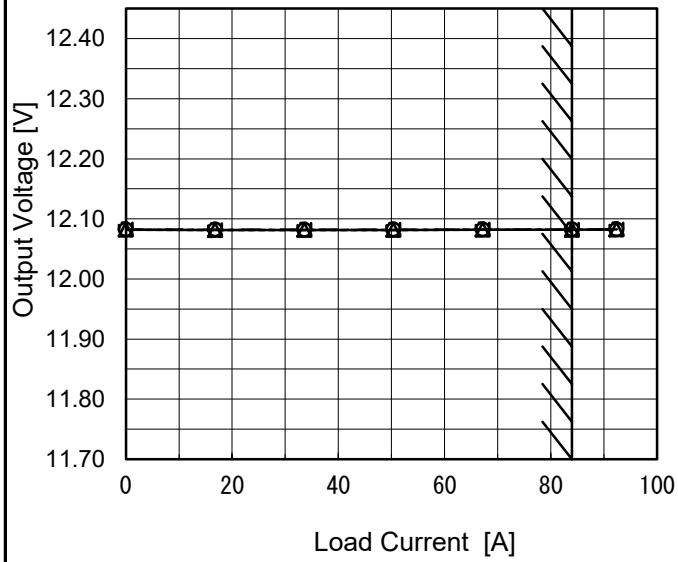
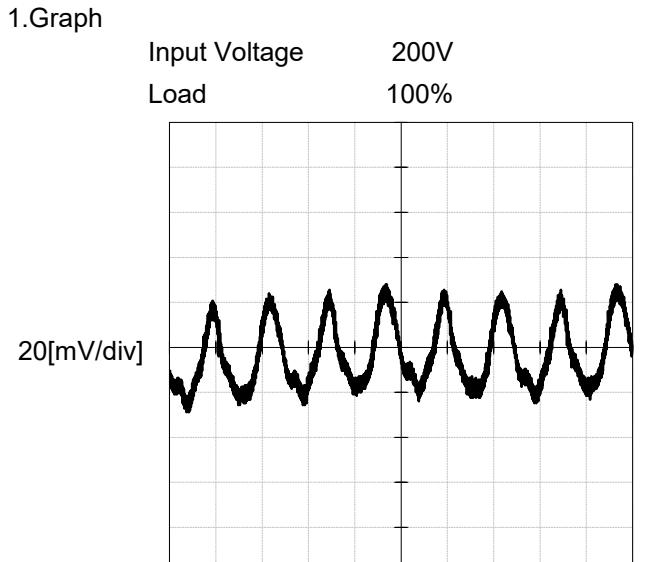


2.Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
80	12.079	12.080
85	12.080	12.081
100	12.080	12.082
120	12.080	12.082
200	12.080	12.082
230	12.080	12.082
277	12.080	12.083
283	12.080	12.083
305	12.080	12.083

Note: Slanted line shows the range of the rated input voltage.

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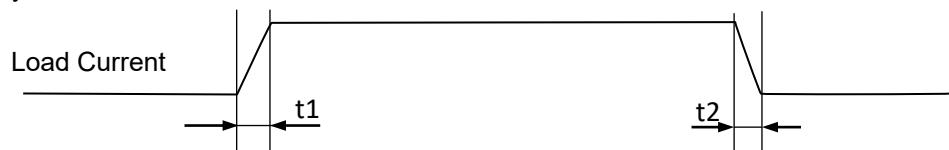
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Object	+12V84A	Testing Circuitry	Figure C																																																			
1.Graph	<p>Input Voltage 200V Load 100%</p> 																																																					

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Model	TUNS1200F12	Temperature Testing Circuitry	25°C Figure A
Item	Dynamic Load Response		
Object	+12V84A		

Input Volt. 100 V Response. $t_1=t_2=50\mu s$. Typ

Cycle 1000 ms

Load 0%(0A) \longleftrightarrow
Load 100%(84A)

500[mV/div]

1[ms/div]

10[ms/div]

Load 0%(0A) \longleftrightarrow
Load 50%(42A)

500[mV/div]

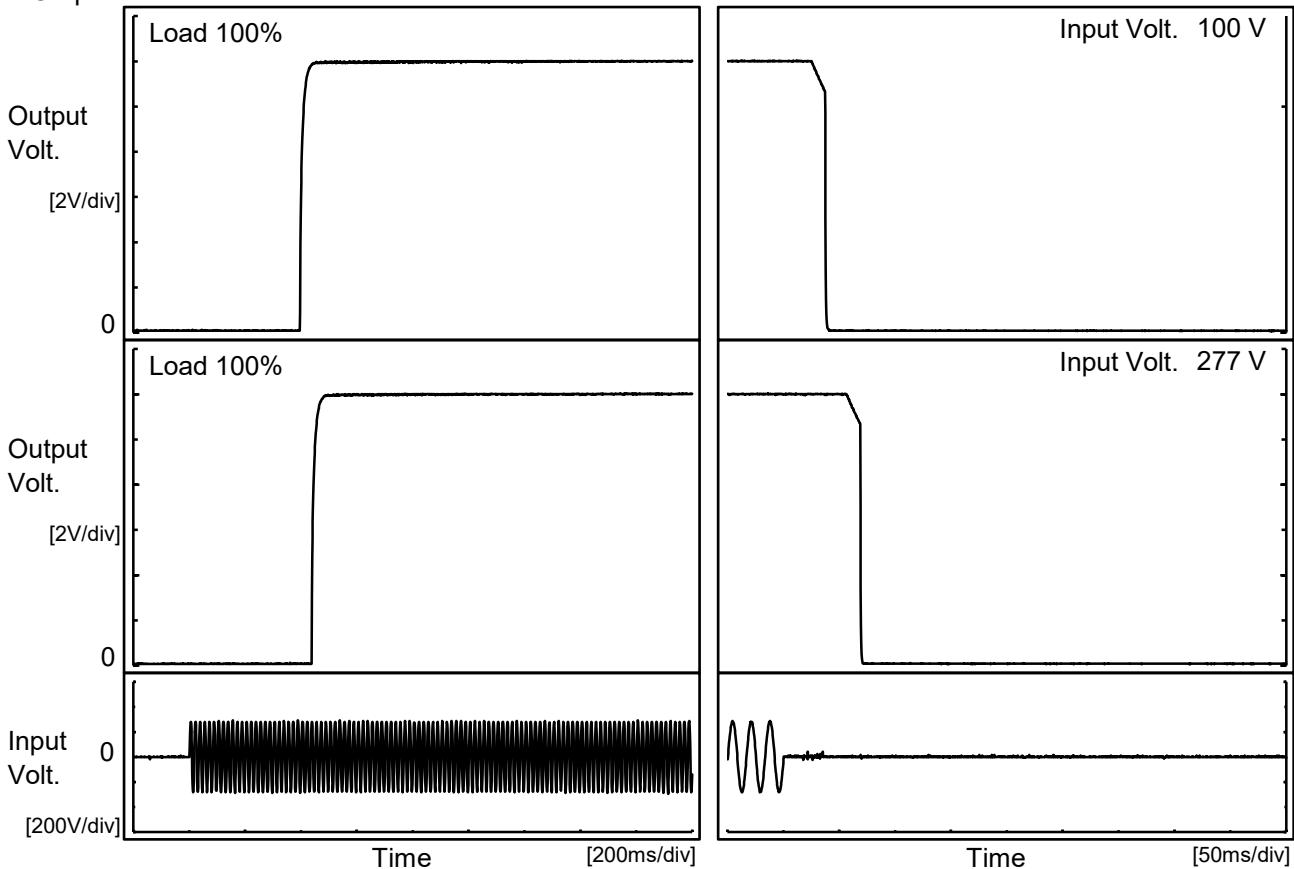
1[ms/div]

10[ms/div]

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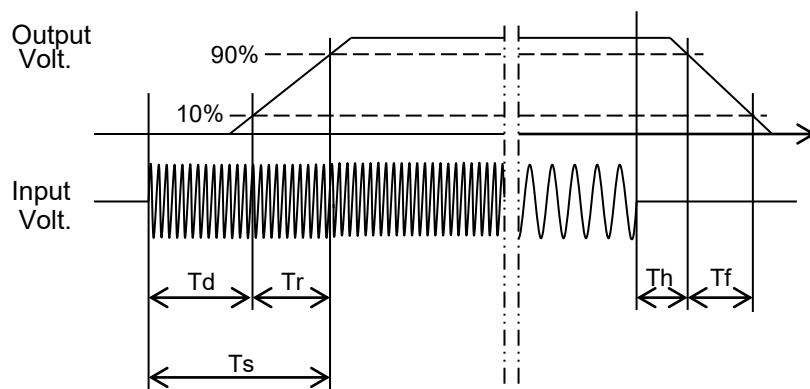
Model	TUNS1200F12	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+12V84A		

1. Graph



2. Values

Input Volt.	Time	Td	Tr	Ts	Th	Tf	[ms]
100 V		397.0	20.0	417.0	35.3	3.3	
277 V		437.0	21.0	458.0	66.8	3.0	

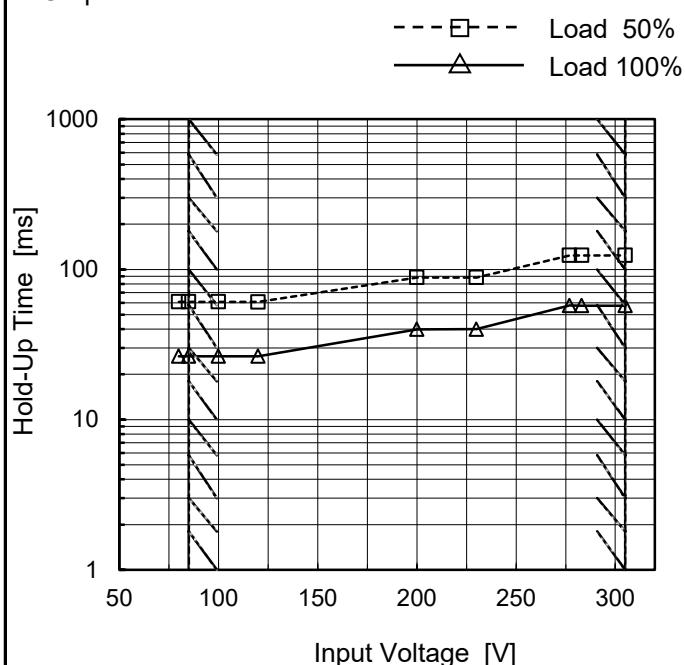


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Model	TUNS1200F12
Item	Hold-Up Time
Object	+12V84A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



2.Values

Input Voltage [V]	Hold-Up Time [ms]	
	Load 50%	Load 100%
80	61	26
85	61	26
100	61	26
120	61	26
200	88	40
230	88	40
277	124	58
283	124	57
305	124	57

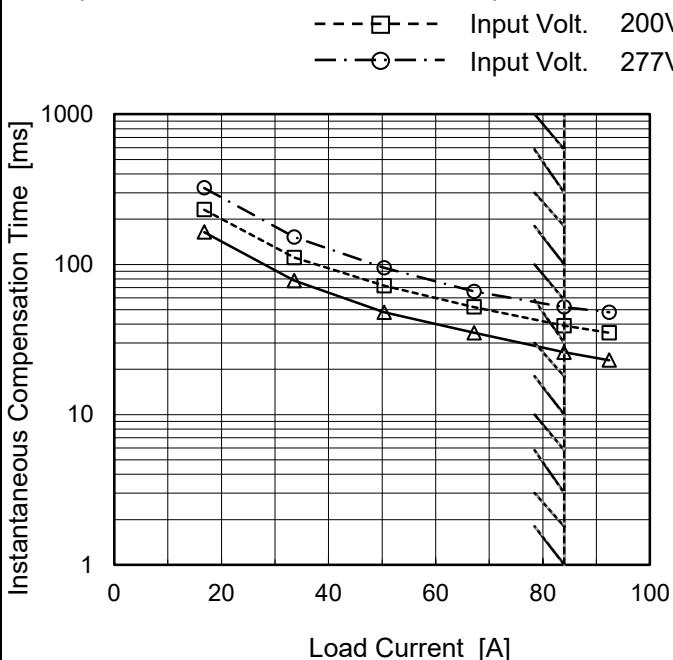
This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.
Note: Slanted line shows the range of the rated input voltage.

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Model	TUNS1200F12
Item	Instantaneous Interruption Compensation
Object	+12V84A

 Temperature 25°C
 Testing Circuitry Figure A

1.Graph



2.Values

Load Current [A]	Time [ms]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 277[V]
0.0	-	-	-
16.8	164	231	323
33.6	78	111	152
50.4	48	72	95
67.2	35	52	66
84.0	26	39	52
92.4	23	35	48
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

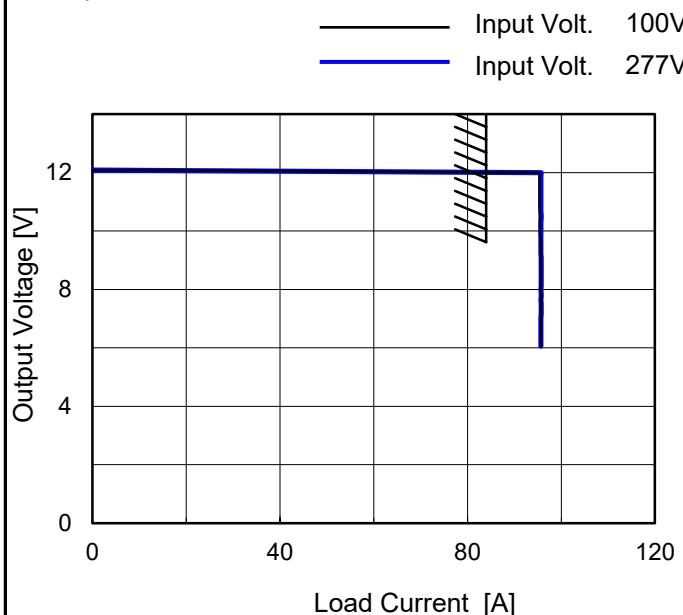
Note: Slanted line shows the range of the rated load current.

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Model	TUNS1200F12
Item	Overcurrent Protection
Object	+12V84A

 Temperature 25°C
 Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current.

Hiccup mode activates when the output voltage is from 6 to 0V.

2. Values

Output Voltage [V]	Load Current [A]	
	Input Volt. 100[V]	Input Volt. 277[V]
11.4	95.38	95.46
10.8	95.41	95.66
9.6	95.59	95.68
8.4	95.70	95.69
7.2	95.69	95.69
--	-	-
--	-	-
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--	-	-
--	-	-
--	-	-
--	-	-
--	-	-

COSEL

Model	TUNS1200F12	Testing Circuitry Figure A
Item	Ambient Temperature Drift	
Object	+12V84A	

1.Values

Load 100%

Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 100V	Input Volt. 200V	Input Volt. 277V
-40	12.033	12.033	12.034
25	12.082	12.082	12.082
80	12.097	12.096	12.096
85	12.096	12.097	12.097

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+12V84A	

1.Values

Ambient Temperature[°C]	Input Voltage [V]	
	Load 50%	Load 100%
-40	68	68
25	69	69
80	69	69
85	69	69

Item	Overvoltage Protection	Testing Circuitry Figure A
Object	+12V84A	

1.Values

Load 0%

Ambient Temperature[°C]	Operating Point [V]	
	Input Volt. 100V	Input Volt. 277V
-40	15.58	15.46
25	15.64	15.64
80	15.64	15.64
85	15.63	15.64

COSEL

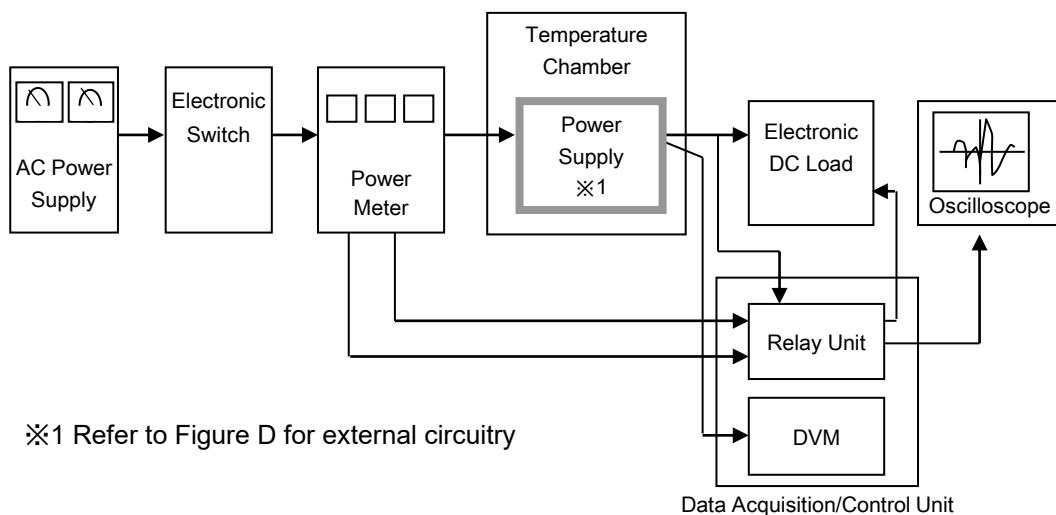


Figure A

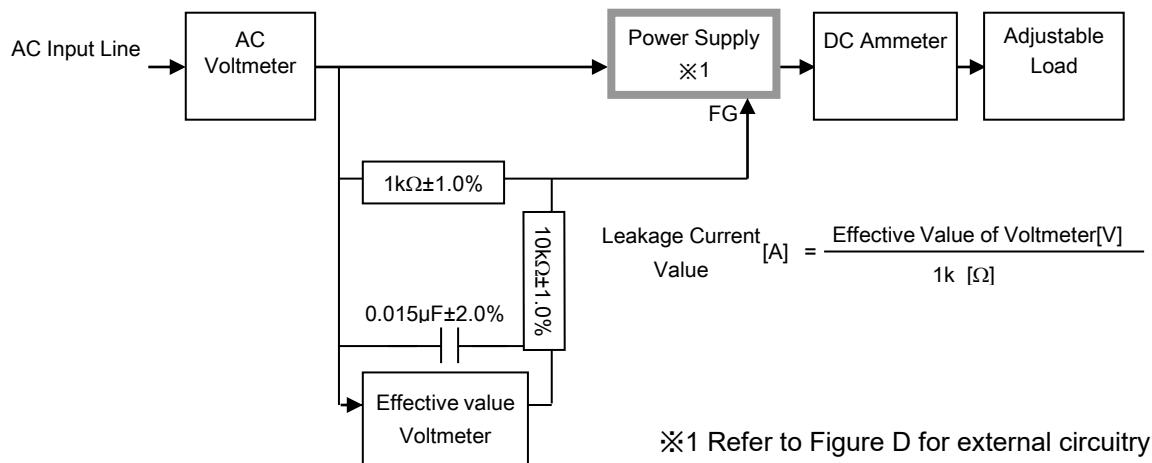


Figure B (IEC60601-1)

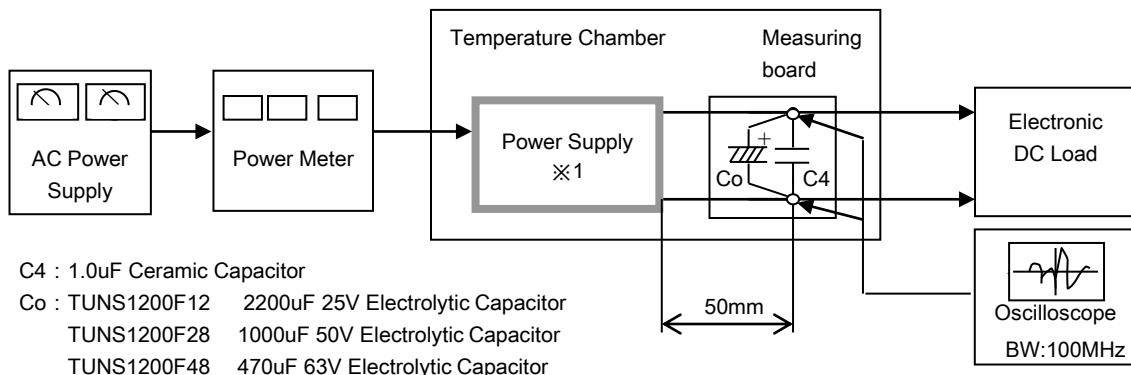
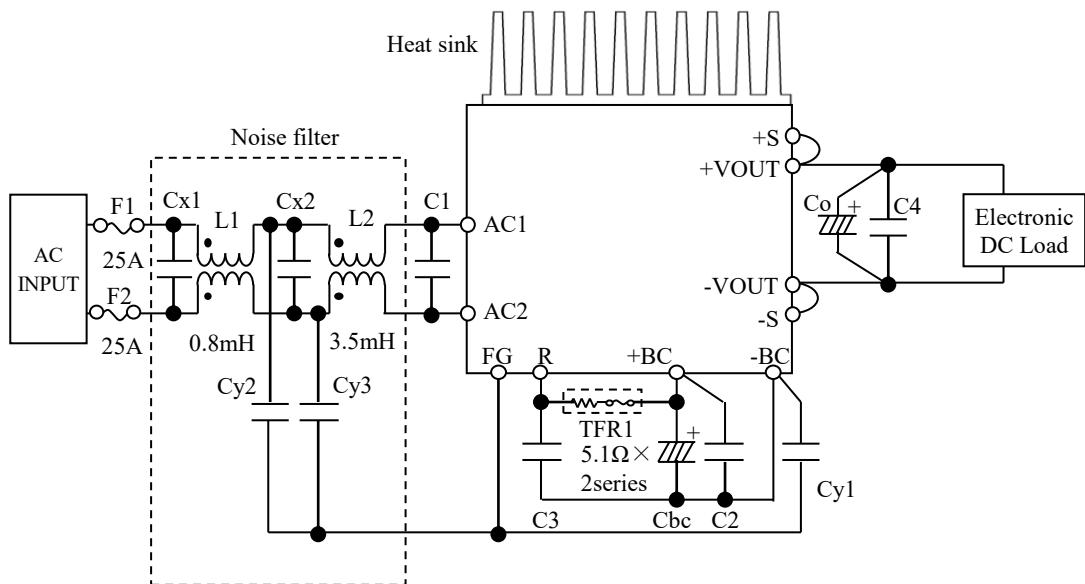


Figure C



- L1 : SCR25-200-1R7A008JH
- L2 : SC15-E350H
- Cx1,Cx2 : 1.5uF 310V Film Capacitor
- Cy1 : 2200pF 400V
- Cy2,Cy3 : 1500pF 400V
- C1 : 1.5uF 310V Film Capacitor × 2parallel
- C2,C3 : 1.0uF 630V Film Capacitor × 2parallel
- C4 : 1.0uF Ceramic Capacitor
- Cbc : 470uF 450V Electrolytic Capacitor × 3parallel ($0 \leq Ta \leq 85^{\circ}C$)
470uF 450V Electrolytic Capacitor × 6parallel ($-40 \leq Ta < 0^{\circ}C$)
- Co : TUNS1200F12 2200uF 25V Electrolytic Capacitor ($0 \leq Ta \leq 85^{\circ}C$)
2200uF 25V Electrolytic Capacitor × 3parallel ($-40 \leq Ta < 0^{\circ}C$)
TUNS1200F28 1000uF 50V Electrolytic Capacitor ($0 \leq Ta \leq 85^{\circ}C$)
1000uF 50V Electrolytic Capacitor × 3parallel ($-40 \leq Ta < 0^{\circ}C$)
TUNS1200F48 470uF 63V Electrolytic Capacitor ($0 \leq Ta \leq 85^{\circ}C$)
470uF 63V Electrolytic Capacitor × 3parallel ($-40 \leq Ta < 0^{\circ}C$)

Ta : Ambient Temp.

Figure D