

TEST DATA OF TEPS65F05

Regulated DC Power Supply
October.3. 2023

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Design Manager

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Design Engineer

COSEL CO.,LTD.

CONTENTS

1.Input Current (by Load Current)	1
2.Efficiency (by Load Current)	2
3.Power Factor (by Load Current)	3
4.Inrush Current	4
5.Leakage Current	5
6.Line Regulation	6
7.Load Regulation	7
8.Ripple-Noise	7
9.Dynamic Load Response	8
10.Rise and Fall Time	9
11.Hold-Up Time	10
12.Instantaneous Interruption Compensation	11
13.Overcurrent Protection	12
14.Ambient Temperature Drift	13
15.Minimum Input Voltage for Regulated Output Voltage	13
16.Overvoltage Protection	13
17.Figure of Testing Circuitry	14

(Final Page 15)

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Model		TEPS65F05		Temperature Testing Circuitry	25°C Figure A																																																			
Item		Input Current (by Load Current)																																																						
Object		_____																																																						
1.Graph		<div><div>—△—</div>Input Volt. 100V</div> <div><div>---□---</div>Input Volt. 200V</div> <div><div>-·-○-·-</div>Input Volt. 230V</div> <p>Input Current [A]</p> <p>Load Current [A]</p> <p>Note: Slanted line shows the range of the rated load current.</p>		2.Values																																																				
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BC-11941

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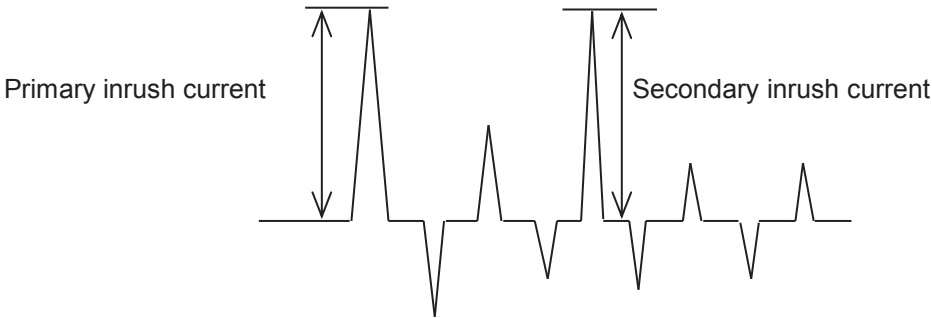
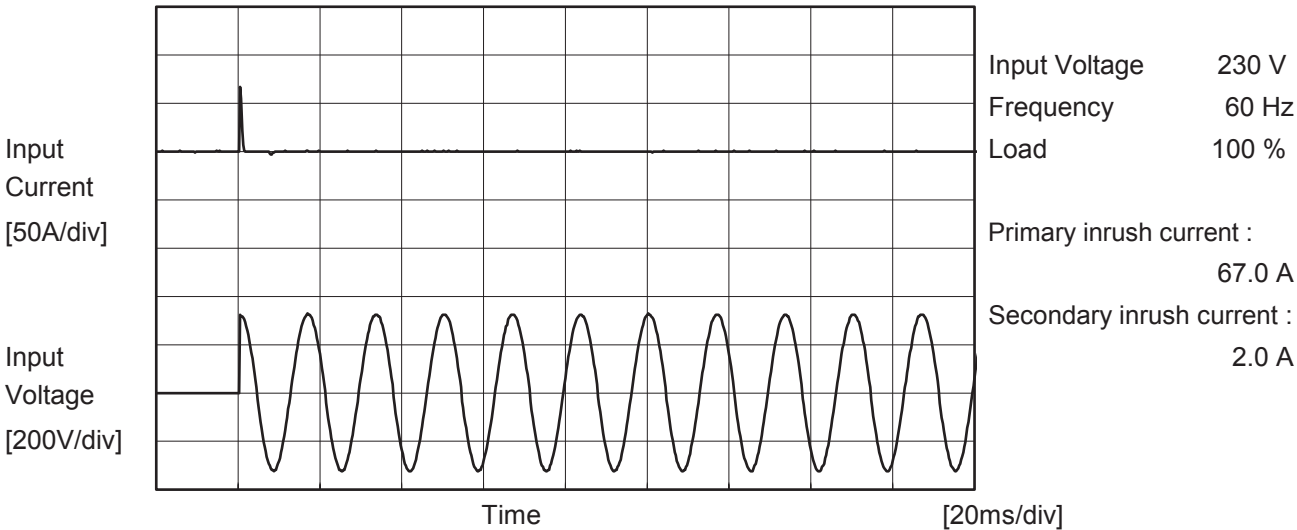
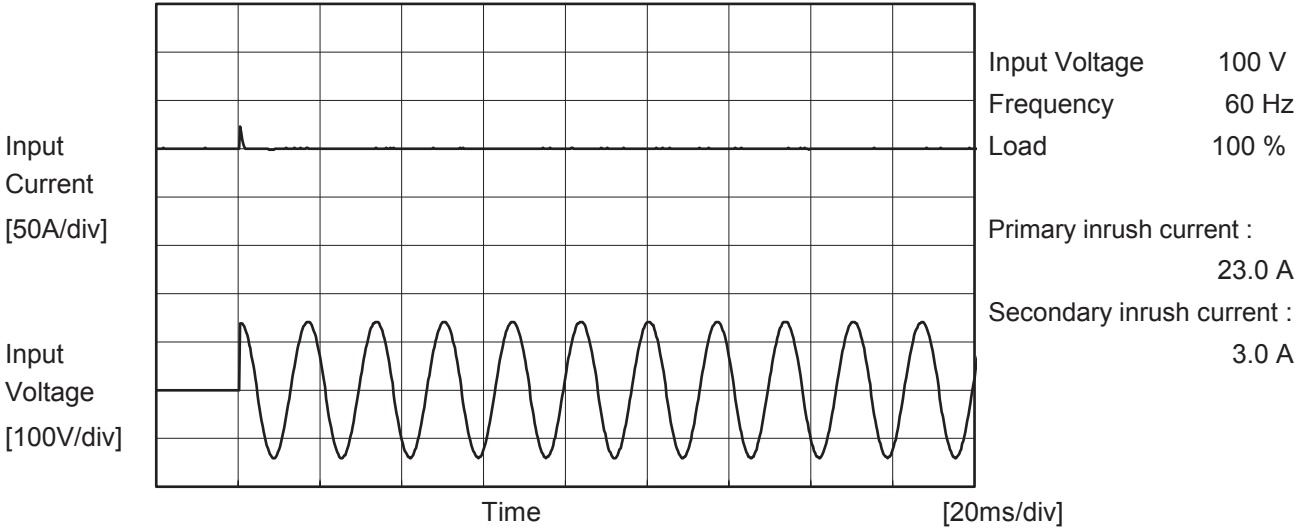
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BC-11941

- 3 -



Model		TEPS65F05	
Item		Inrush Current	Temperature 25°C Testing Circuitry Figure A
Object			





Model		TEPS65F05	Temperature 25°C Testing Circuitry Figure C
Item		Leakage Current	
Object		_____	

1.Results

[mA]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	230 [V]	264 [V]	
DEN-AN	Figure C-1	Both phases	0.03	0.07	0.08	Operation
		One of phases	0.05	0.11	0.13	Stand by
IEC62368-1	Figure C-2	Both phases	0.03	0.07	0.08	Operation
		One of phases	0.05	0.11	0.13	Stand by
	Figure C-3	Both phases	0.03	0.07	0.08	Operation
		One of phases	0.05	0.11	0.13	Stand by

The value for "One of phases" is the reference value only.

2.Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.



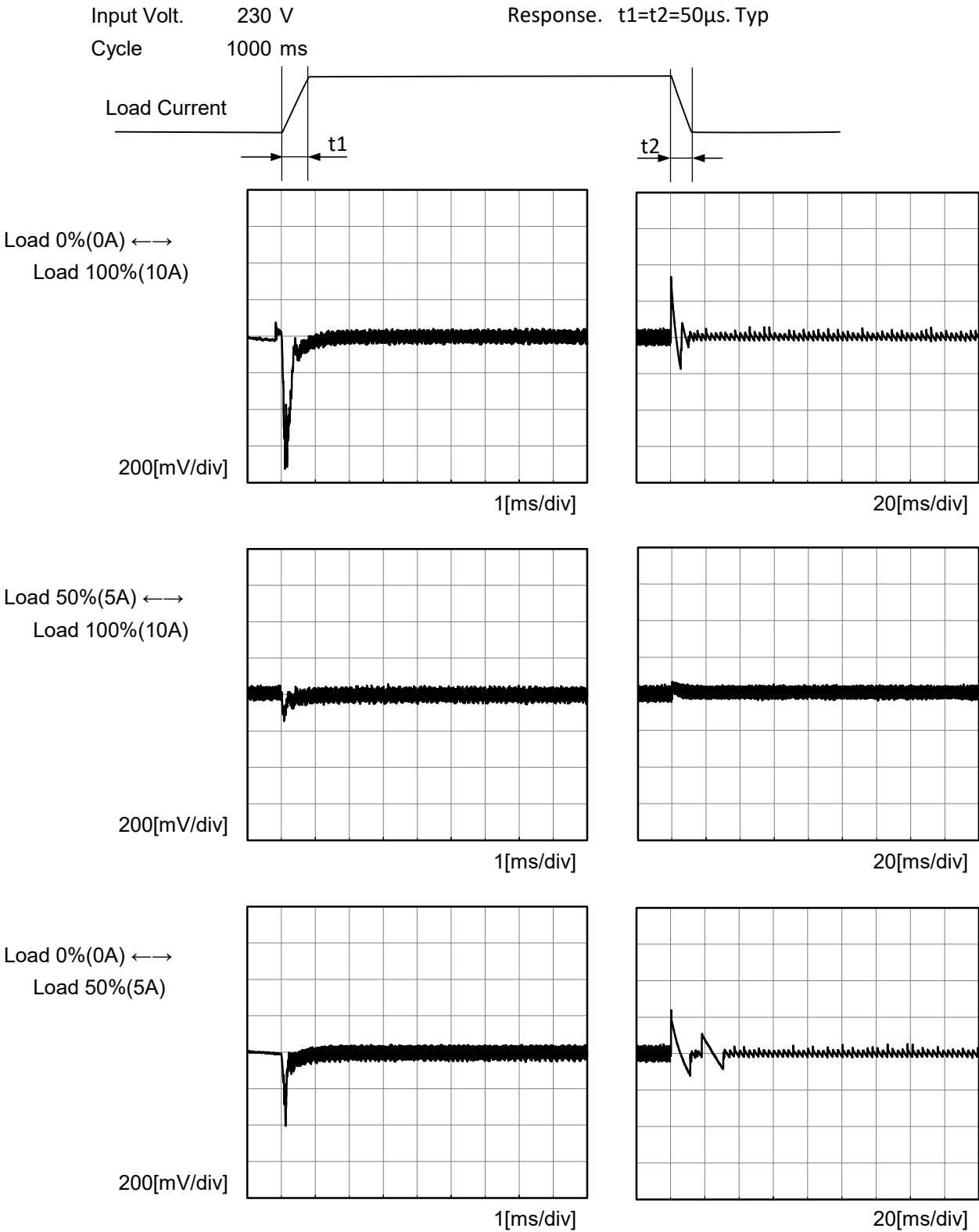
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Model	TEPS65F05																																		
Item	Line Regulation	Temperature	25°C																																
Object	+5V10A	Testing Circuitry	Figure A																																
1.Graph		2.Values																																	
<div><div><div><div><div>---</div><div>□</div><div>---</div></div><div>Load 50%</div></div><div><div>—</div><div>△</div><div>—</div></div><div>Load 100%</div></div></div> <div><div><div>Output Voltage [V]</div><div><div><div><div>5.12</div><div>5.10</div><div>5.08</div><div>5.06</div><div>5.04</div><div>5.02</div><div>5.00</div><div>4.98</div></div><div><div><div><div>50</div><div>100</div><div>150</div><div>200</div><div>250</div><div>300</div></div></div></div><div><div>Input Voltage [V]</div></div></div></div></div><div><div>Note: Slanted line shows the range of the rated input voltage.</div></div></div>		<table><tr><th rowspan="2">Input Voltage [V]</th><th colspan="2">Output Voltage [V]</th></tr><tr><th>Load 50%</th><th>Load 100%</th></tr><tr><td>85</td><td>5.069</td><td>-</td></tr><tr><td>100</td><td>5.069</td><td>5.068</td></tr><tr><td>115</td><td>5.070</td><td>5.068</td></tr><tr><td>200</td><td>5.070</td><td>5.068</td></tr><tr><td>230</td><td>5.070</td><td>5.067</td></tr><tr><td>264</td><td>5.070</td><td>5.067</td></tr><tr><td>280</td><td>5.070</td><td>5.067</td></tr><tr><td>--</td><td>-</td><td>-</td></tr><tr><td>--</td><td>-</td><td>-</td></tr></table>		Input Voltage [V]	Output Voltage [V]		Load 50%	Load 100%	85	5.069	-	100	5.069	5.068	115	5.070	5.068	200	5.070	5.068	230	5.070	5.067	264	5.070	5.067	280	5.070	5.067	--	-	-	--	-	-
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Object	+5V10A	Testing Circuitry	Figure B																																																			
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<div><div>Input Voltage</div><div>230V</div></div> <div><div>Load</div><div>100%</div></div> <p>50[mV/div]</p> <p>40[μs/div]</p>																																																						



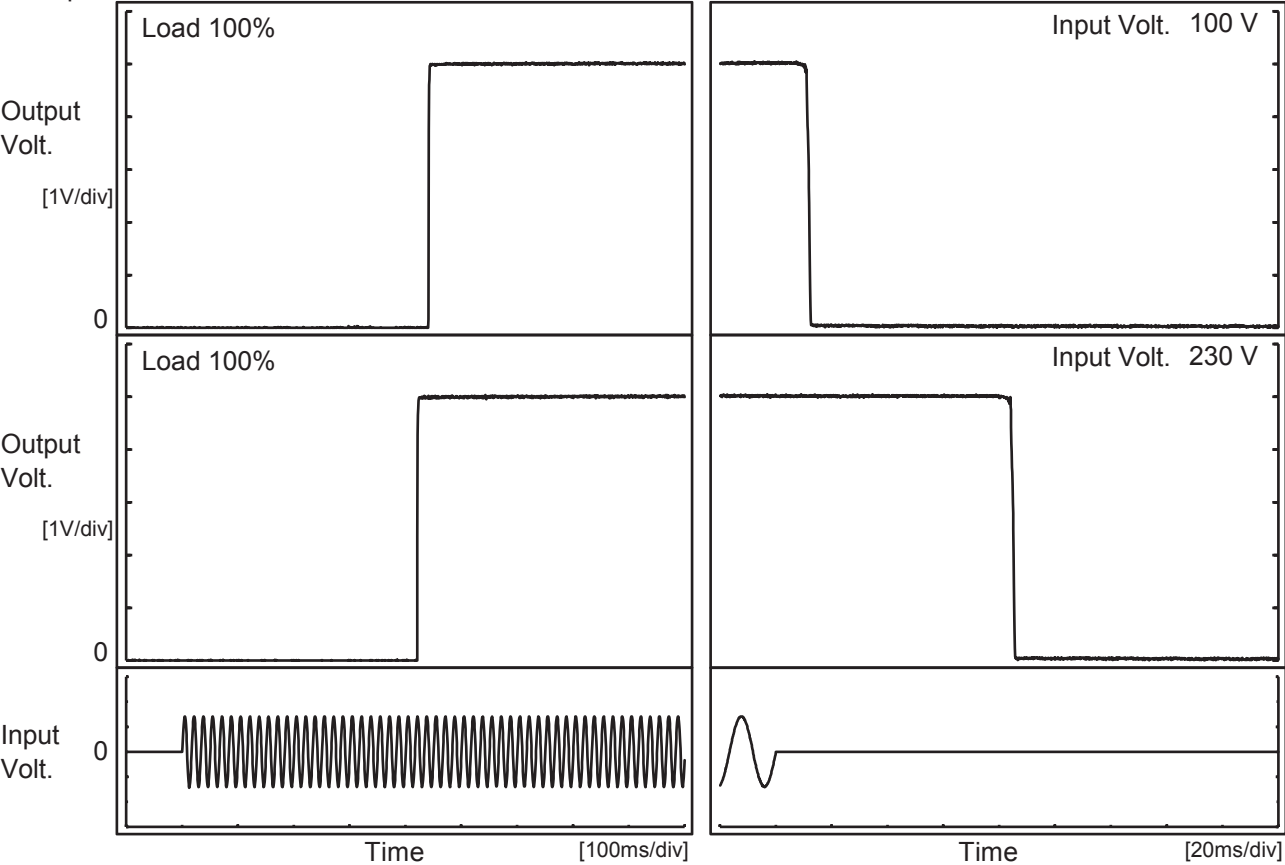
Model		TEPS65F05	Temperature 25°C Testing Circuitry Figure A
Item		Dynamic Load Response	
Object		+5V10A	





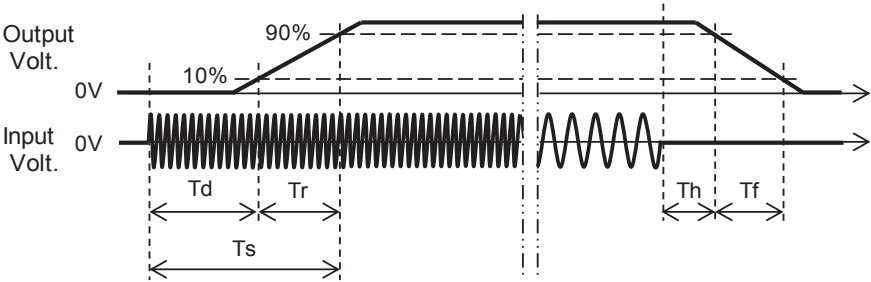
Model		TEPS65F05	Temperature 25°C Testing Circuitry Figure A
Item		Rise and Fall Time	
Object		+24V2.75A	

1.Graph



2.Values

		[ms]				
Input Volt.	Time	Td	Tr	Ts	Th	Tf
100 V		442.0	1.5	443.5	11.2	1.2
230 V		421.5	1.0	422.5	84.3	1.3



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Model		TEPS65F05	
Item		Hold-Up Time	
Object		+5V10A	
1.Graph		2.Values	

<

Model		TEPS65F05	Temperature 25°C Testing Circuitry Figure A																																																			
Item		Instantaneous Interruption Compensation																																																				
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1.Graph		<div><div>—△—</div>Input Volt. 100V</div> <div><div>- - □ - -</div>Input Volt. 200V</div> <div><div>- · ○ - ·</div>Input Volt. 230V</div> <p>Instantaneous Compensation Time [ms]</p> <p>Load Current [A]</p>	2.Values																																																			
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- 11 -

BC-11941



Model		TEPS65F05	Temperature Testing Circuitry	25°C Figure A
Item		Overcurrent Protection		
Object		+5V10A		

1.Graph

Input Volt. 100V

Input Volt. 230V

Output Voltage [V]

</



Model		TEPS65F05	Testing Circuitry Figure A
Item		Ambient Temperature Drift	
Object		+5V10A	
1.Values Load 100%			
Ambient Temperature[°C]		Output Voltage [V]	
		Input Volt. 100V	Input Volt. 200V
		Input Volt. 230V	
-10	5.094	5.093	5.092
25	5.070	5.069	5.067
50	5.053	5.052	5.051

Item		Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object		+5V10A	
1.Values			
Ambient Temperature[°C]		Input Voltage [V]	
		Load 50%	Load 100%
-10	62	64	
25	63	65	
50	63	65	

Item		Overvoltage Protection	Testing Circuitry Figure A
Object		+5V10A	
1.Values Load 0%			
Ambient Temperature[°C]		Operating Point [V]	
		Input Volt. 100V	Input Volt. 230V
-10	5.79	5.79	
25	5.79	5.79	
50	5.71	5.71	

- 13 -

BC-11941

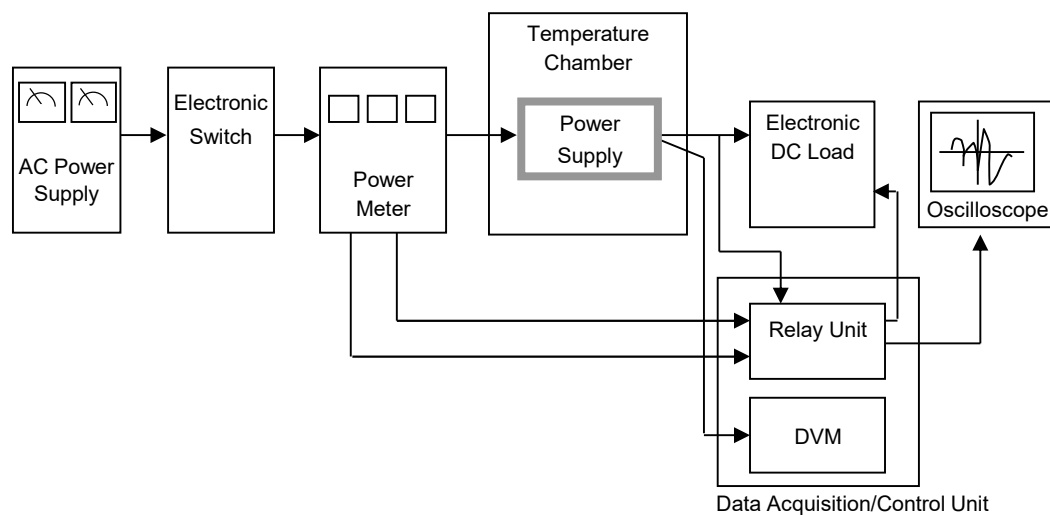


Figure A

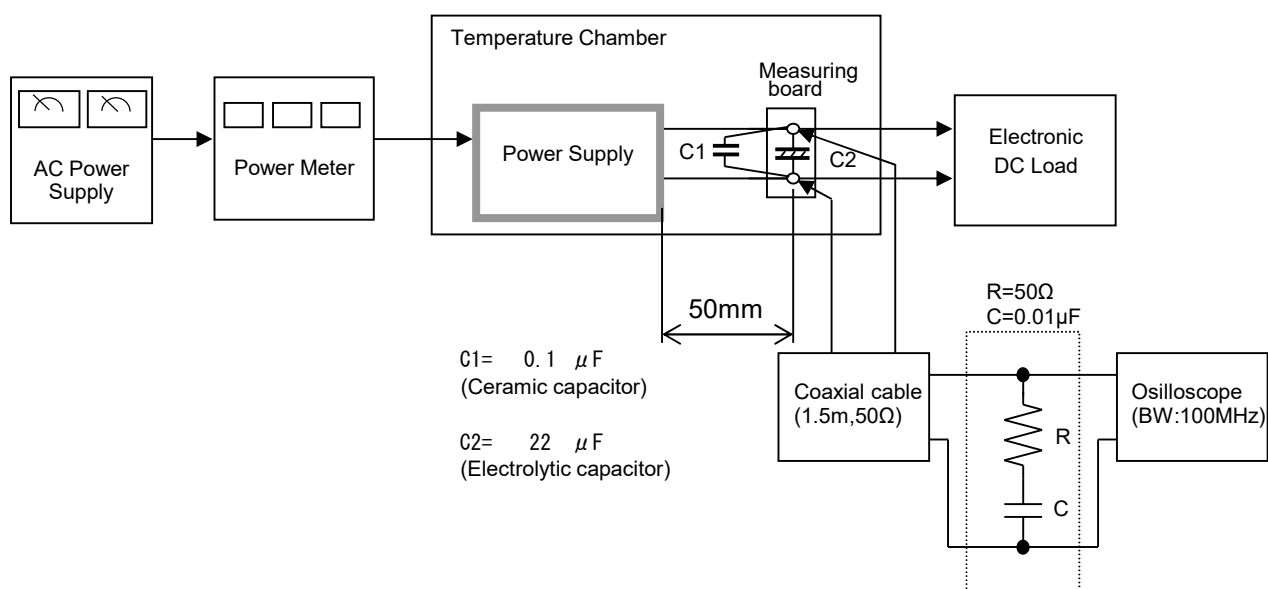


Figure B

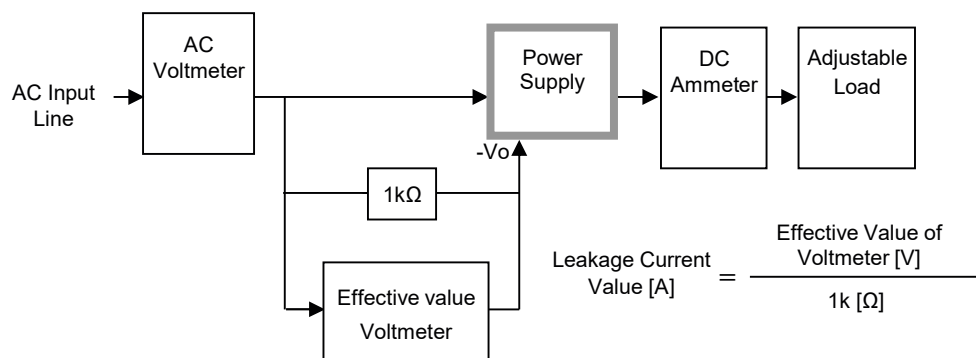


Figure C-1 (DEN-AN)

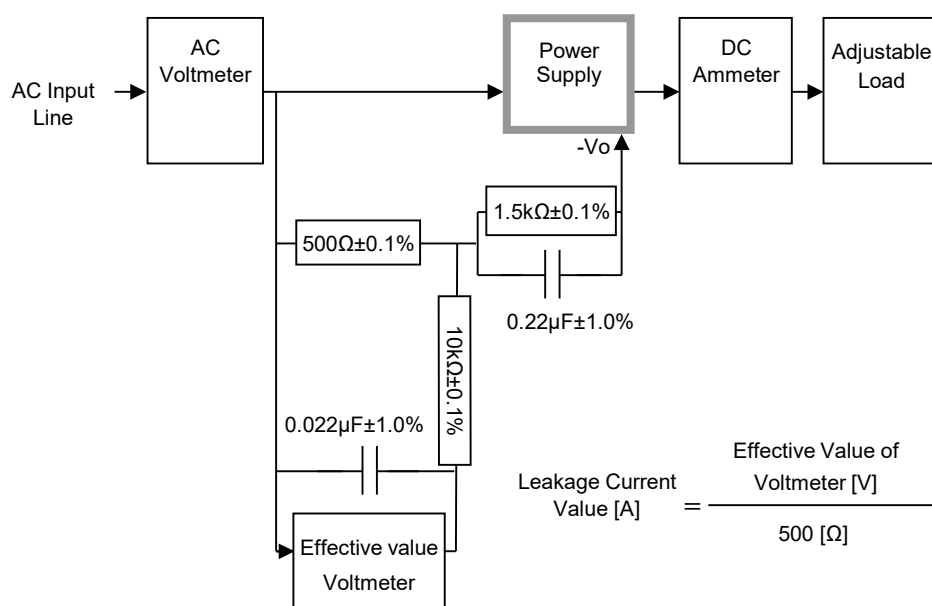


Figure C-2 (IEC62368-1 refer to IEC60990 Fig.4)

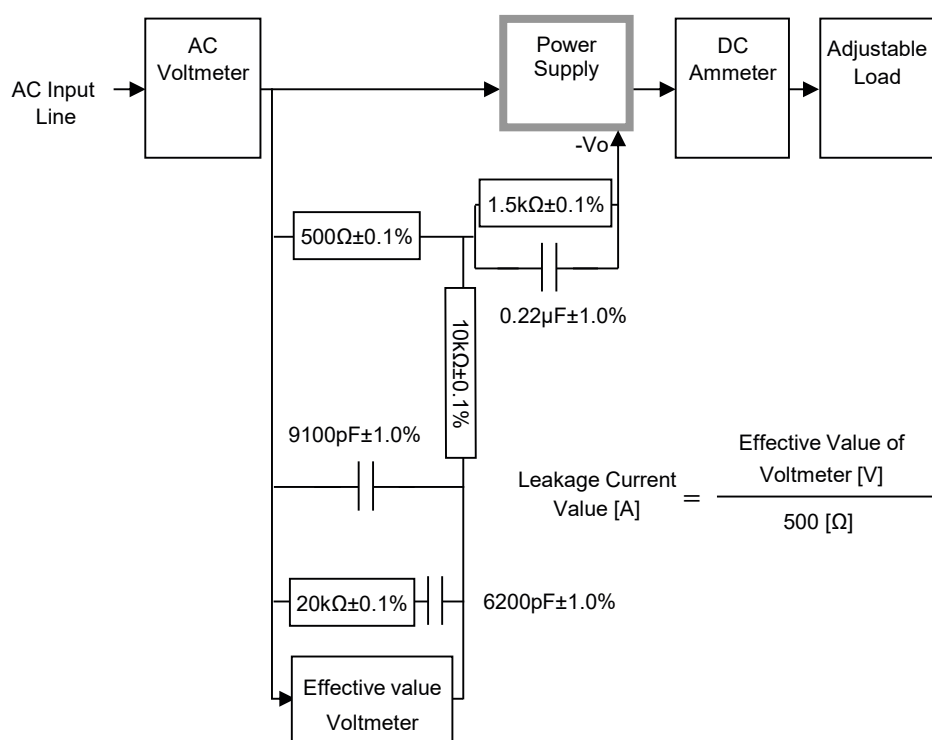


Figure C-3 (IEC62368-1 refer to IEC60990 Fig.5)