

TEST DATA OF SUTS61205

Regulated DC Power Supply
March 11, 2009

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Kazunari Asano Design Manager

Prepared by : Sho Saito
Sho Saito Design Engineer

COSEL CO.,LTD.

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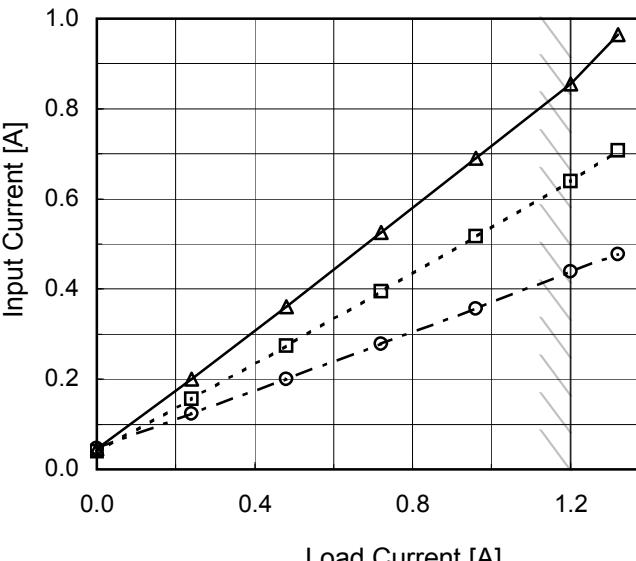
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Model	SUTS61205	Temperature Testing Circuitry 25°C Figure A																																											
Item	Input Current (by Input Voltage)																																												
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Note: Slanted line shows the range of the rated input voltage.

Input Voltage [V]	Input Current [A]		
	Load 0%	Load 50%	Load 100%
0.0	0.000	0.000	0.000
2.0	0.001	0.001	0.001
4.0	0.001	0.001	0.001
6.0	0.002	0.002	0.002
7.0	0.002	0.002	0.002
7.6	0.050	0.519	1.028
8.0	0.048	0.492	0.977
9.0	0.045	0.436	0.861
10.0	0.043	0.397	0.769
12.0	0.040	0.333	0.637
14.0	0.041	0.289	0.547
16.0	0.043	0.259	0.482
18.0	0.047	0.238	0.443
20.0	0.049	0.222	0.397
21.0	0.050	0.215	0.382
21.8	0.050	0.211	0.370
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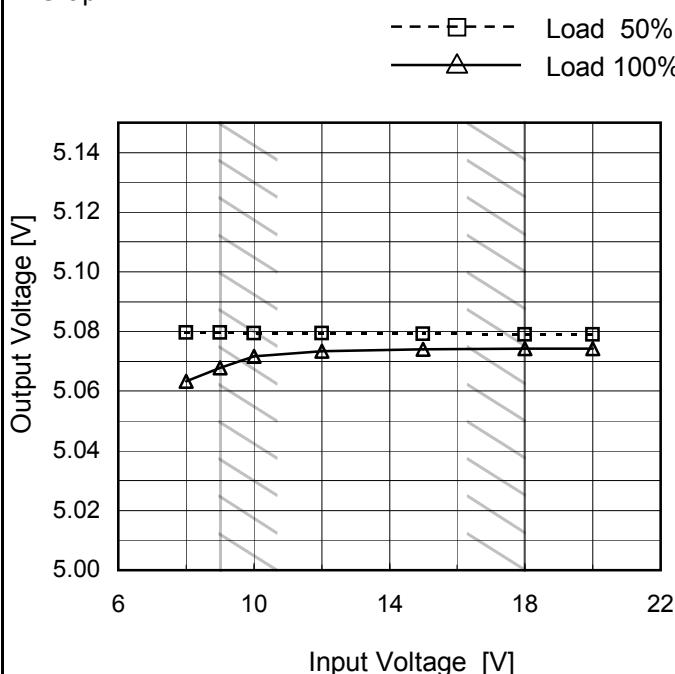
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<p>The graph plots Efficiency [%] on the y-axis (30 to 100) against Input Voltage [V] on the x-axis (6 to 22). Two data series are shown: Load 50% (dashed line with square markers) and Load 100% (solid line with triangle markers). Both series show a slight initial increase in efficiency followed by a gradual decrease as input voltage increases. The Load 50% curve starts at approximately 78% at 8V and ends at about 68% at 20V. The Load 100% curve starts at approximately 77% at 8V and ends at about 75% at 20V. A shaded gray area between two slanted lines indicates the rated input voltage range, which is approximately 8V to 12V.</p>			<table border="1"> <thead> <tr> <th rowspan="2">Input Voltage [V]</th> <th colspan="2">Efficiency [%]</th> </tr> <tr> <th>Load 50%</th> <th>Load 100%</th> </tr> </thead> <tbody> <tr> <td>8</td> <td>77.4</td> <td>77.9</td> </tr> <tr> <td>9</td> <td>77.5</td> <td>79.1</td> </tr> <tr> <td>10</td> <td>77.3</td> <td>79.5</td> </tr> <tr> <td>12</td> <td>76.5</td> <td>79.8</td> </tr> <tr> <td>15</td> <td>74.3</td> <td>79.2</td> </tr> <tr> <td>18</td> <td>71.0</td> <td>77.9</td> </tr> <tr> <td>20</td> <td>68.7</td> <td>76.8</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> </tr> </tbody> </table>	Input Voltage [V]	Efficiency [%]		Load 50%	Load 100%	8	77.4	77.9	9	77.5	79.1	10	77.3	79.5	12	76.5	79.8	15	74.3	79.2	18	71.0	77.9	20	68.7	76.8	--	-	-	--	-	-
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Item	Line Regulation
Object	+5V1.2A

Temperature 25°C
Testing Circuitry Figure A

1. Graph



2. Values

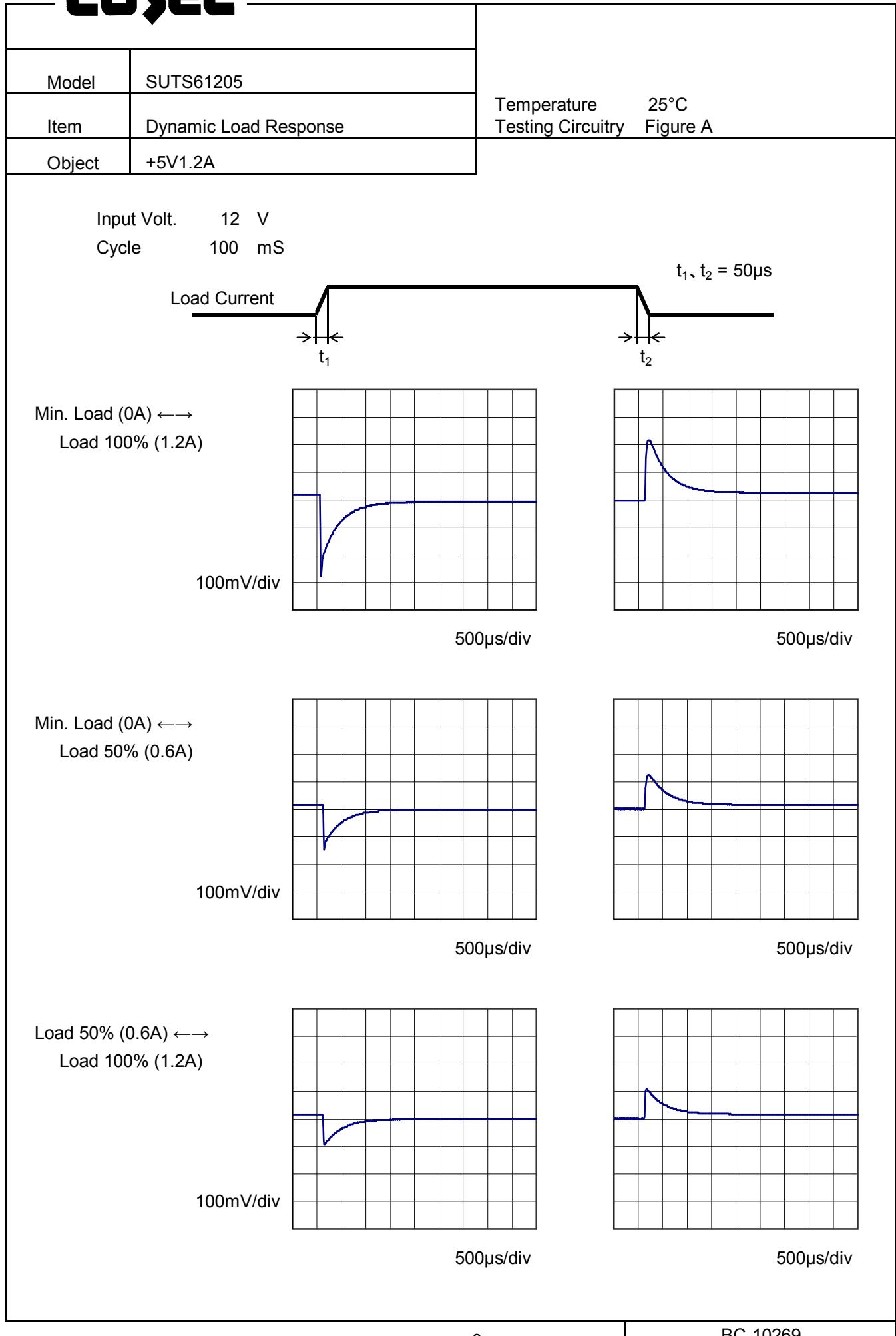
Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
8	5.080	5.063
9	5.080	5.068
10	5.079	5.072
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Note: Slanted line shows the range of the rated load current.

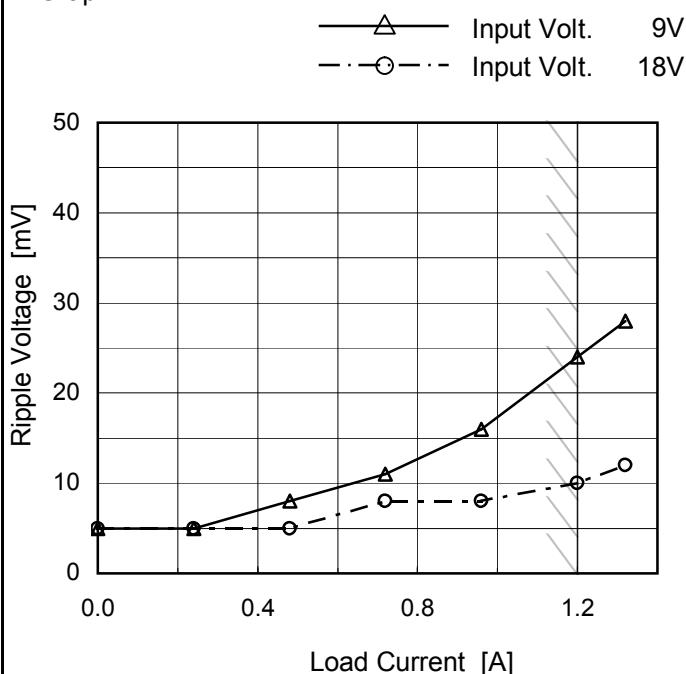
COSEL



Model	SUTS61205
Item	Ripple Voltage (by Load Current)
Object	+5V1.2A

Temperature 25°C
Testing Circuitry Figure B

1. Graph



2. Values

Load Current [A]	Ripple Voltage [mV]	
	Input Volt. 9 [V]	Input Volt. 18 [V]
0.00	5	5
0.24	5	5
0.48	8	5
0.72	11	8
0.96	16	8
1.20	24	10
1.32	28	12
--	-	-
--	-	-
--	-	-
--	-	-

Ripple Voltage is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

Ripple [mVp-p]

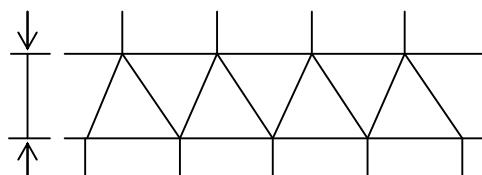
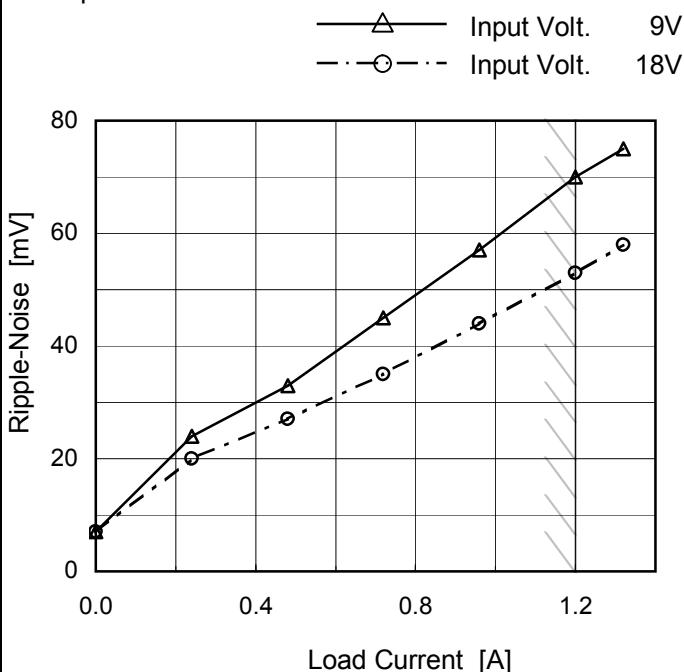


Fig.Complex Ripple Wave Form

Model	SUTS61205
Item	Ripple-Noise
Object	+5V1.2A

Temperature 25°C
Testing Circuitry Figure B

1.Graph



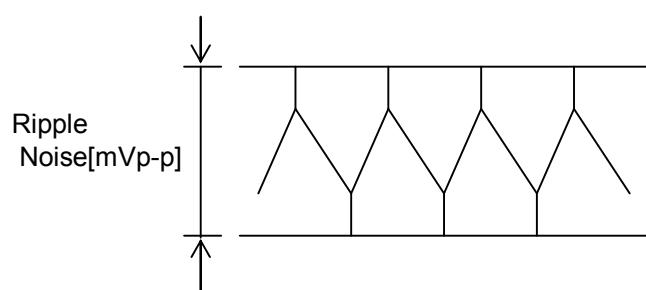
Measured by 100 MHz Oscilloscope.

Ripple-Noise is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

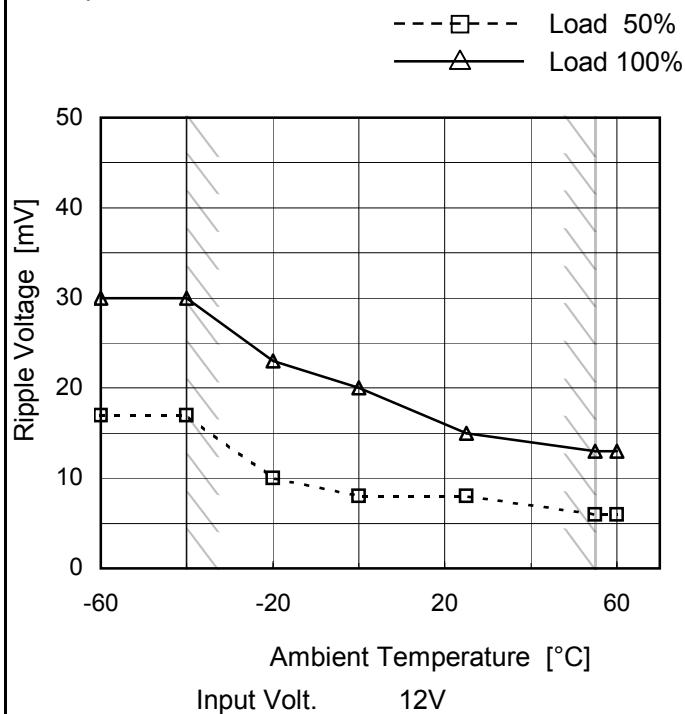
2.Values

Load Current [A]	Ripple-Noise [mV]	
	Input Volt. 9 [V]	Input Volt. 18 [V]
0.00	7	7
0.24	24	20
0.48	33	27
0.72	45	35
0.96	57	44
1.20	70	53
1.32	75	58
--	-	-
--	-	-
--	-	-
--	-	-



Model	SUTS61205
Item	Ripple Voltage (by Ambient Temp.)
Object	+5V1.2A

1. Graph



Measured by 100 MHz Oscilloscope.

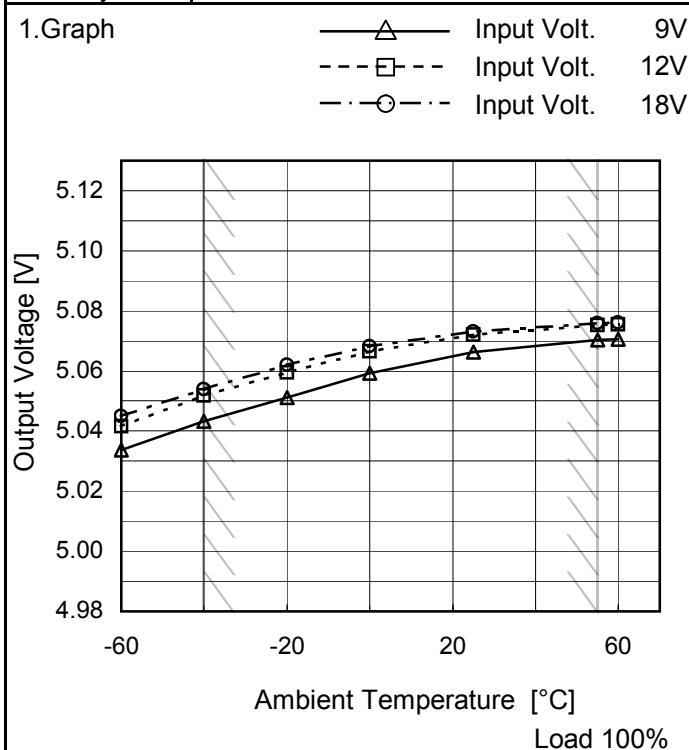
Note: Slanted line shows the range of the rated ambient temperature.

Testing Circuitry Figure B

2.Values

Ambient Temperature [°C]	Ripple Voltage [mV]	
	Load 50%	Load 100%
-60	17	30
-40	17	30
-20	10	23
0	8	20
25	8	15
55	6	13
60	6	13
--	-	-
--	-	-
--	-	-
--	-	-

Model	SUTS61205
Item	Ambient Temperature Drift
Object	+5V1.2A



Testing Circuitry Figure A

2. Values

Ambient Temperature [°C]	Output Voltage [V]		
	Input Volt. 9[V]	Input Volt. 12[V]	Input Volt. 18[V]
-60	5.034	5.042	5.045
-40	5.043	5.052	5.054
-20	5.051	5.060	5.062
0	5.059	5.066	5.068
25	5.066	5.072	5.073
55	5.070	5.075	5.076
60	5.071	5.075	5.076
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

Note: Slanted line shows the range of the rated ambient temperature.



Model	SUTS61205	Testing Circuitry Figure A
Item	Output Voltage Accuracy	
Object	+5V1.2A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -40 - 55°C

Input Voltage : 9 - 18V

Load Current : 0 - 1.2A

* Output Voltage Accuracy = $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

$$\text{* Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

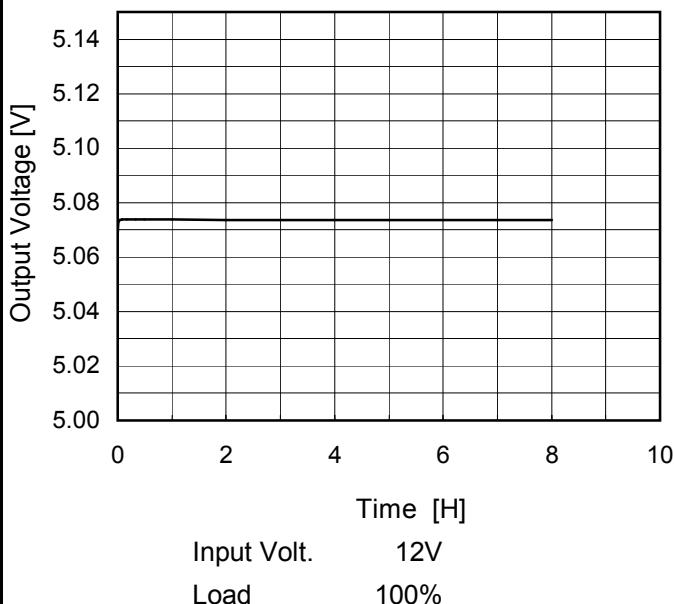
2. Values

Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ration [%]
Maximum Voltage	55	18	0	5.088	±23	±0.5
Minimum Voltage	-40	9	1.2	5.043		

COSEL

Model	SUTS61205
Item	Time Lapse Drift
Object	+5V1.2A

1. Graph



Temperature 25°C
Testing Circuitry Figure A

2. Values

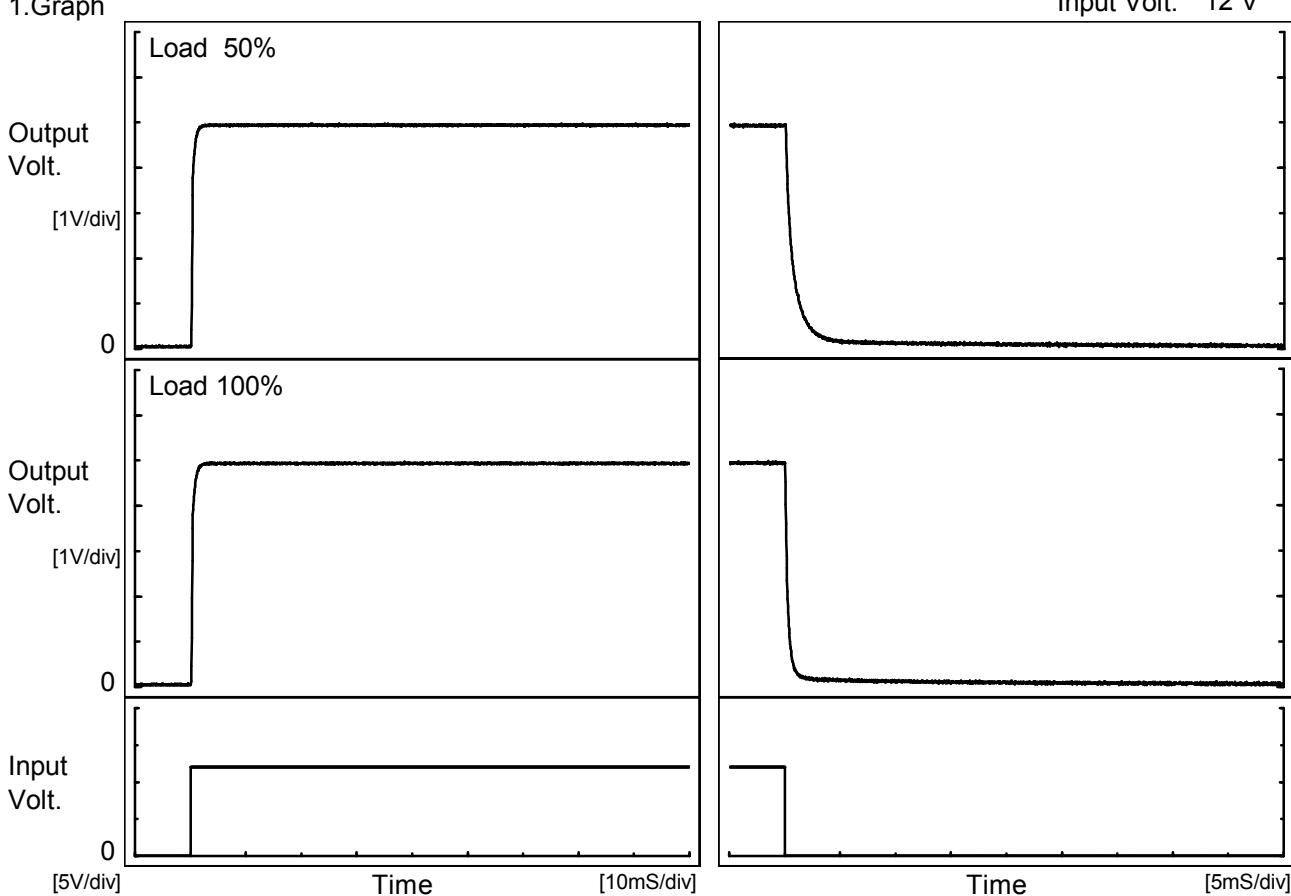
Time since start [H]	Output Voltage [V]
0.0	5.069
0.5	5.074
1.0	5.074
2.0	5.074
3.0	5.074
4.0	5.074
5.0	5.074
6.0	5.074
7.0	5.074
8.0	5.074

COSEL

Model	SUTS61205
Item	Rise and Fall Time
Object	+5V1.2A

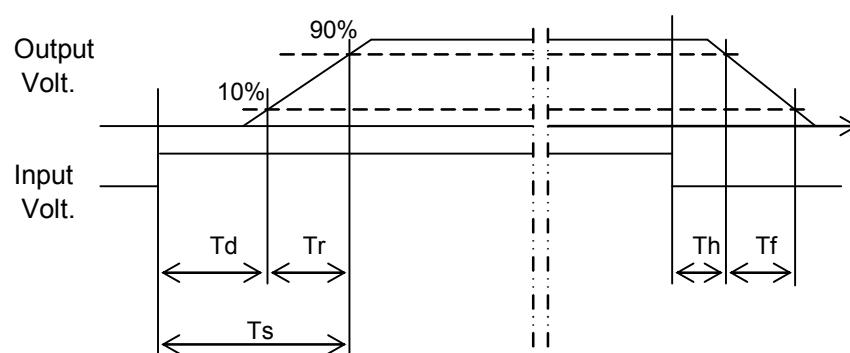
Temperature 25°C
Testing Circuitry Figure A

1. Graph



2. Values

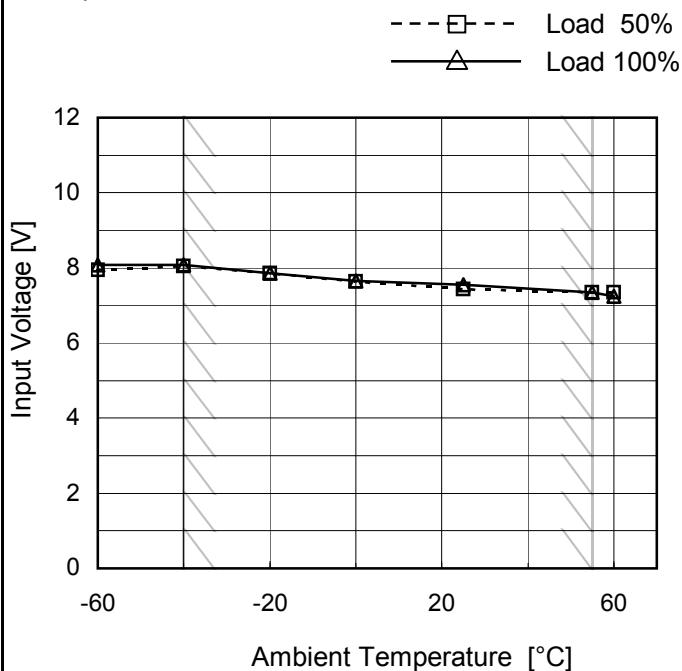
Load	Time	Td	Tr	Ts	Th	Tf	[mS]
50 %		0.3	0.7	1.0	0.1	2.0	
100 %		0.3	0.7	1.0	0.1	0.7	



Model	SUTS61205
Item	Minimum Input Voltage for Regulated Output Voltage
Object	+5V1.2A

Testing Circuitry Figure A

1. Graph



2. Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-60	8.0	8.1
-40	8.1	8.1
-20	7.9	7.9
0	7.7	7.7
25	7.5	7.6
55	7.4	7.4
60	7.4	7.3
--	-	-
--	-	-
--	-	-
--	-	-

Note: Slanted line shows the range of the rated ambient temperature.

Model	SUTS61205	Temperature Testing Circuitry 25°C Figure A																																																							
Item	Overshoot Protection																																																								
Object	+5V1.2A																																																								
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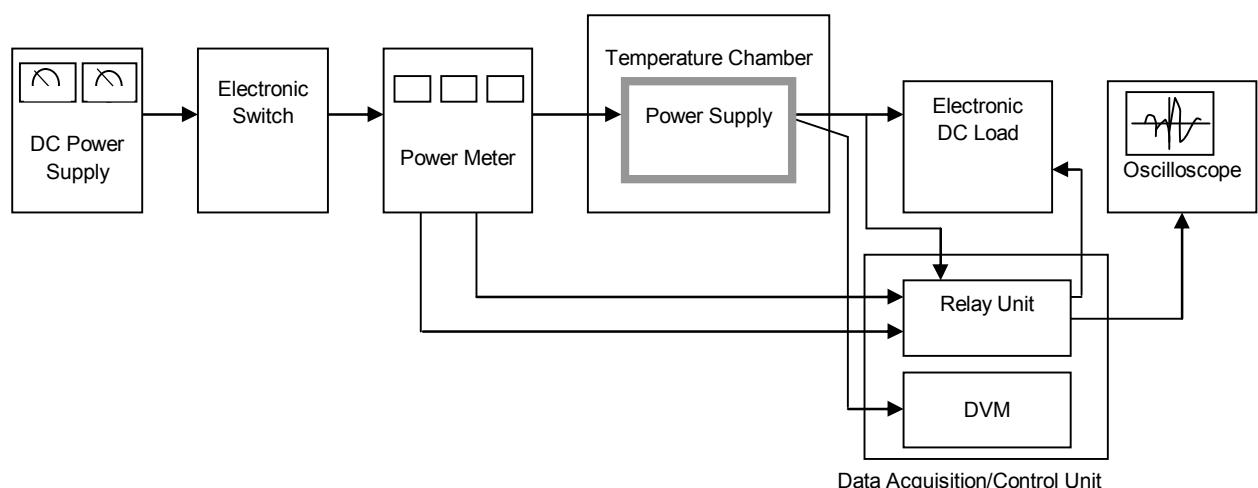


Figure A

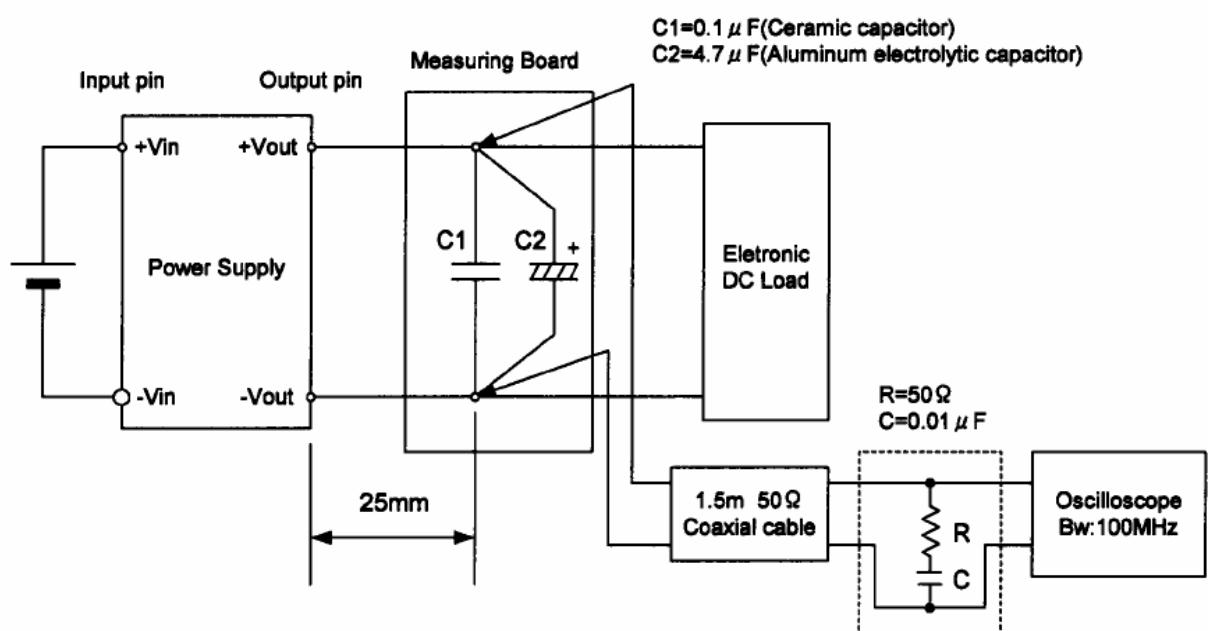


Figure B (Ripple and Ripple noise Characteristic)