

TEST DATA OF SUTS100512

Regulated DC Power Supply
February 6, 2009

Approved by : *Kazunari Asano*
Kazunari Asano Design Manager

Prepared by : *Sho Saito*
Sho Saito Design Engineer

COSEL CO.,LTD.

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Model		SUTS100512		Temperature 25°C																																																																																
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Item		Efficiency (by Load Current)		Testing Circuitry	Figure A	
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			Input Volt. 4.5[V]	Input Volt. 5[V]	Input Volt. 9[V]	
	0.00	-	-	-	-	
	0.18	80.3	78.9	78.4		
	0.36	84.3	84.4	83.3		
	0.54	85.9	86.3	86.2		
	0.72	86.1	86.6	86.9		
	0.90	85.6	86.1	87.6		
	0.99	84.8	85.8	87.8		
	--	-	-	-		
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<p>1.Graph</p> <div style="text-align: right;"> <p>---□--- Load 50%</p> <p>—△— Load 100%</p> </div> <p style="text-align: center;">Output Voltage [V]</p> <p style="text-align: center;">Input Voltage [V]</p> <p>Note: Slanted line shows the range of the rated input voltage.</p>		<p>2.Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Input Voltage [V]</th> <th colspan="2">Output Voltage [V]</th> </tr> <tr> <th>Load 50%</th> <th>Load 100%</th> </tr> </thead> <tbody> <tr><td>4.0</td><td>12.031</td><td>12.025</td></tr> <tr><td>4.5</td><td>12.031</td><td>12.026</td></tr> <tr><td>5.0</td><td>12.031</td><td>12.027</td></tr> <tr><td>6.0</td><td>12.031</td><td>12.027</td></tr> <tr><td>7.0</td><td>12.031</td><td>12.027</td></tr> <tr><td>8.0</td><td>12.031</td><td>12.027</td></tr> <tr><td>9.0</td><td>12.031</td><td>12.027</td></tr> <tr><td>10.0</td><td>12.031</td><td>12.027</td></tr> <tr><td>--</td><td>-</td><td>-</td></tr> </tbody> </table>		Input Voltage [V]	Output Voltage [V]		Load 50%	Load 100%	4.0	12.031	12.025	4.5	12.031	12.026	5.0	12.031	12.027	6.0	12.031	12.027	7.0	12.031	12.027	8.0	12.031	12.027	9.0	12.031	12.027	10.0	12.031	12.027	--	-	-
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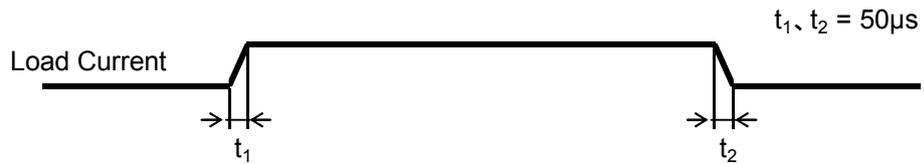


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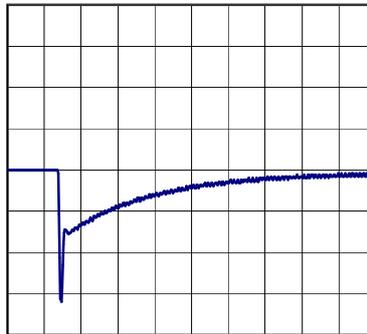
Model	SUTS100512	Temperature	25°C
Item	Dynamic Load Response	Testing Circuitry	Figure A
Object	+12V0.9A		

Input Volt. 5 V
 Cycle 100 mS



Min. Load (0A) \longleftrightarrow
 Load 100% (0.9A)

200mV/div



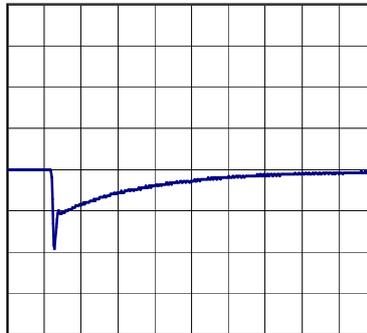
500µs/div



500µs/div

Min. Load (0A) \longleftrightarrow
 Load 50% (0.45A)

200mV/div



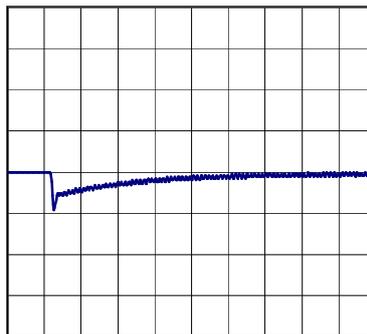
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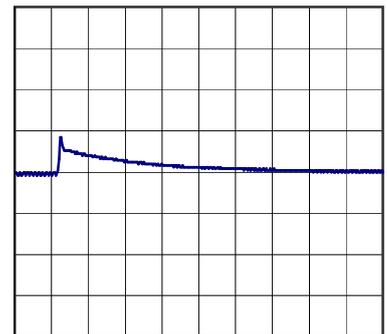
500µs/div

Load 50% (0.45A) \longleftrightarrow
 Load 100% (0.9A)

200mV/div



500µs/div



500µs/div

<p>Model SUTS100512</p>		<p>Temperature 25°C Testing Circuitry Figure B</p>																																						
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<p>Model SUTS100512</p>		<p>Temperature 25°C Testing Circuitry Figure B</p>																																						
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Object	+12V0.9A																																							
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COSEL																																								
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COSEL		
Model	SUTS100512	
Item	Output Voltage Accuracy	Testing Circuitry Figure A
Object	+12V0.9A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -40 - 55°C

Input Voltage : 4.5 - 9V

Load Current : 0 - 0.9A

* Output Voltage Accuracy = $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

* Output Voltage Accuracy (Ration) = $\frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$

2. Values

Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ration [%]
Maximum Voltage	55	5	0	12.039	±23	±0.2
Minimum Voltage	-40	4.5	0.9	11.993		



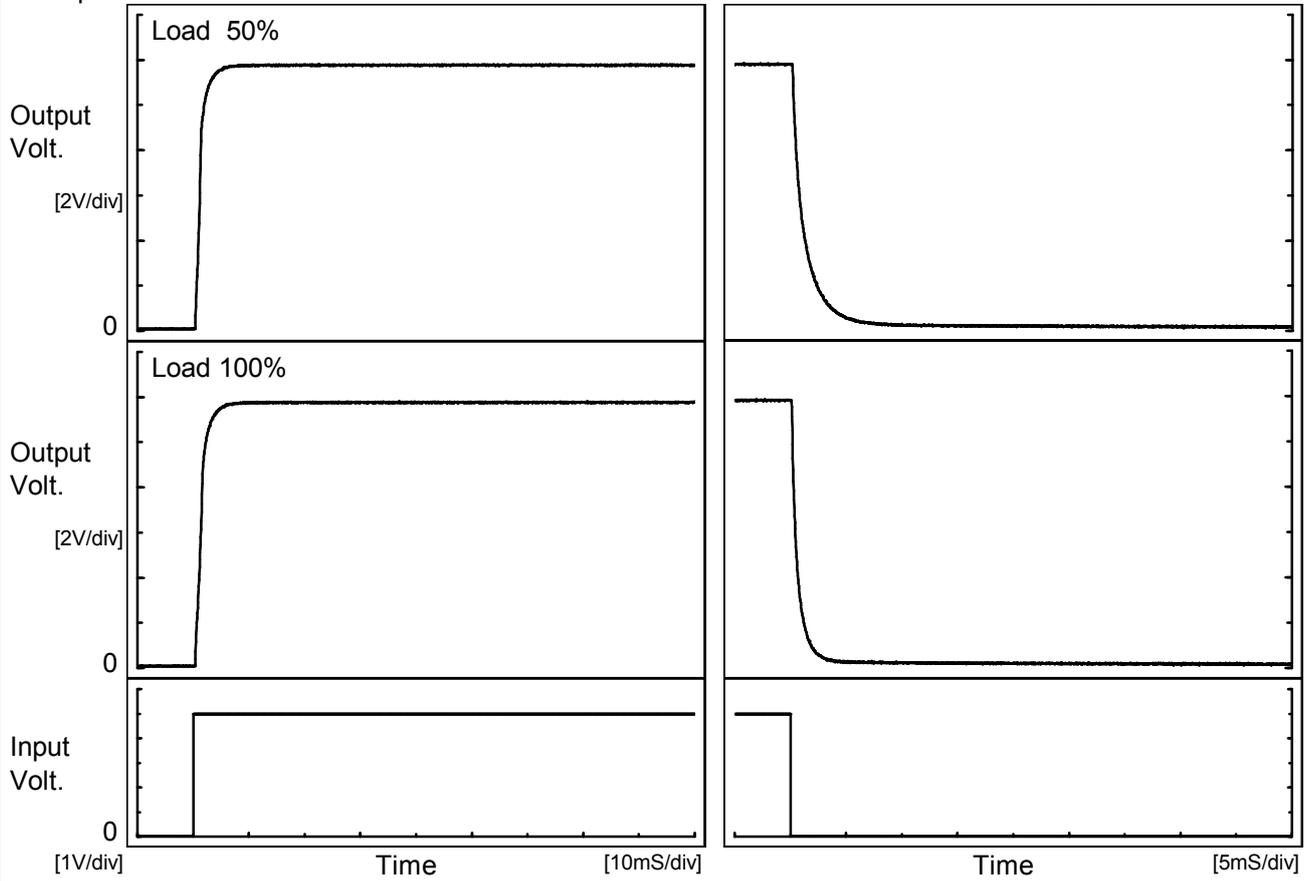
COSEL																								
Model	SUTS100512																							
Item	Time Lapse Drift	Temperature 25°C Testing Circuitry Figure A																						
Object	+12V0.9A																							
<p>1. Graph</p> <p style="text-align: center;">Time [H]</p> <p>Input Volt. 5V Load 100%</p>		<p>2. Values</p> <table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>12.023</td></tr> <tr><td>0.5</td><td>12.027</td></tr> <tr><td>1.0</td><td>12.026</td></tr> <tr><td>2.0</td><td>12.027</td></tr> <tr><td>3.0</td><td>12.027</td></tr> <tr><td>4.0</td><td>12.026</td></tr> <tr><td>5.0</td><td>12.027</td></tr> <tr><td>6.0</td><td>12.026</td></tr> <tr><td>7.0</td><td>12.026</td></tr> <tr><td>8.0</td><td>12.027</td></tr> </tbody> </table>	Time since start [H]	Output Voltage [V]	0.0	12.023	0.5	12.027	1.0	12.026	2.0	12.027	3.0	12.027	4.0	12.026	5.0	12.027	6.0	12.026	7.0	12.026	8.0	12.027
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COSEL			
Model	SUTS100512	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+12V0.9A		

1. Graph

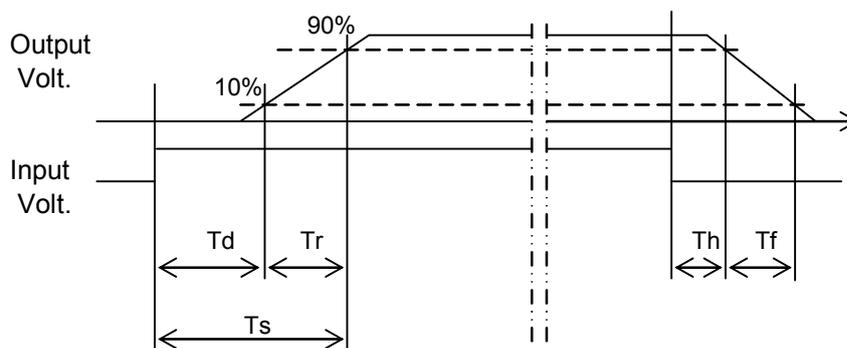
Input Volt. 5 V



2. Values

[mS]

Load \ Time	Td	Tr	Ts	Th	Tf
50 %	0.6	2.3	2.9	0.2	3.0
100 %	0.6	2.7	3.3	0.2	1.4





COSEL																																								
Model	SUTS100512																																							
Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A																																						
Object	+12V0.9A																																							
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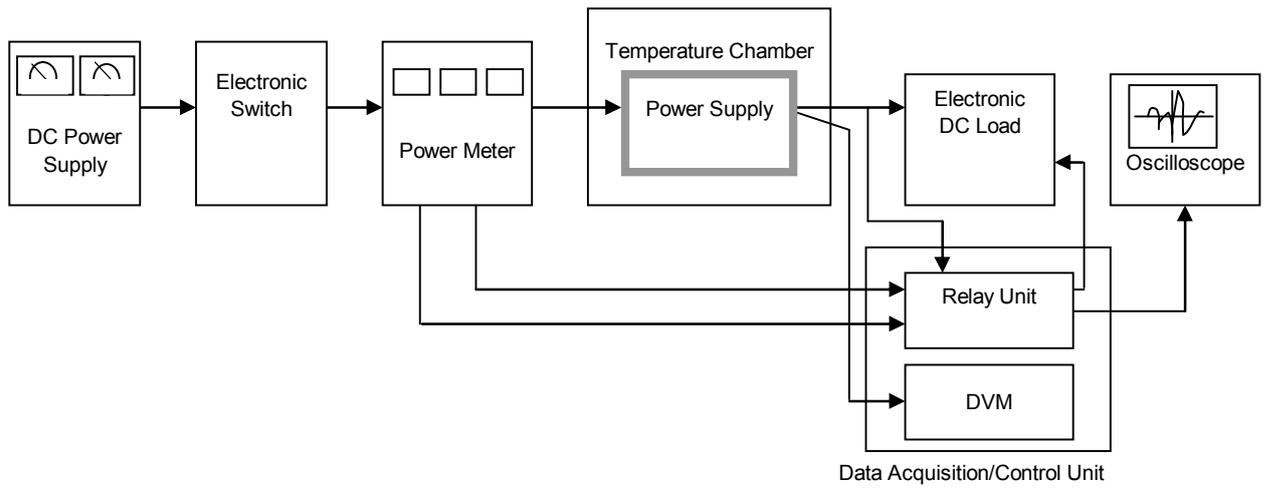


Figure A

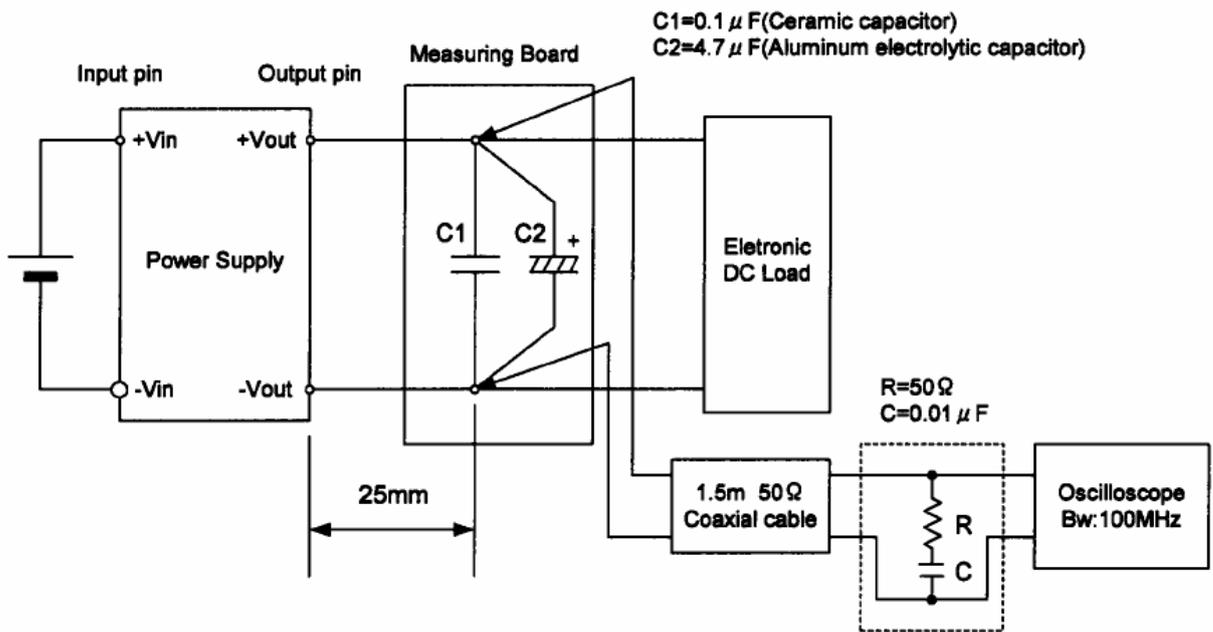


Figure B (Ripple and Ripple noise Characteristic)