

TEST DATA OF STMGFS804815

Regulated DC Power Supply
May 20, 2021

Approved by : Hironobu Shimizu Design Manager

Prepared by : Hikaru Inagaki Design Engineer

COSEL CO.,LTD.

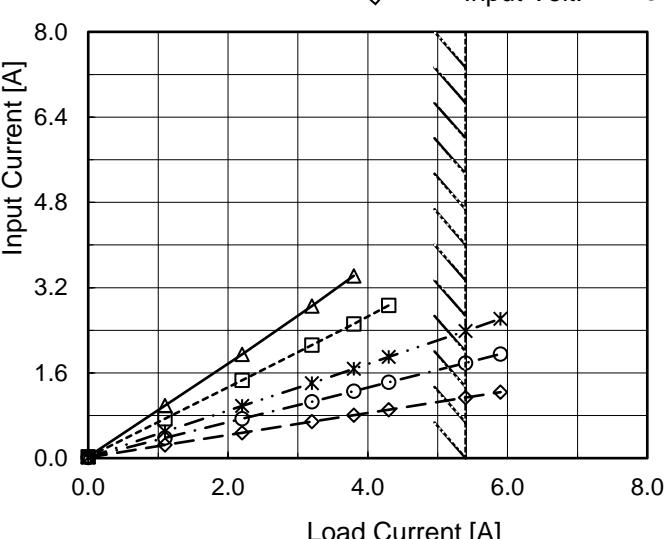


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Model	STMGFS804815	Temperature 25°C Testing Circuitry Figure A																																																																																
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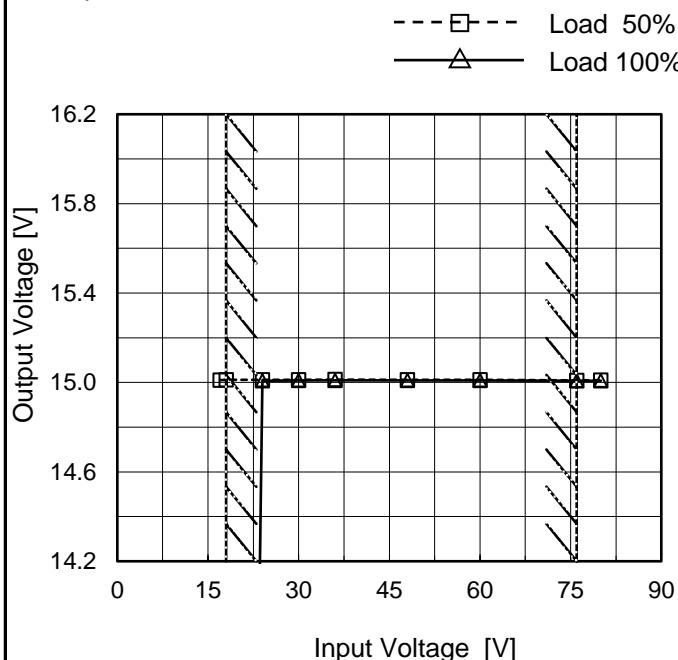
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Model	STMGFS804815
Item	Line Regulation
Object	+15V5.4A

Temperature 25°C
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated input voltage.

2. Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
17	15.011	-
18	15.011	-
24	15.012	-
30	15.013	15.009
36	15.013	15.010
48	15.012	15.009
60	15.011	15.009
76	15.009	15.008
80	15.010	15.008

※1 Maximum output current at minimum input Voltage is 70% of rated load current.

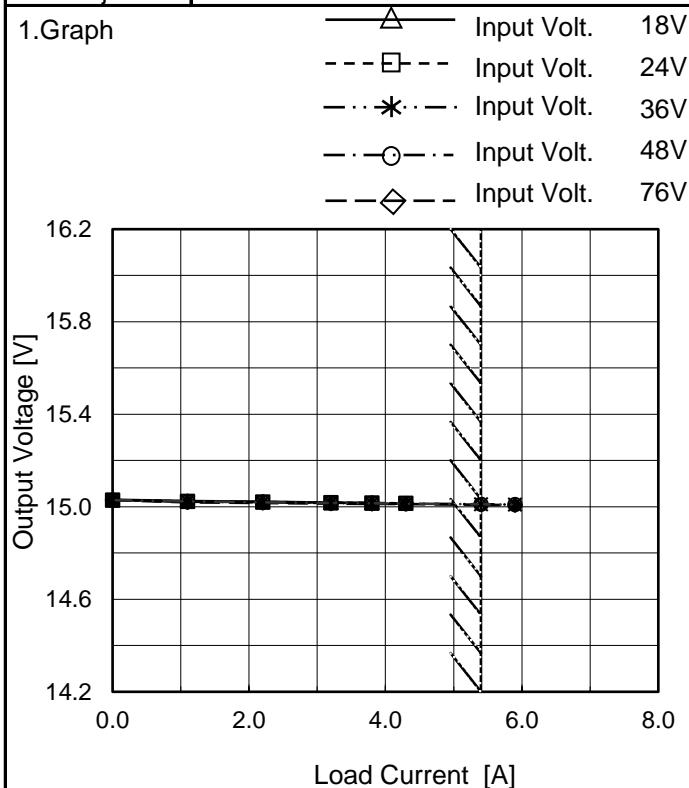
※2 Maximum output current at 24V input Voltage is 80% of rated load current.

Refer to instruction manuals for details of input derating.

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Model	STMGFS804815
Item	Load Regulation
Object	+15V5.4A

Temperature 25°C
Testing Circuitry Figure A



2.Values

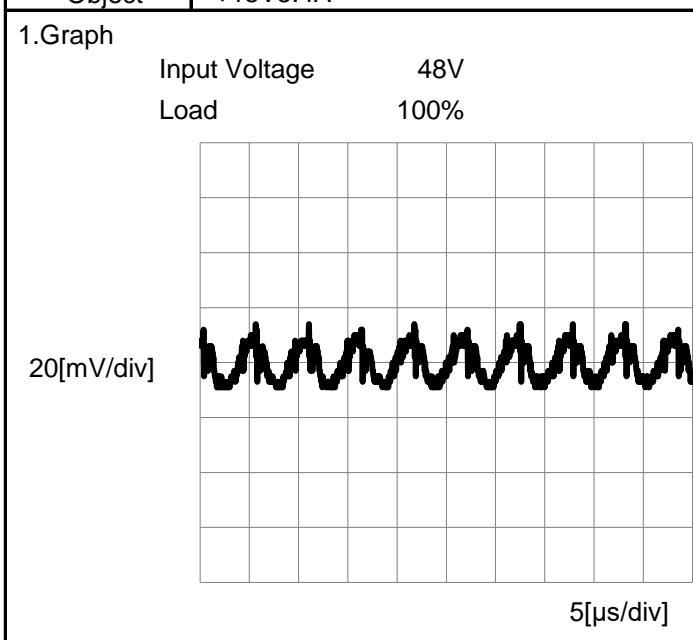
Load Current [A]	Output Voltage [V]				
	18[V]	24[V]	36[V]	48[V]	76[V]
0.0	15.030	15.029	15.028	15.029	15.029
1.1	15.025	15.024	15.023	15.022	15.019
2.2	15.022	15.021	15.020	15.019	15.016
3.2	15.018	15.018	15.017	15.017	15.014
3.8	15.016	15.016	15.016	15.015	15.012
4.3	-※1	15.014	15.014	15.014	15.012
5.4	-※1	-※2	15.011	15.010	15.009
5.9	-※1	-※2	15.009	15.009	15.007
--	-	-	-	-	-
--	-	-	-	-	-
--	-	-	-	-	-

※1 Maximum output current at minimum input Voltage is 70% of rated load current.

※2 Maximum output current at 18V input Voltage is 80% of rated load current.
Refer to instruction manuals for details of input derating.

Item	Ripple-Noise
Object	+15V5.4A

Temperature 25°C
Testing Circuitry Figure B

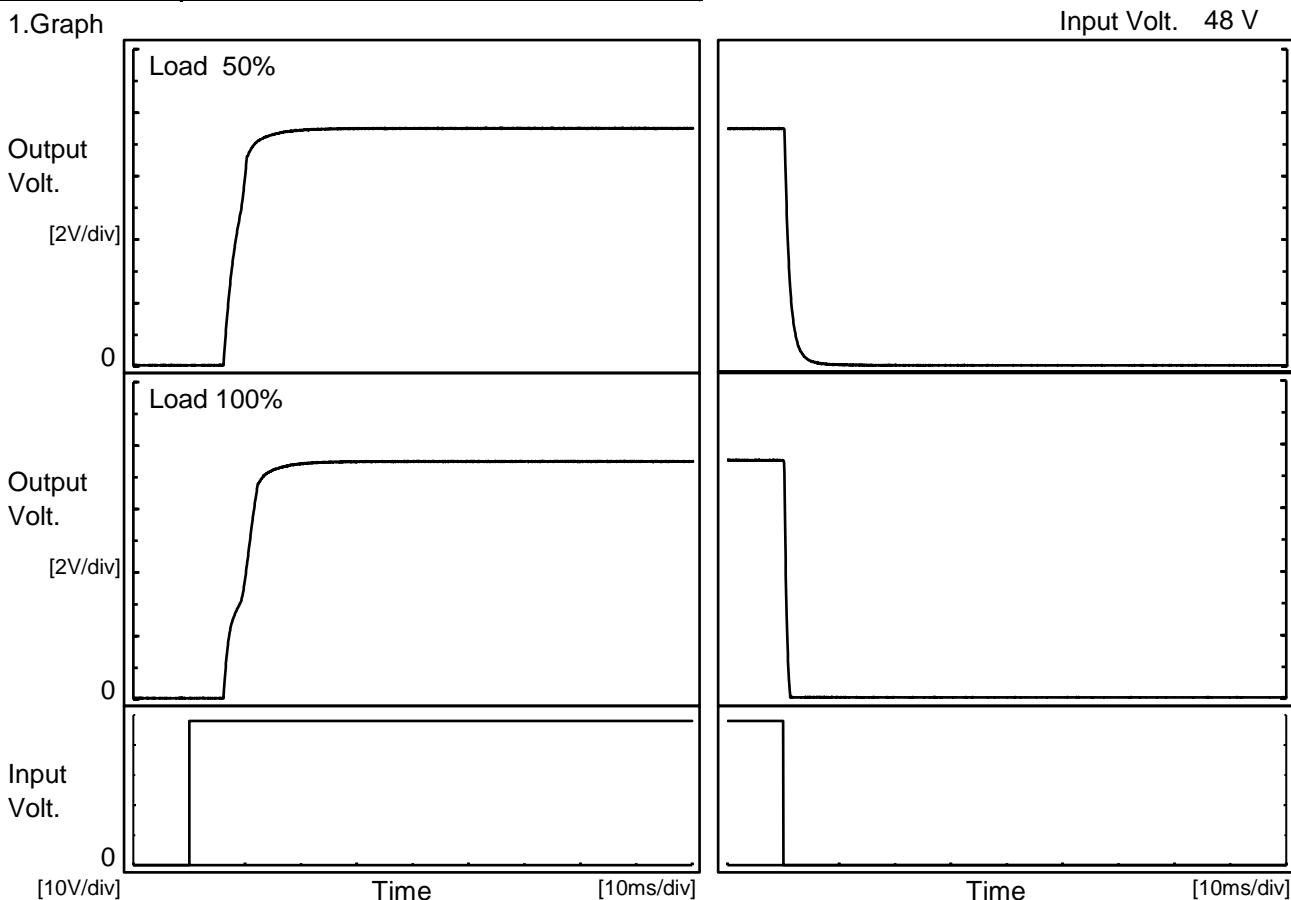


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Model	STMGFS804815
Item	Rise and Fall Time
Object	+15V5.4A

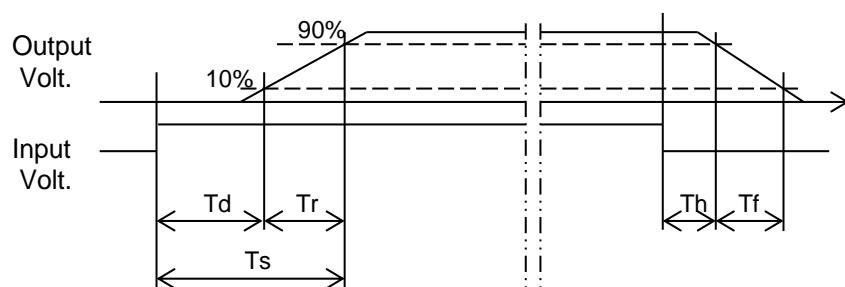
Temperature 25°C
Testing Circuitry Figure A

1. Graph



2. Values

Load	Time	Td	Tr	Ts	Th	Tf	[ms]
50 %		6.4	4.5	10.9	0.3	2.2	
100 %		6.4	5.9	12.3	0.2	0.7	





Model	STMGFS804815	Temperature	25°C																																																																																			
Item	Overcurrent Protection	Testing Circuitry	Figure A																																																																																			
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Note: Slanted line shows the range of the rated load current.

Intermittent operation occurs when overcurrent protection is activated.

※1 Maximum output current at minimum input Voltage is 70% of rated load current.

※2 Maximum output current at 24V input Voltage is 80% of rated load current.

Refer to instruction manuals for details of input derating.



Model	STMGFS804815	
Item	Ambient Temperature Drift	Testing Circuitry Figure A
Object	+15V5.4A	

1.Values

Load 100%

Ambient Temperature[°C]	Output Voltage [V]				
	Input Volt. 18V	Input Volt. 24V	Input Volt. 36V	Input Volt. 48V	Input Volt. 76V
-20	14.970	14.976	14.966	14.966	14.965
25	15.010	15.012	15.007	15.007	15.005
50	15.019	15.019	15.014	15.014	15.012

Note: In case of input Volt.18V, Load 70%. 24V, Load 80%.

Other case Load 100%.

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+15V5.4A	

1.Values

Ambient Temperature[°C]	Input Voltage [V]	
	Load 50%	Load 70%
-20	15.1	15.1
25	15.2	15.1
50	15.2	15.1

Item	Overvoltage Protection	Testing Circuitry Figure A
Object	+15V5.4A	

1.Values

Load 0%

Ambient Temperature[°C]	Operating Point [V]				
	Input Volt. 18V	Input Volt. 24V	Input Volt. 36V	Input Volt. 48V	Input Volt. 76V
-20	18.84	18.84	18.84	18.86	18.86
25	18.98	18.98	18.98	19.00	19.00
50	18.98	18.98	18.98	18.99	18.99

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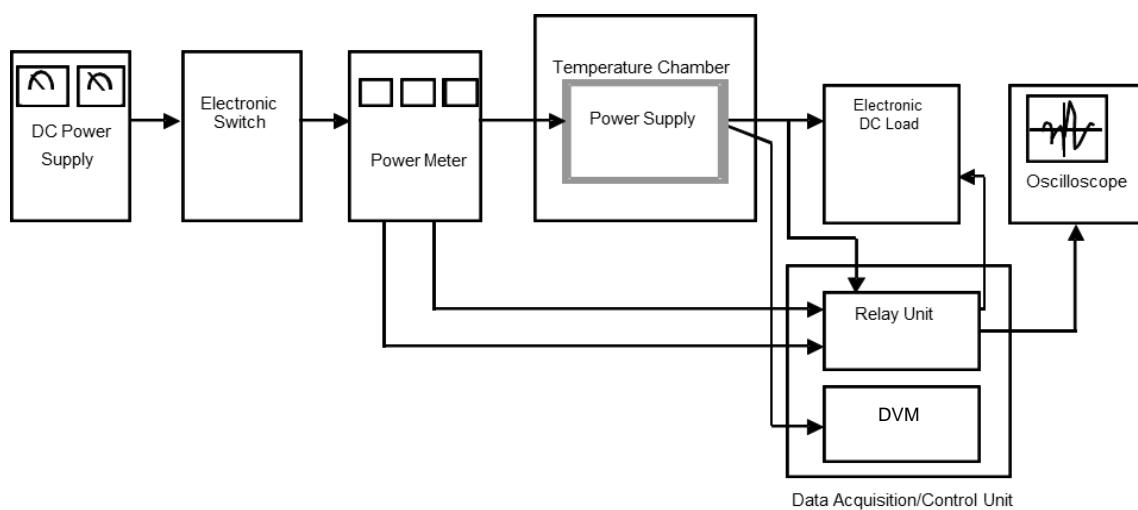


Figure A

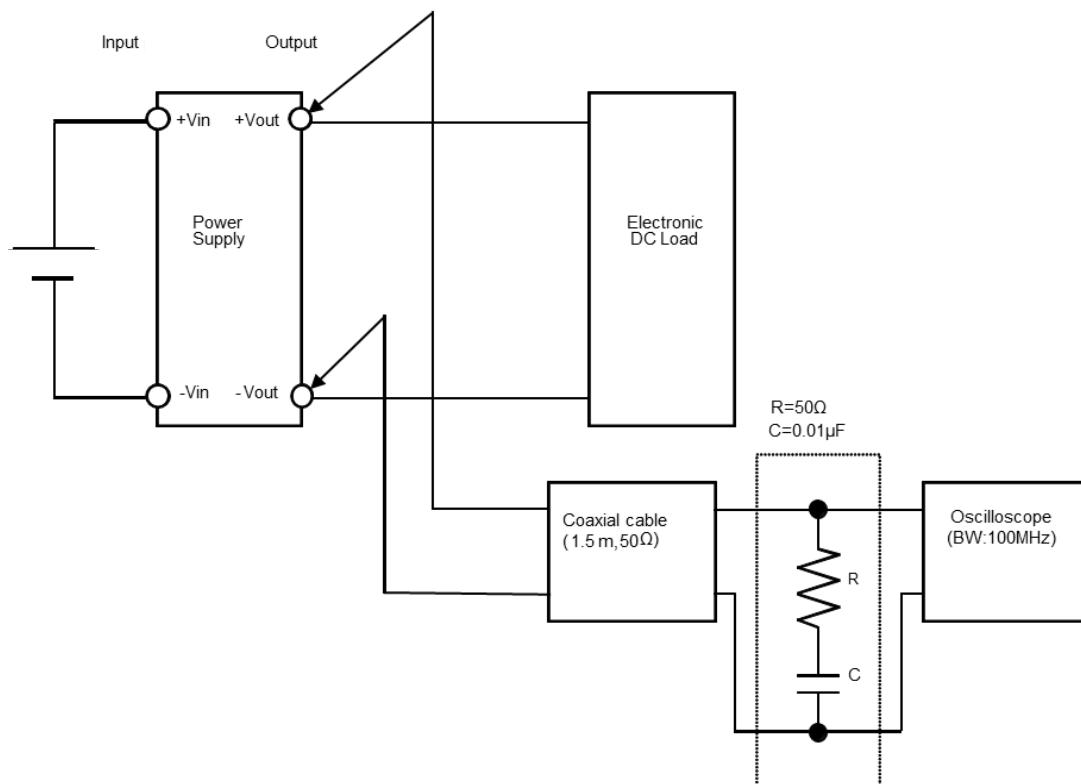


Figure B (Ripple noise Characteristic)