



TEST DATA OF LGA75A-15

Regulated DC Power Supply
May 20, 2011

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Kenji Shihoh Design Manager

Prepared by : Yosuke Saitou
Yosuke Saitou Design Engineer

COSEL CO.,LTD.



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Model	LGA75A-15	Temperature 25°C Testing Circuitry Figure A																																																				
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	<p>The graph shows efficiency increasing from approximately 74% at 1A to about 83% at 5A, then remaining relatively constant. The 85V curve is the highest, followed by 100V, and then 132V. A slanted line is drawn through the curves between 2A and 4A.</p> <table border="1"> <thead> <tr> <th>Load Current [A]</th> <th>Efficiency [85V] (%)</th> <th>Efficiency [100V] (%)</th> <th>Efficiency [132V] (%)</th> </tr> </thead> <tbody> <tr><td>1.0</td><td>74.9</td><td>74.8</td><td>72.5</td></tr> <tr><td>2.0</td><td>80.9</td><td>80.7</td><td>78.4</td></tr> <tr><td>3.0</td><td>82.6</td><td>82.7</td><td>81.6</td></tr> <tr><td>4.0</td><td>83.6</td><td>83.8</td><td>82.9</td></tr> <tr><td>5.0</td><td>83.4</td><td>84.0</td><td>84.6</td></tr> <tr><td>5.5</td><td>83.5</td><td>84.0</td><td>84.8</td></tr> </tbody> </table>	Load Current [A]	Efficiency [85V] (%)	Efficiency [100V] (%)	Efficiency [132V] (%)	1.0	74.9	74.8	72.5	2.0	80.9	80.7	78.4	3.0	82.6	82.7	81.6	4.0	83.6	83.8	82.9	5.0	83.4	84.0	84.6	5.5	83.5	84.0	84.8	2.Values	—																							
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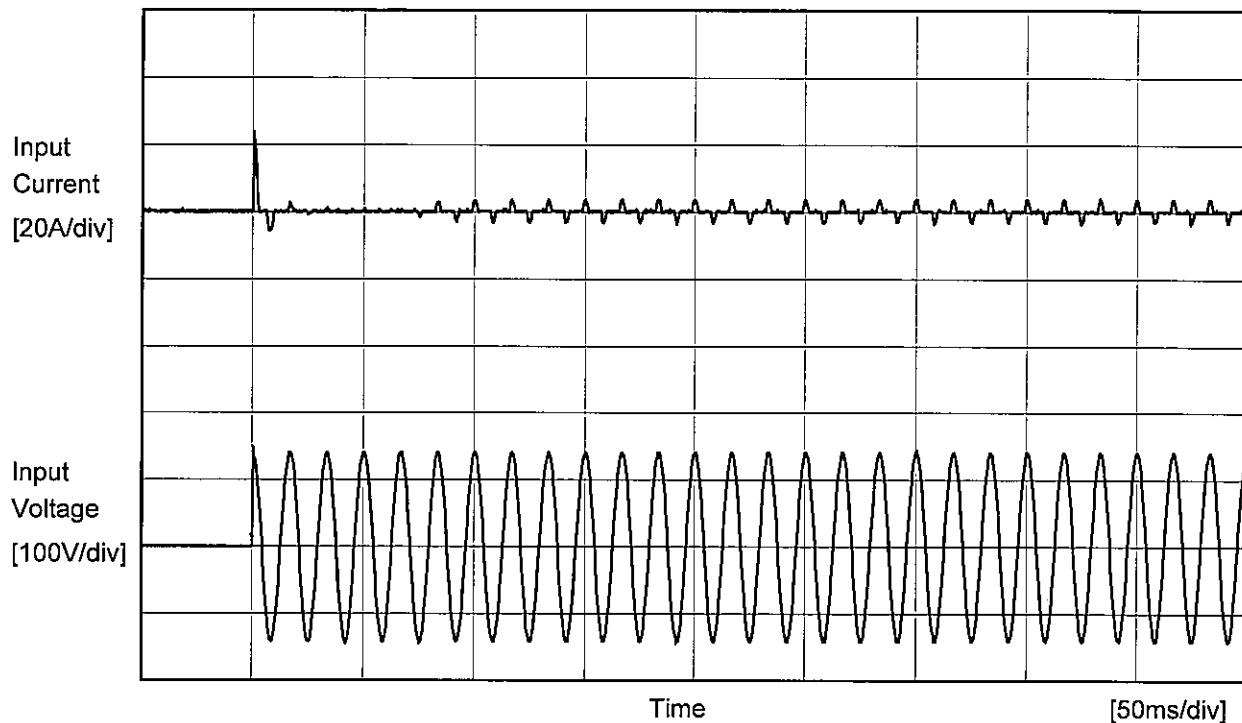
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Testing Circuitry	Figure A		
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1.6	0.557	0.534	0.495
2.4	0.575	0.553	0.513
3.2	0.582	0.560	0.524
4.0	0.589	0.564	0.529
4.8	0.598	0.570	0.532
5.0	0.598	0.573	0.530
5.5	0.601	0.577	0.533
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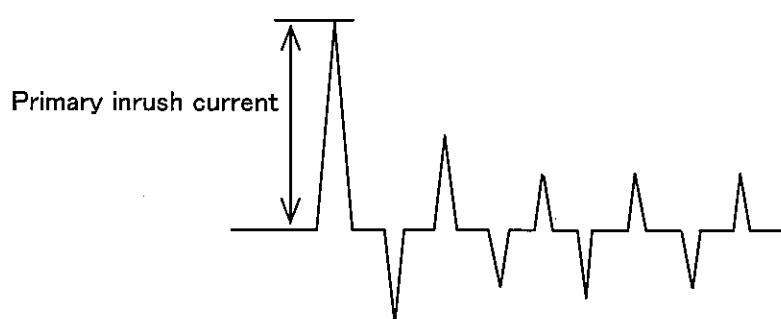
COSEL

Model	LGA75A-15
Item	Inrush Current
Object	_____

Temperature 25°C
Testing Circuitry Figure A

Input Voltage	100 V
Frequency	60 Hz
Load	100 %

Primary inrush current 23.7 A





Model	LGA75A-15		
Item	Leakage Current	Temperature	25°C
Object	Testing Circuitry Figure B		

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 100 [V]	Input Volt. 120 [V]	Input Volt. 132 [V]
(A)DEN-AN	0.08	0.10	0.12
(B)IEC60950-1	0.09	0.11	0.12

frequency 60Hz

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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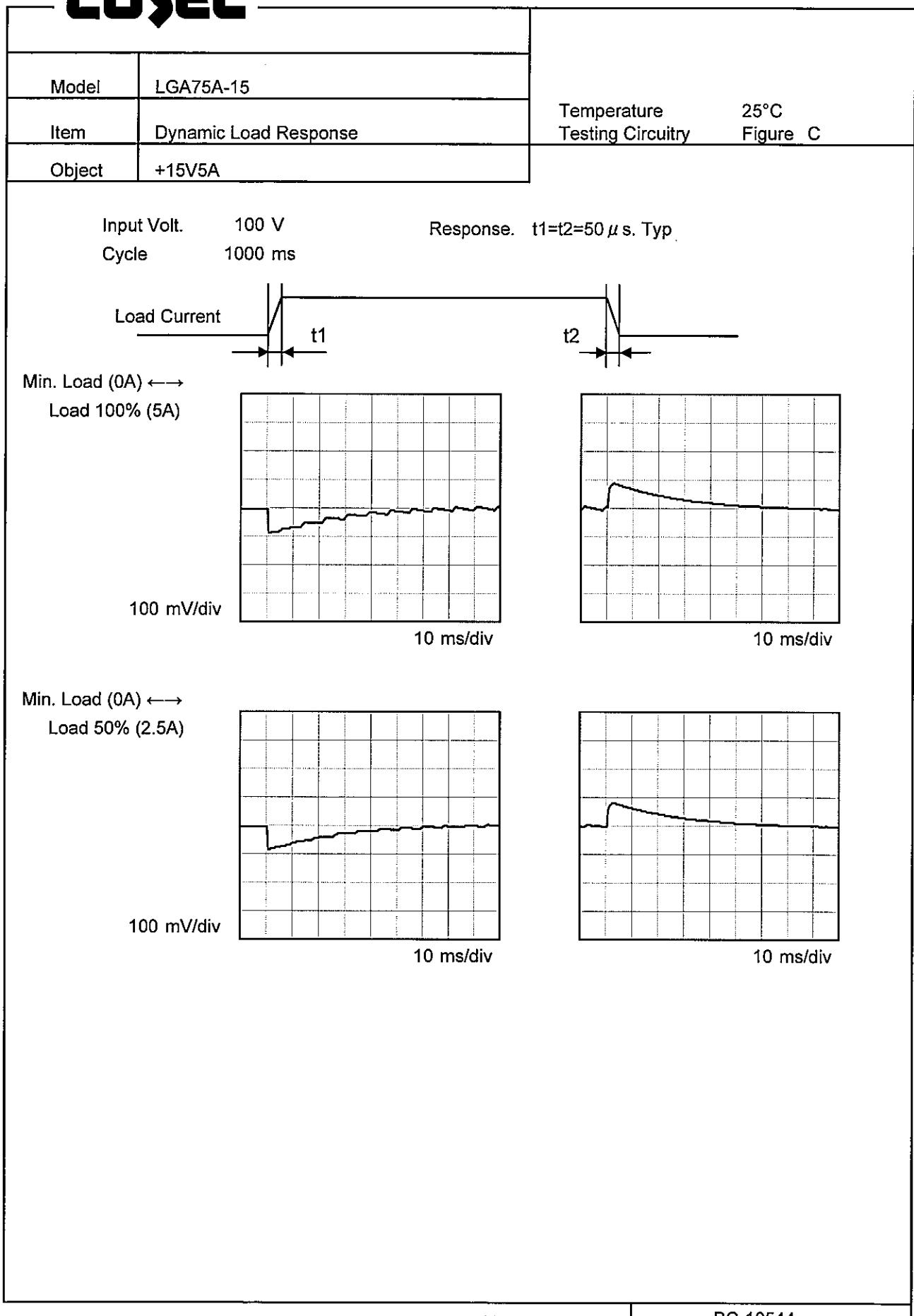
Model	LGA75A-15																																	
Item	Line Regulation	Temperature 25°C Testing Circuitry Figure A																																
Object	+15V5A																																	
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<p>Output Voltage [V]</p> <p>Input Voltage [V]</p> <p>Legend: ---□--- Load 50% —△— Load 100%</p>																																		
<p>Note: Slanted line shows the range of the rated input voltage.</p>																																		
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Model	LGA75A-15																																																							
Item	Load Regulation																																																							
Object	+15V5A																																																							
1.Graph	—△— Input Volt. 85V ---□--- Input Volt. 100V ---○--- Input Volt. 132V	Temperature 25°C Testing Circuitry Figure A	2.Values																																																					
<p>The graph plots Output Voltage [V] on the Y-axis (from 14.70 to 15.40) against Load Current [A] on the X-axis (from 0 to 6). Three data series are shown for different input voltages: 85V (triangles), 100V (squares), and 132V (circles). All series show a constant output voltage of approximately 15.10V up to a load current of about 4.5A, after which the output voltage drops sharply. A slanted line on the graph indicates the range of the rated load current.</p>			<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Output Voltage [V]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr> <td>0.0</td><td>15.094</td><td>15.094</td><td>15.094</td></tr> <tr> <td>0.8</td><td>15.093</td><td>15.093</td><td>15.093</td></tr> <tr> <td>1.6</td><td>15.092</td><td>15.093</td><td>15.093</td></tr> <tr> <td>2.4</td><td>15.092</td><td>15.093</td><td>15.093</td></tr> <tr> <td>3.2</td><td>15.092</td><td>15.092</td><td>15.093</td></tr> <tr> <td>4.0</td><td>15.092</td><td>15.092</td><td>15.093</td></tr> <tr> <td>4.8</td><td>15.092</td><td>15.092</td><td>15.093</td></tr> <tr> <td>5.0</td><td>15.092</td><td>15.092</td><td>15.093</td></tr> <tr> <td>5.5</td><td>15.092</td><td>15.093</td><td>15.093</td></tr> <tr> <td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr> <td>--</td><td>-</td><td>-</td><td>-</td></tr> </tbody> </table>			Load Current [A]	Output Voltage [V]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0.0	15.094	15.094	15.094	0.8	15.093	15.093	15.093	1.6	15.092	15.093	15.093	2.4	15.092	15.093	15.093	3.2	15.092	15.092	15.093	4.0	15.092	15.092	15.093	4.8	15.092	15.092	15.093	5.0	15.092	15.092	15.093	5.5	15.092	15.093	15.093	--	-	-	-	--	-	-	-
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Note: Slanted line shows the range of the rated load current.

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COSEL

Model	LGA75A-15		Temperature 25°C Testing Circuitry Figure C																																						
Item	Ripple Voltage (by Load Current)																																								
Object	+15V5A																																								
1. Graph																																									
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Load Current [A]	Ripple Voltage [mV]																																								
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COSEL

Model	LGA75A-15																																							
Item	Ripple-Noise	Temperature 25°C Testing Circuitry Figure C																																						
Object	+15V5A																																							
1. Graph																																								
<p>Graph showing Ripple-Noise [mV] vs Load Current [A]. The Y-axis ranges from 0 to 200 mV, and the X-axis ranges from 0 to 6 A. Two data series are plotted: Input Volt. 85V (solid triangles) and Input Volt. 132V (open circles). A slanted line indicates the rated load current range.</p> <table border="1"> <caption>Data extracted from Graph</caption> <thead> <tr> <th>Load Current [A]</th> <th>Ripple-Noise [mV] (Input Volt. 85V)</th> <th>Ripple-Noise [mV] (Input Volt. 132V)</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>30</td><td>80</td></tr> <tr><td>0.8</td><td>30</td><td>80</td></tr> <tr><td>1.6</td><td>35</td><td>80</td></tr> <tr><td>2.4</td><td>40</td><td>80</td></tr> <tr><td>3.2</td><td>40</td><td>80</td></tr> <tr><td>4.0</td><td>40</td><td>80</td></tr> <tr><td>4.8</td><td>40</td><td>80</td></tr> <tr><td>5.0</td><td>40</td><td>80</td></tr> <tr><td>5.5</td><td>40</td><td>90</td></tr> </tbody> </table>			Load Current [A]	Ripple-Noise [mV] (Input Volt. 85V)	Ripple-Noise [mV] (Input Volt. 132V)	0.0	30	80	0.8	30	80	1.6	35	80	2.4	40	80	3.2	40	80	4.0	40	80	4.8	40	80	5.0	40	80	5.5	40	90								
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<p>Measured by 20 MHz Oscilloscope. Ripple-Noise is shown as p-p in the figure below. Note: Slanted line shows the range of the rated load current.</p> <p>T1: Due to AC Input Line T2: Due to Switching</p> <p>Fig. Complex Ripple Wave Form</p>																																								

COSEL

Model	LGA75A-15	Testing Circuitry Figure C																											
Item	Ripple Voltage (by Ambient Temp.)																												
Object	+15V5A																												
1. Graph		2. Values																											
<p>Input Volt. 100V Input Load. 100%</p> <p>Measured by 20 MHz Oscilloscope. Note: Slanted line shows the range of the rated ambient temperature.</p>																													
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COSEL

Model	LGA75A-15																																																					
Item	Ambient Temperature Drift																																																					
Object	+15V5A																																																					
1.Graph	<p>Output Voltage [V]</p> <p>Ambient Temperature [°C]</p> <p>Load 100%</p> <p>Legend:</p> <ul style="list-style-type: none"> Input Volt. 85V Input Volt. 100V Input Volt. 132V 																																																					
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Note: Slanted line shows the range of the rated ambient temperature.



Model	LGA75A-15	Testing Circuitry Figure A
Item	Output Voltage Accuracy	
Object	+15V5A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10 - 50°C

Input Voltage : 85 - 132V

Load Current : 0 - 5A

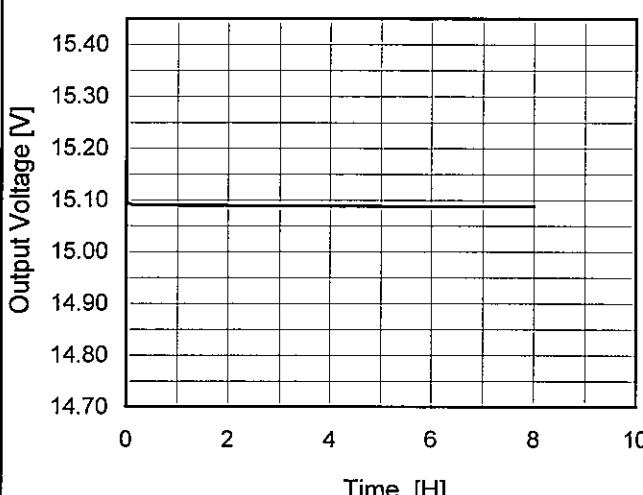
* Output Voltage Accuracy = $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

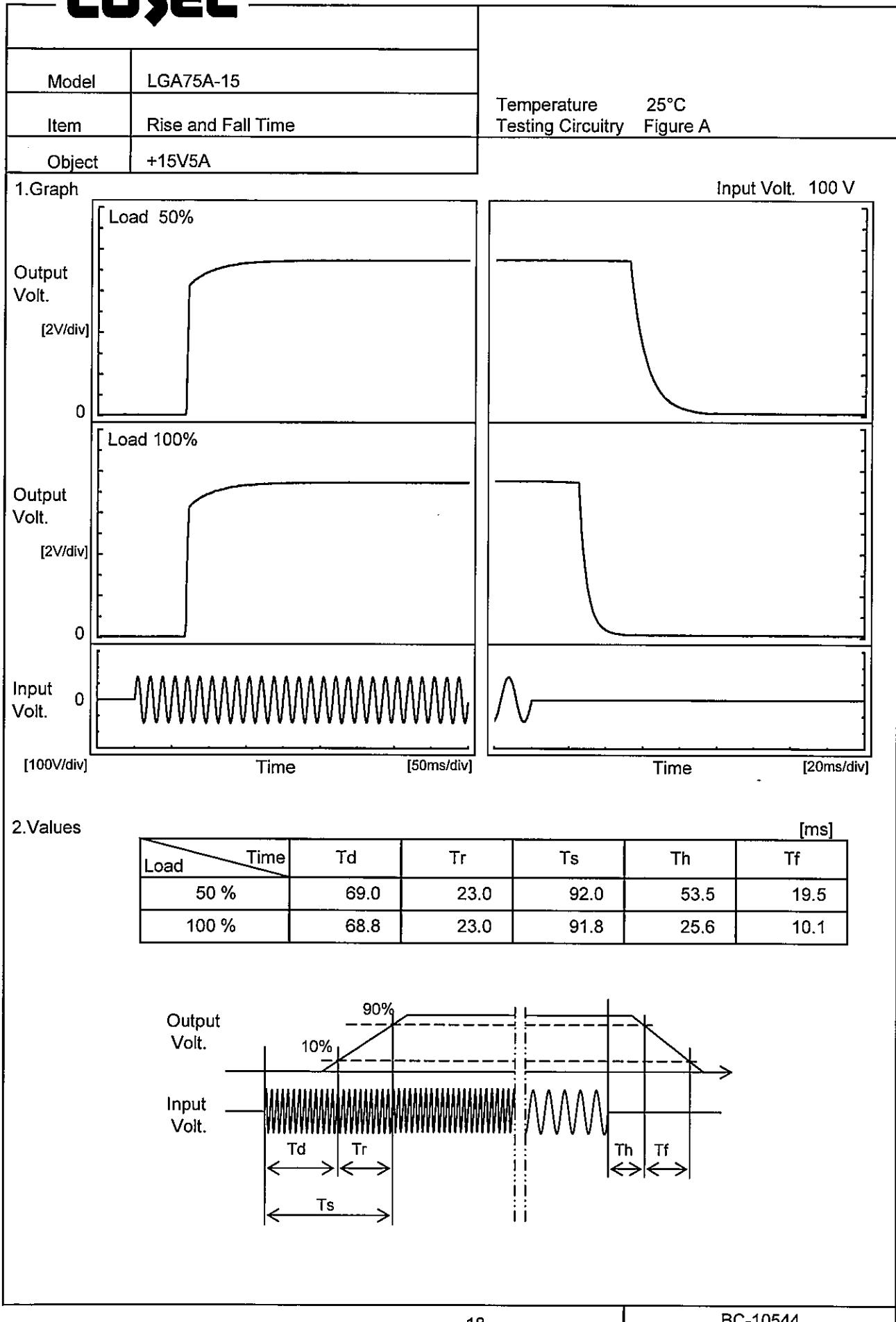
$$\text{* Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

2. Values

Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ration [%]
Maximum Voltage	25	85	0	15.096	± 6	± 0.1
Minimum Voltage	50	85	5	15.085		

COSEL

Model	LGA75A-15	Temperature Testing Circuitry 25°C Figure A																						
Item	Time Lapse Drift																							
Object	+15V5A																							
1. Graph		2. Values																						
 <p>Output Voltage [V]</p> <p>Time [H]</p> <p>Input Volt. 100V Load 100%</p>		<table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>15.101</td></tr> <tr><td>0.5</td><td>15.090</td></tr> <tr><td>1.0</td><td>15.090</td></tr> <tr><td>2.0</td><td>15.090</td></tr> <tr><td>3.0</td><td>15.090</td></tr> <tr><td>4.0</td><td>15.089</td></tr> <tr><td>5.0</td><td>15.089</td></tr> <tr><td>6.0</td><td>15.088</td></tr> <tr><td>7.0</td><td>15.089</td></tr> <tr><td>8.0</td><td>15.089</td></tr> </tbody> </table>	Time since start [H]	Output Voltage [V]	0.0	15.101	0.5	15.090	1.0	15.090	2.0	15.090	3.0	15.090	4.0	15.089	5.0	15.089	6.0	15.088	7.0	15.089	8.0	15.089
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7.0	15.089																							
8.0	15.089																							

COSEL

COSEL

Model	LGA75A-15	Temperature 25°C Testing Circuitry Figure A																															
Item	Hold-Up Time																																
Object	+15V5A																																
1. Graph		2. Values																															
<p>The graph illustrates the relationship between input voltage and hold-up time for two load conditions: 50% load (dashed line with squares) and 100% load (solid line with triangles). The y-axis is logarithmic, ranging from 1 to 1000 ms. The x-axis represents input voltage from 70 to 150 V. Both curves show an increase in hold-up time as the input voltage drops below the rated range (approximately 90-100 V). A slanted line on the graph indicates the range of the rated input voltage.</p>																																	
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Input Voltage [V]	Hold-Up Time [ms]																																
	Load 50%	Load 100%																															
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80	26	12																															
85	32	15																															
90	39	18																															
100	53	26																															
110	70	34																															
120	87	43																															
132	112	55																															
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<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy. Note: Slanted line shows the range of the rated input voltage.</p>																																	

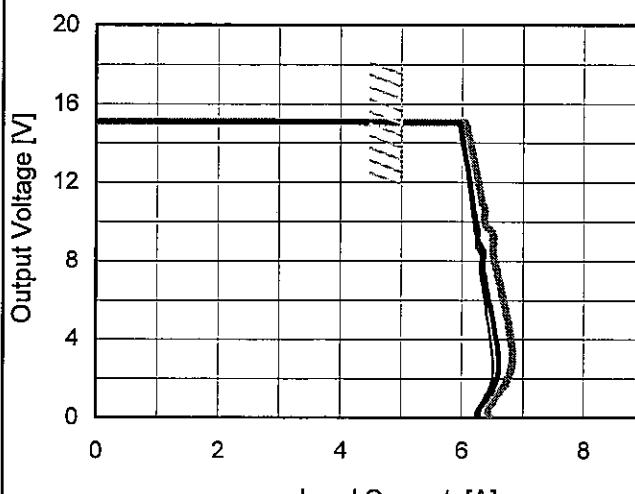
COSEL

Model	LGA75A-15																																																					
Item	Instantaneous Interruption Compensation																																																					
Object	+15V5A																																																					
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<p>—△— Input Volt. 85V - - -□- - Input Volt. 100V - - ○- - Input Volt. 132V</p> <table border="1"> <caption>Data points estimated from Graph 1</caption> <thead> <tr> <th>Load Current [A]</th> <th>85V [ms]</th> <th>100V [ms]</th> <th>132V [ms]</th> </tr> </thead> <tbody> <tr><td>0.8</td><td>98</td><td>161</td><td>329</td></tr> <tr><td>1.6</td><td>52</td><td>85</td><td>173</td></tr> <tr><td>2.4</td><td>31</td><td>56</td><td>118</td></tr> <tr><td>3.2</td><td>22</td><td>40</td><td>89</td></tr> <tr><td>4.0</td><td>19</td><td>31</td><td>71</td></tr> <tr><td>4.8</td><td>14</td><td>27</td><td>57</td></tr> <tr><td>5.0</td><td>14</td><td>23</td><td>55</td></tr> <tr><td>5.5</td><td>13</td><td>23</td><td>48</td></tr> </tbody> </table>				Load Current [A]	85V [ms]	100V [ms]	132V [ms]	0.8	98	161	329	1.6	52	85	173	2.4	31	56	118	3.2	22	40	89	4.0	19	31	71	4.8	14	27	57	5.0	14	23	55	5.5	13	23	48															
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COSEL

Model	LGA75A-15																																							
Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A																																						
Object	+15V5A																																							
1.Graph																																								
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COSEL

Model	LGA75A-15	Temperature	25°C
Item	Overcurrent Protection	Testing Circuitry	Figure A
Object	+15V5A		
1. Graph	<p>— Input Volt. 85V — Input Volt. 100V — Input Volt. 132V</p> 	2.Values	
Output Voltage [V]	Load Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
15.0	5.99	5.96	6.05
14.3	6.01	6.00	6.11
13.5	5.99	5.96	6.05
12.0	6.11	6.12	6.26
10.5	6.16	6.19	6.37
9.0	6.23	6.24	6.53
7.5	6.29	6.35	6.57
6.0	6.37	6.43	6.68
4.5	6.44	6.53	6.78
3.0	6.50	6.60	6.83
1.5	6.46	6.53	6.68
0.0	6.22	6.28	6.48

Note: Slanted line shows the range of the rated load current.



Model	LGA75A-15																																							
Item	Overvoltage Protection																																							
Object	+15V5A																																							
1.Graph																																								
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Note: Slanted line shows the range of the rated ambient temperature.

COSEL

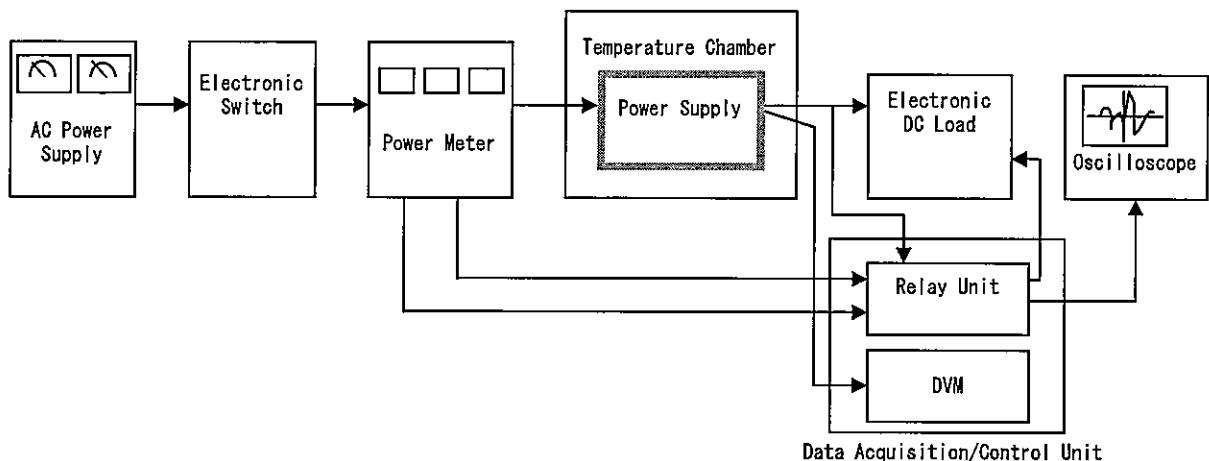


Figure A

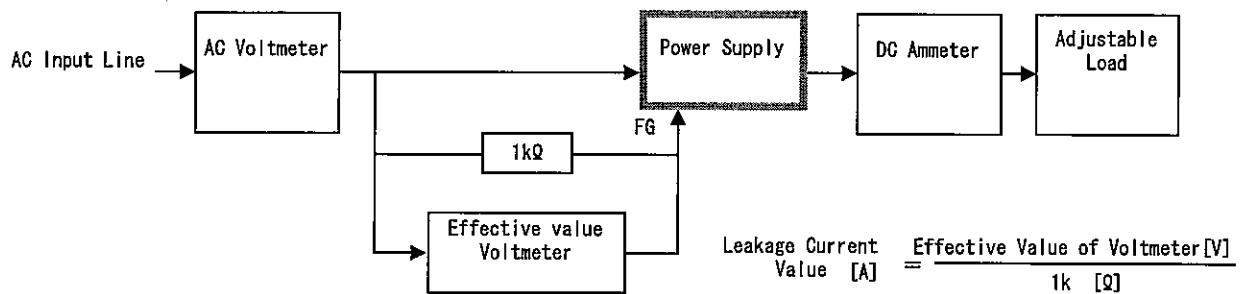


Figure B (DEN-AN)

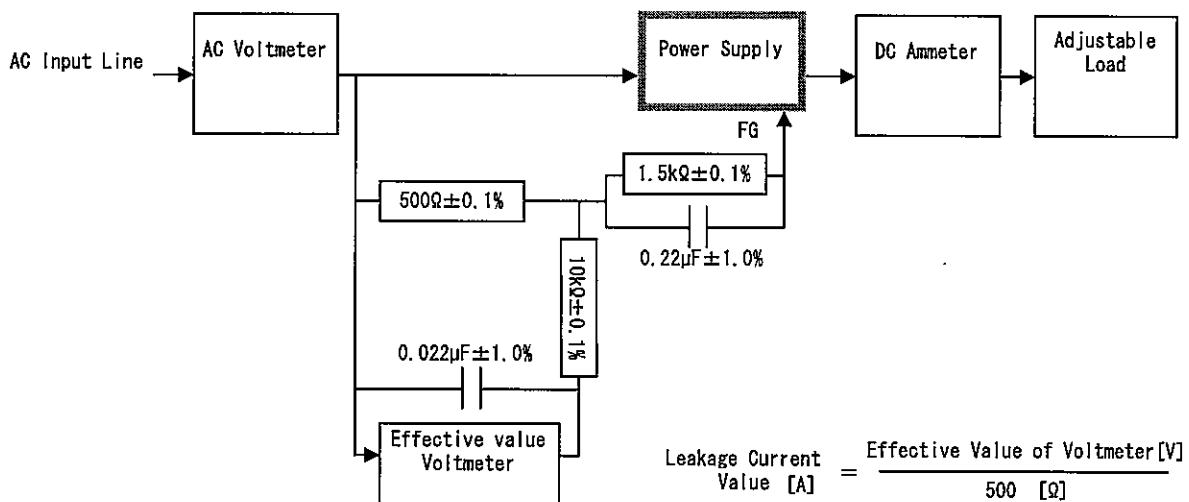


Figure B (IEC60950-1)

COSEL

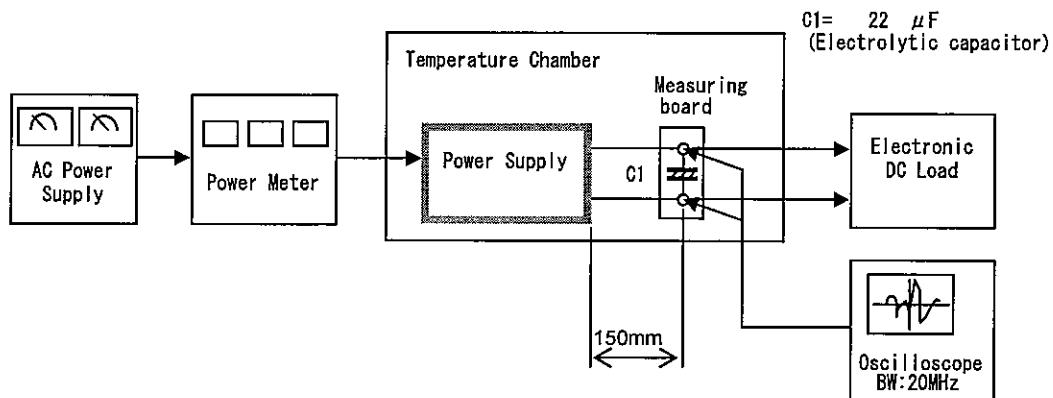


Figure C