



TEST DATA OF LGA150A-48

Regulated DC Power Supply
May 20, 2011

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Hironobu Shimizu Design Engineer

COSEL CO.,LTD.

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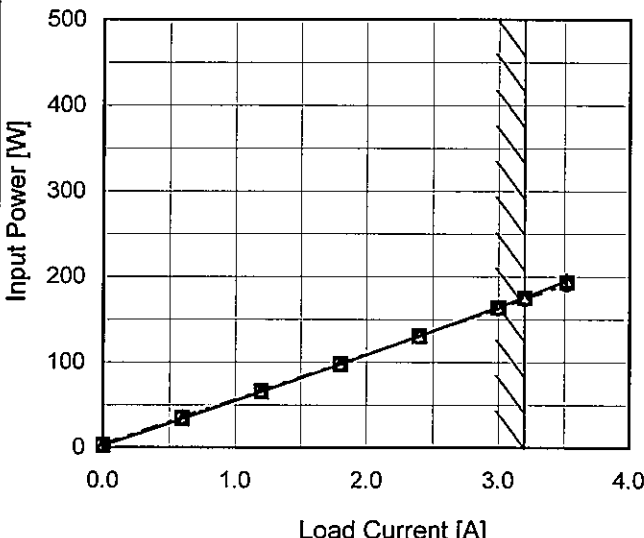
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Model		LGA150A-48																																																				
Item		Input Current (by Load Current)																																																				
Object																																																						
1.Graph		2.Values																																																				
<div><div><div>—△—</div><div>---□---</div><div>-·-○-·-</div></div><div>Input Volt. 85V</div><div>Input Volt. 100V</div><div>Input Volt. 132V</div></div> <p>Input Current [A]</p> <p>Load Current [A]</p> <p>Note: Slanted line shows the range of the rated load current.</p>		<table><tr><th rowspan="2">Load Current [A]</th><th colspan="3">Input Current [A]</th></tr><tr><th>Input Volt. 85[V]</th><th>Input Volt. 100[V]</th><th>Input Volt. 132[V]</th></tr><tr><td>0.00</td><td>0.089</td><td>0.092</td><td>0.095</td></tr><tr><td>0.60</td><td>0.878</td><td>0.803</td><td>0.657</td></tr><tr><td>1.20</td><td>1.517</td><td>1.395</td><td>1.128</td></tr><tr><td>1.80</td><td>2.141</td><td>1.924</td><td>1.615</td></tr><tr><td>2.40</td><td>2.754</td><td>2.451</td><td>2.037</td></tr><tr><td>3.00</td><td>3.345</td><td>2.952</td><td>2.471</td></tr><tr><td>3.20</td><td>3.558</td><td>3.131</td><td>2.610</td></tr><tr><td>3.52</td><td>3.890</td><td>3.416</td><td>2.819</td></tr><tr><td>--</td><td>-</td><td>-</td><td>-</td></tr><tr><td>--</td><td>-</td><td>-</td><td>-</td></tr><tr><td>--</td><td>-</td><td>-</td><td>-</td></tr></table>		Load Current [A]	Input Current [A]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0.00	0.089	0.092	0.095	0.60	0.878	0.803	0.657	1.20	1.517	1.395	1.128	1.80	2.141	1.924	1.615	2.40	2.754	2.451	2.037	3.00	3.345	2.952	2.471	3.20	3.558	3.131	2.610	3.52	3.890	3.416	2.819	--	-	-	-	--	-	-	-	--	-	-	-
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Model		LGA150A-48	
Item		Input Power (by Load Current)	
Object			
1.Graph			
		—△—	Input Volt. 85V
		---□---	Input Volt. 100V
		---○---	Input Volt. 132V
			
Note: Slanted line shows the range of the rated load current.			

Temperature		25°C	
Testing Circuitry		Figure A	
2.Values			
Load Current [A]	Input Power [W]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	2.4	2.9	3.6
0.60	33.6	34.2	35.6
1.20	65.7	65.6	66.6
1.80	97.8	97.8	98.4
2.40	131.1	130.5	130.5
3.00	164.8	163.5	162.9
3.20	176.4	174.6	173.7
3.52	196.0	192.9	191.4
--	-	-	-
--	-	-	-
--	-	-	-

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COSEL

Model

LGA150A-48

Item

Efficiency (by Input Voltage)

Object

1.Graph

□

Load 50%

△

Load 100%

Efficiency [%]

90

82

74

66

58

50

42

34

70

90

110

130

150

Input Voltage [V]

Note: Slanted line shows the range of the rated input voltage.

Temperature

25°C

Testing Circuitry

Figure A

2.Values

Input Voltage [V]	Efficiency [%]	
	Load 50%	Load 100%
75	87.0	85.6
80	87.0	86.1
85	87.3	86.5
90	87.3	86.9
100	87.3	87.2
110	87.1	87.6
120	87.1	87.7
132	86.9	87.8
140	86.4	87.7

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Model	LGA150A-48
Item	Efficiency (by Load Current)
Object	

1.Graph

—△—

Input Volt.

85V

---□---

Input Volt.

100V

---○---

Input Volt.

132V

Efficiency [%]

Load Current [A]

Note: Slanted line shows the range of the rated load current.

Temperature
25°C

Testing Circuitry
Figure A

2.Values

Load Current [A]	Efficiency [%]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	-	-	-
0.60	83.4	81.9	78.9
1.20	86.5	86.6	85.4
1.80	87.5	87.6	87.0
2.40	87.3	87.6	87.6
3.00	86.8	87.5	87.8
3.20	86.6	87.5	87.9
3.52	85.8	87.1	87.8
--	-	-	-
--	-	-	-
--	-	-	-

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Model LGA150A-48

Item Power Factor (by Input Voltage)

Object

Temperature 25°C
Testing Circuitry Figure A

1.Graph

---□--- Load 50%
---△--- Load 100%

Input Voltage [V]	Power Factor (Load 50%)	Power Factor (Load 100%)
75	0.551	0.608
80	0.546	0.596
85	0.530	0.582
90	0.521	0.577
100	0.508	0.558
110	0.493	0.541
120	0.479	0.529
132	0.461	0.513
140	0.458	0.508

Note: Slanted line shows the range of the rated input voltage.

2.Values

Input Voltage [V]	Power Factor	
	Load 50%	Load 100%
75	0.551	0.608
80	0.546	0.596
85	0.530	0.582
90	0.521	0.577
100	0.508	0.558
110	0.493	0.541
120	0.479	0.529
132	0.461	0.513
140	0.458	0.508



LOVEL

Model	LGA150A-48
Item	Power Factor (by Load Current)
Object	_____

1.Graph

—△—

Input Volt.

85V

---□---

Input Volt.

100V

-·-○-·-

Input Volt.

132V

Note: Slanted line shows the range of the rated load current.

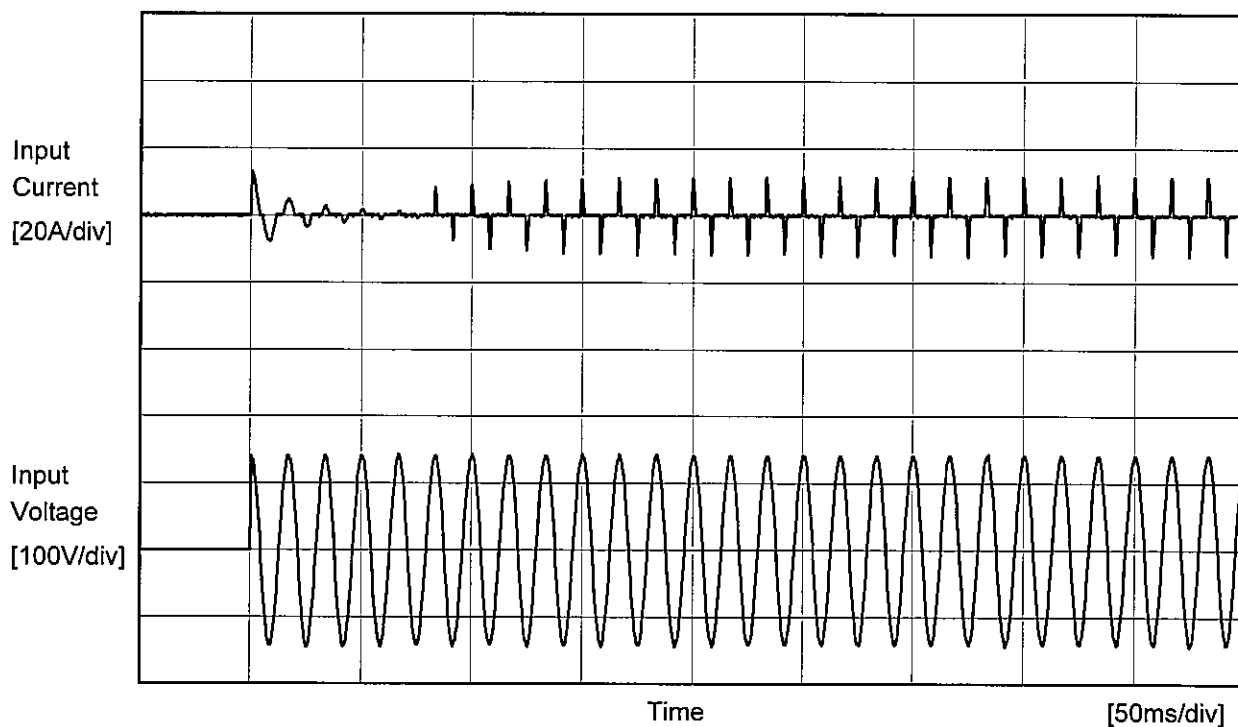
Temperature	25°C
Testing Circuitry	Figure A

2.Values

Load Current [A]	Power Factor		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	0.321	0.310	0.284
0.60	0.450	0.426	0.411
1.20	0.510	0.471	0.447
1.80	0.537	0.508	0.461
2.40	0.560	0.532	0.485
3.00	0.579	0.554	0.499
3.20	0.584	0.558	0.504
3.52	0.592	0.565	0.514
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--	-	-	-
--	-	-	-

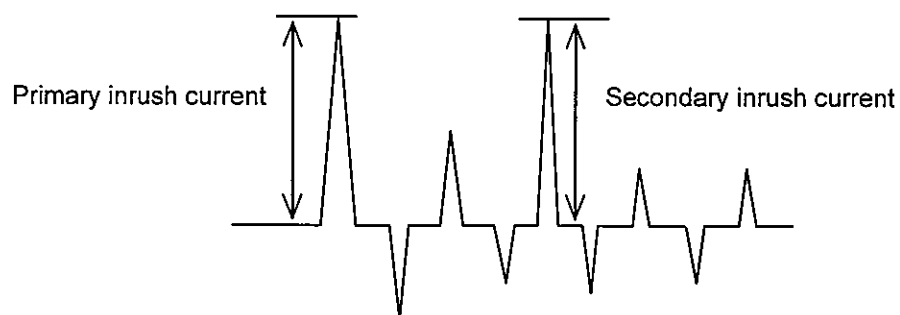
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Model	LGA150A-48	Temperature	25°C
Item	Inrush Current	Testing Circuitry	Figure A
Object	_____		



Input Voltage 100 V
 Frequency 60 Hz
 Load 100 %

Primary inrush current 13.0 A
 Secondary inrush current 12.6 A



1. Results

frequency 60Hz

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.



Model

LGA150A-48

Item

Line Regulation

Object

+48V3.2A

1.Graph

□

Load 50%

△

Load 100%

Input Voltage [V]	Output Voltage [V] (Load 50%)	Output Voltage [V] (Load 100%)
75	48.480	48.477
80	48.478	48.475
85	48.477	48.472
90	48.475	48.470
100	48.472	48.465
110	48.468	48.460
120	48.465	48.454
132	48.462	48.446
140	48.459	48.440

Note: Slanted line shows the range of the rated input voltage.

Temperature

25°C

Testing Circuitry

Figure A

2.Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
75	48.480	48.477
80	48.478	48.475
85	48.477	48.472
90	48.475	48.470
100	48.472	48.465
110	48.468	48.460
120	48.465	48.454
132	48.462	48.446
140	48.459	48.440

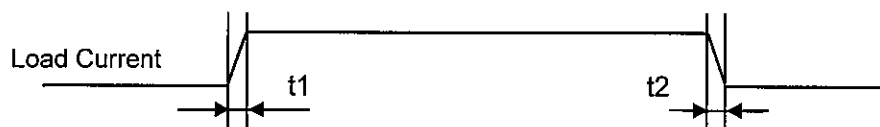
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Model	LGA150A-48	Temperature	25°C
Item	Dynamic Load Response	Testing Circuitry	Figure C
Object	+48V3.2A		

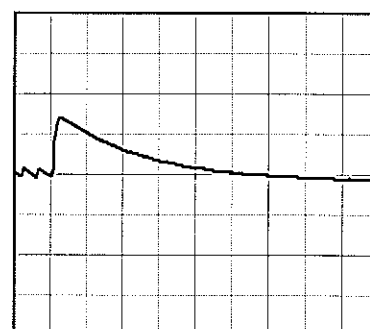
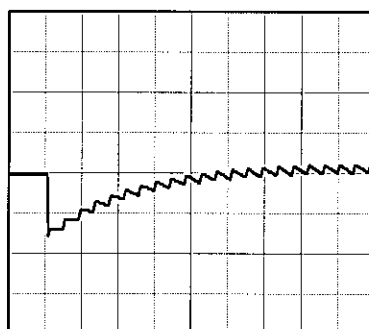
Input Volt. 100 V
Cycle 1000 ms

Response. $t_1=t_2=50\ \mu\text{s}$. Typ



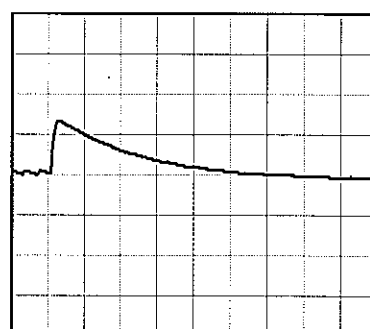
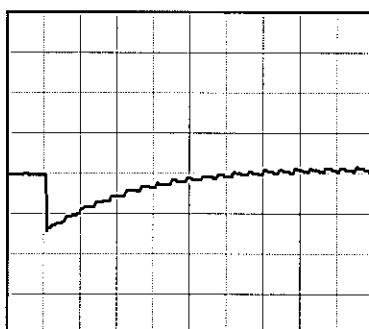
Min. Load (0A) \longleftrightarrow
Load 100% (3.2A)

100 mV/div



Min. Load (0A) \longleftrightarrow
Load 50% (1.6A)

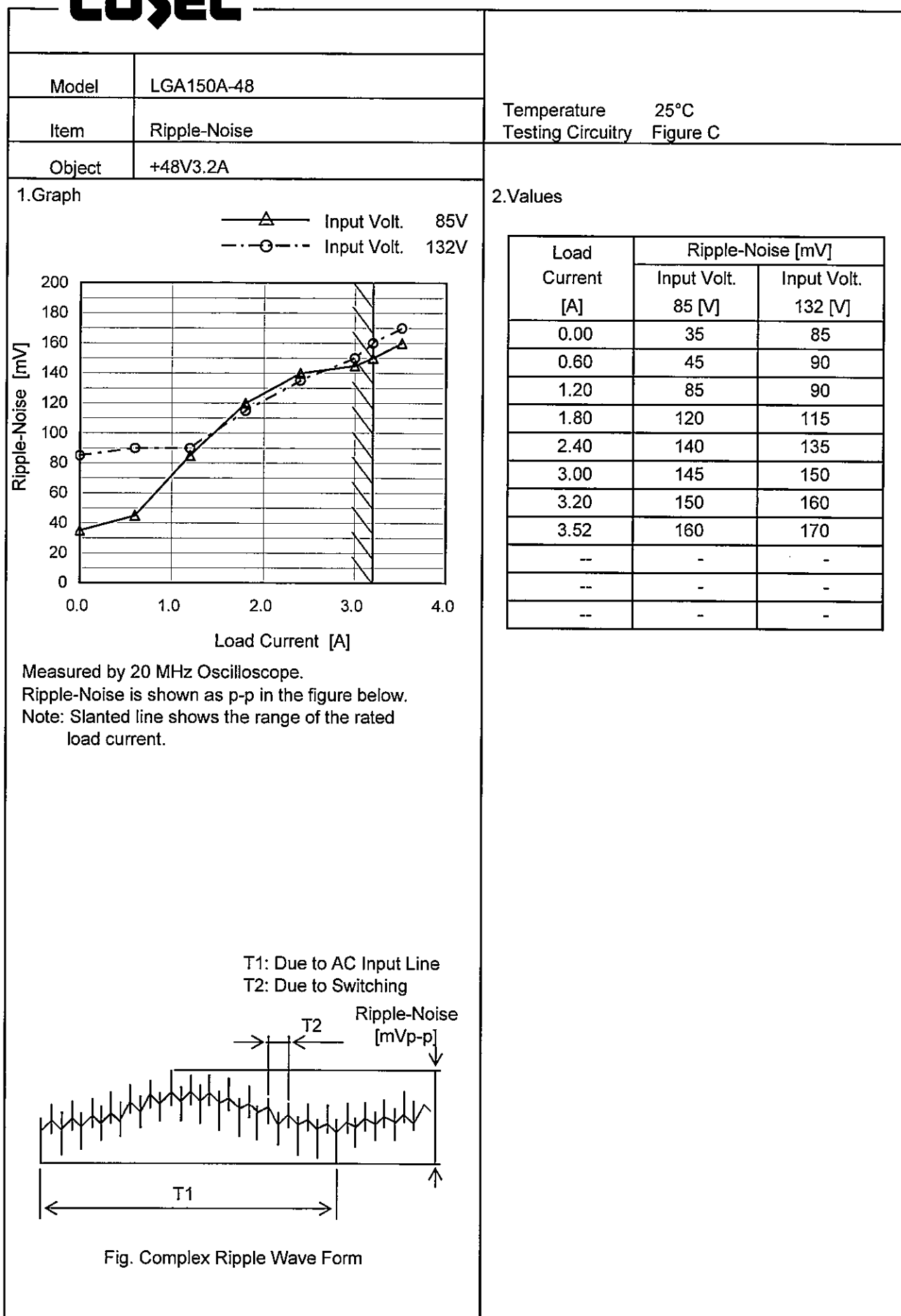
100 mV/div



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Model	LGA150A-48																																								
Item	Ripple Voltage (by Load Current)	Temperature	25°C																																						
Object	+48V3.2A	Testing Circuitry	Figure C																																						
1.Graph		2.Values																																							
<div><div><div><div><div></div><div>Input Volt. 85V</div></div><div><div></div><div>Input Volt. 132V</div></div></div><div><p>Measured by 20 MHz Oscilloscope. Ripple Voltage is shown as p-p in the figure below. Note: Slanted line shows the range of the rated load current.</p></div></div></div>		<table><tr><th rowspan="2">Load Current [A]</th><th colspan="2">Ripple Voltage [mV]</th></tr><tr><th>Input Volt. 85 [V]</th><th>Input Volt. 132 [V]</th></tr><tr><td>0.00</td><td>20</td><td>10</td></tr><tr><td>0.60</td><td>25</td><td>15</td></tr><tr><td>1.20</td><td>30</td><td>25</td></tr><tr><td>1.80</td><td>40</td><td>25</td></tr><tr><td>2.40</td><td>45</td><td>25</td></tr><tr><td>3.00</td><td>50</td><td>30</td></tr><tr><td>3.20</td><td>55</td><td>30</td></tr><tr><td>3.52</td><td>70</td><td>35</td></tr><tr><td>--</td><td>-</td><td>-</td></tr><tr><td>--</td><td>-</td><td>-</td></tr><tr><td>--</td><td>-</td><td>-</td></tr></table>		Load Current [A]	Ripple Voltage [mV]		Input Volt. 85 [V]	Input Volt. 132 [V]	0.00	20	10	0.60	25	15	1.20	30	25	1.80	40	25	2.40	45	25	3.00	50	30	3.20	55	30	3.52	70	35	--	-	-	--	-	-	--	-	-
Load Current [A]	Ripple Voltage [mV]																																								
	Input Volt. 85 [V]	Input Volt. 132 [V]																																							
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1.80	40	25																																							
2.40	45	25																																							
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<div><div><div><div></div><div>T1: Due to AC Input Line</div></div><div><div></div><div>T2: Due to Switching</div></div></div><div><p>Fig. Complex Ripple Wave Form</p></div></div>																																									

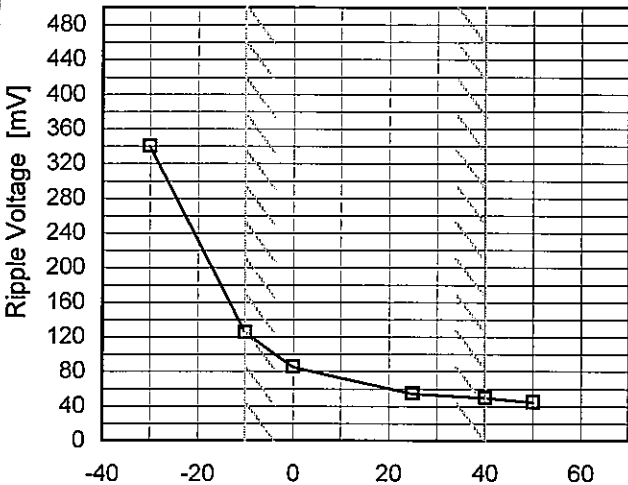
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Model		LGA150A-48
Item		Ripple Voltage (by Ambient Temp.)
Object		+48V3.2A

1.Graph



Ambient Temperature [°C]

Input Volt. 100V
Input Load. 100%

Measured by 20 MHz Oscilloscope.
Note: Slanted line shows the range of the rated ambient temperature.

2.Values

Ambient Temperature [°C]	Ripple Voltage [mV]
-30	340
-10	125
0	85
25	55
40	50
50	45
--	-
--	-
--	-
--	-
--	-

T1: Due to AC Input Line
T2: Due to Switching

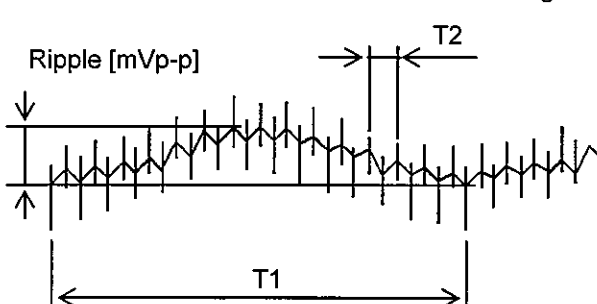


Fig. Complex Ripple Wave Form

Testing Circuitry

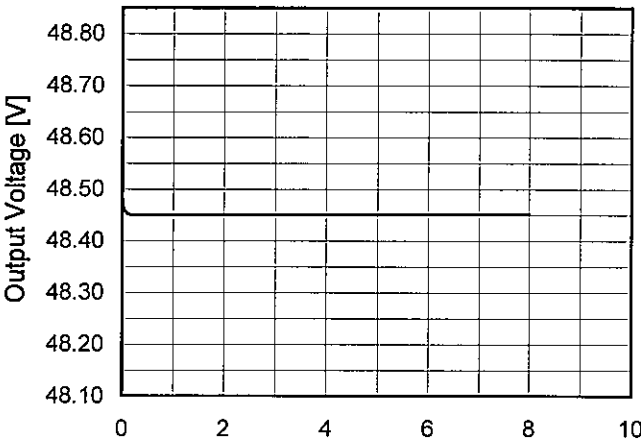
Figure C

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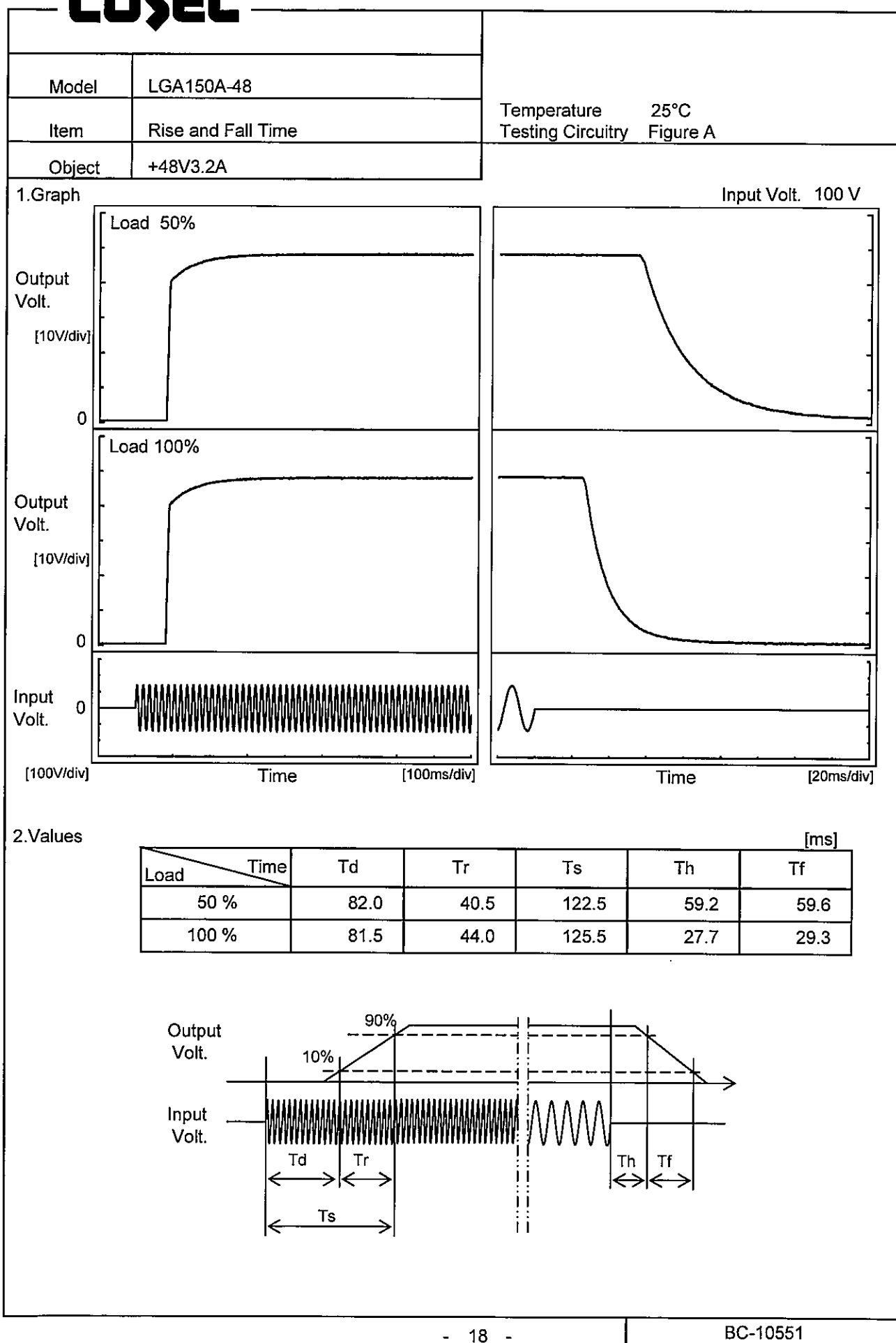
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Model	LGA150A-48																								
Item	Time Lapse Drift	Temperature	25°C																						
		Testing Circuitry	Figure A																						
Object	+48V3.2A																								
1.Graph		2.Values																							
<div><p>Output Voltage [V]</p><p>Time [H]</p><p>Input Volt. 100V</p><p>Load 100%</p></div>		<table><tr><th>Time since start [H]</th><th>Output Voltage [V]</th></tr><tr><td>0.0</td><td>48.486</td></tr><tr><td>0.5</td><td>48.451</td></tr><tr><td>1.0</td><td>48.451</td></tr><tr><td>2.0</td><td>48.451</td></tr><tr><td>3.0</td><td>48.451</td></tr><tr><td>4.0</td><td>48.451</td></tr><tr><td>5.0</td><td>48.452</td></tr><tr><td>6.0</td><td>48.452</td></tr><tr><td>7.0</td><td>48.452</td></tr><tr><td>8.0</td><td>48.453</td></tr></table>		Time since start [H]	Output Voltage [V]	0.0	48.486	0.5	48.451	1.0	48.451	2.0	48.451	3.0	48.451	4.0	48.451	5.0	48.452	6.0	48.452	7.0	48.452	8.0	48.453
Time since start [H]	Output Voltage [V]																								
0.0	48.486																								
0.5	48.451																								
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6.0	48.452																								
7.0	48.452																								
8.0	48.453																								

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Model	LGA150A-48
Item	Hold-Up Time
Object	+48V3.2A

Temperature	25°C
Testing Circuitry	Figure A

1.Graph

Legend:

- Load 50%
- △— Load 100%

Input Voltage [V]	Hold-Up Time [ms] (Load 50%)	Hold-Up Time [ms] (Load 100%)
75	21	8
80	27	11
85	34	15
90	41	18
100	56	26
110	73	35
120	92	44
132	116	57
140	134	66

2.Values

Input Voltage [V]	Hold-Up Time [ms]	
	Load 50%	Load 100%
75	21	8
80	27	11
85	34	15
90	41	18
100	56	26
110	73	35
120	92	44
132	116	57
140	134	66

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.
 Note: Slanted line shows the range of the rated input voltage.

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Model		LGA150A-48	
Item		Instantaneous Interruption Compensation	
Object		+48V3.2A	
1.Graph		2.Values	

—△—

Input Volt.

85V

---□---

Input Volt.

100V

---○---

Input Volt.

132V

Instantaneous Compensation Time [ms]

1000

100

10

1

0.0

1.0

2.0

3.0

4.0

Load Current [A]

Note: Slanted line shows the range of the rated load current.

Load Current [A]	Time [ms]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	-	-	-
0.60	96	152	303
1.20	48	78	157
1.80	31	51	106
2.40	22	37	79
3.00	14	29	62
3.20	14	26	57
3.52	13	23	51
--	-	-	-
--	-	-	-
--	-	-	-

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Model LGA150A-48

Item Minimum Input Voltage
for Regulated Output Voltage

Object +48V3.2A

Testing Circuitry Figure A

1.Graph

---□--- Load 50%
---△--- Load 100%

Ambient Temperature [°C]	Input Voltage [V] (Load 50%)	Input Voltage [V] (Load 100%)
-20	60	66
-10	60	65
0	60	65
10	60	65
20	59	64
30	58	64
40	58	64
50	58	64
60	58	64

Note: Slanted line shows the range of the rated ambient temperature.

2.Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	60	66
-10	59	65
0	59	65
10	59	65
20	58	65
25	58	64
30	58	64
40	58	64
50	58	64
60	58	64
--	-	-



Model		LGA150A-48	
Item		Overcurrent Protection	
Object		+48V3.2A	

1.Graph

Input Volt. 85V

Input Volt. 100V

Input Volt. 132V

Output Voltage [V]

</



Model

LGA150A-48

Item

Overvoltage Protection

Object

+48V3.2A

1.Graph

—△—

Input Volt. 85V

---□---

Input Volt. 132V

Operating Point [V]

Ambient Temperature [°C]

Load 0%

Note: Slanted line shows the range of the rated ambient temperature.

2.Values

Ambient Temperature [°C]	Operating Point [V]	
	Input Volt. 85[V]	Input Volt. 132[V]
-20	58.48	58.36
-10	58.95	58.95
0	59.53	59.53
10	60.08	60.08
20	60.66	60.66
25	60.90	60.96
30	61.19	61.19
40	61.78	61.78
50	62.25	62.36
60	62.89	62.95
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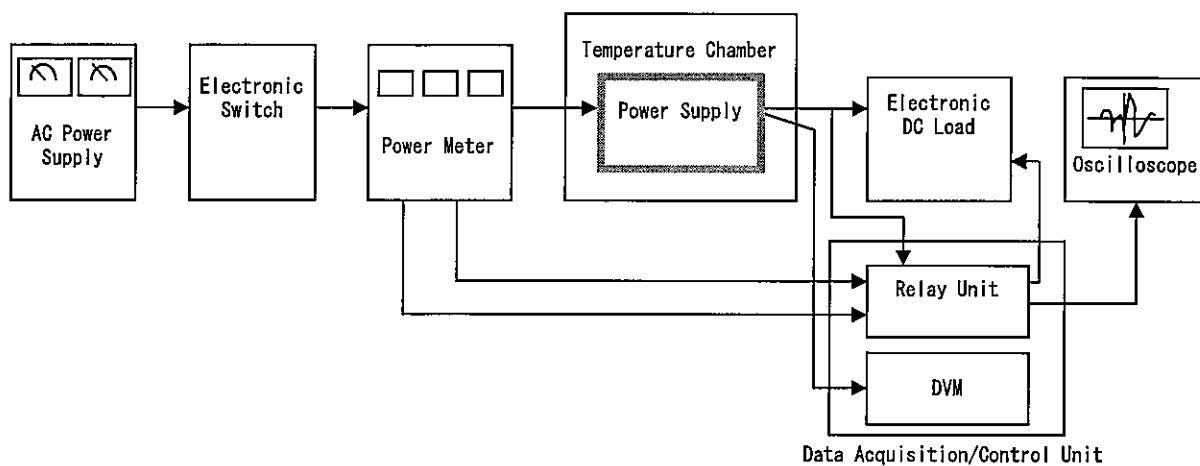


Figure A

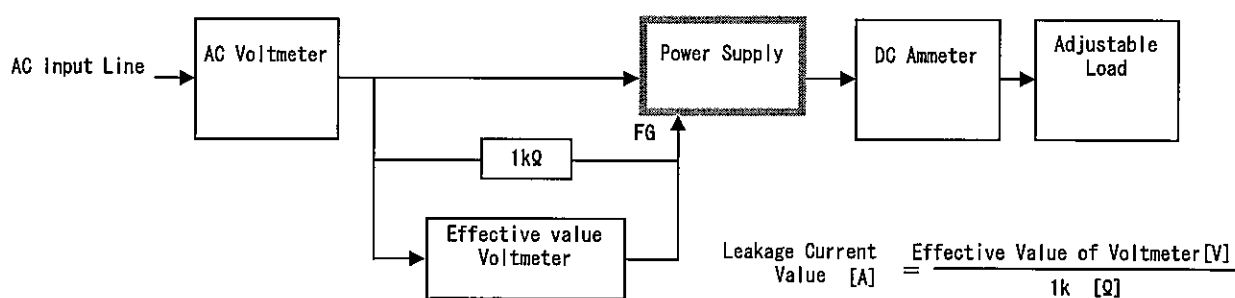


Figure B (DEN-AN)

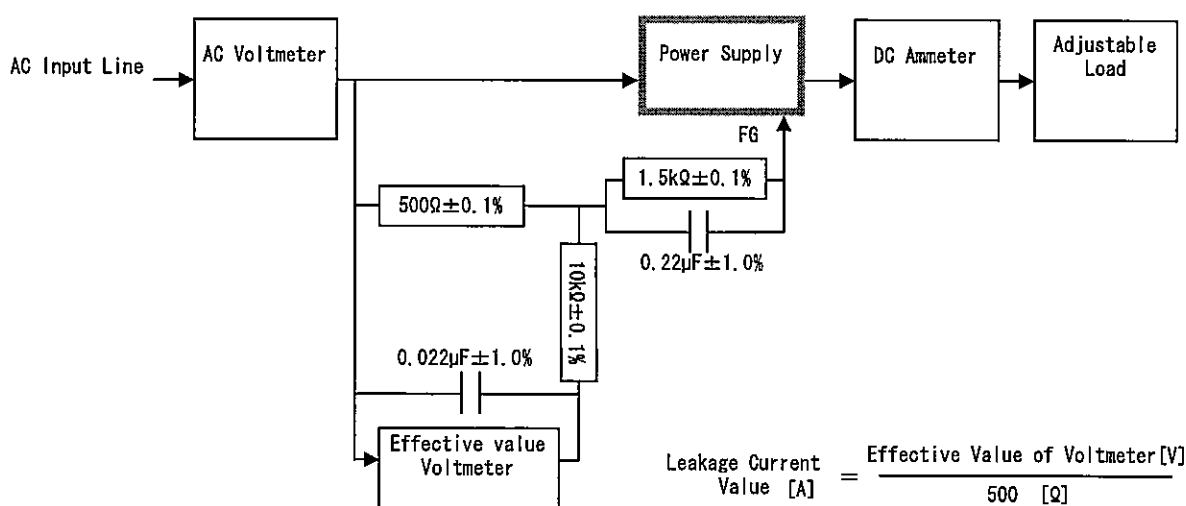


Figure B (IEC60950-1)

COSEL

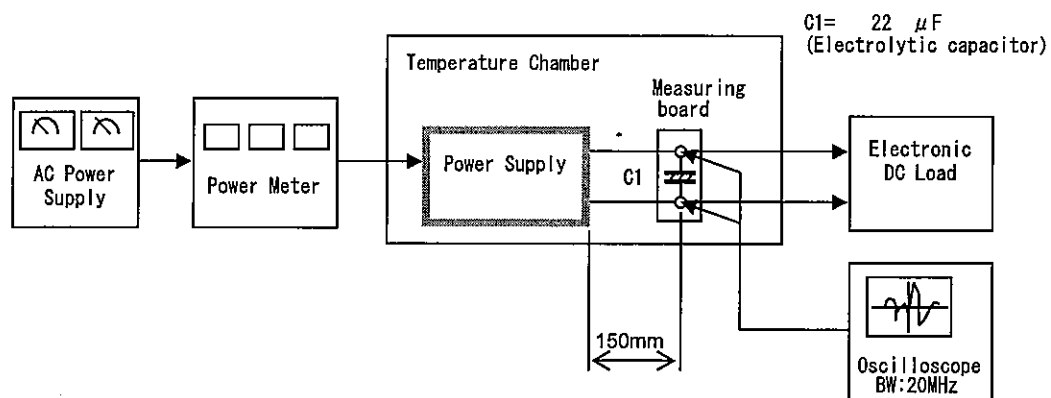


Figure C