



# TEST DATA OF LGA150A-48

Regulated DC Power Supply  
May 20, 2011

Approved by : Kenji Shiho  
Kenji Shiho Design Manager

Prepared by : Hironobu Shimizu  
Hironobu Shimizu Design Engineer

**COSEL CO.,LTD.**



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Model	LGA150A-48																																																					
Item	Input Current (by Load Current)	Temperature 25°C	Testing Circuitry Figure A																																																			
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<p>The graph shows efficiency increasing from approximately 82% at 0.5A to nearly 90% at 3.5A. The 85V curve is the highest, followed by 100V, and then 132V. A vertical slanted line is drawn through the curves between approximately 1.5A and 2.5A, representing the rated load current range.</p>																																																						
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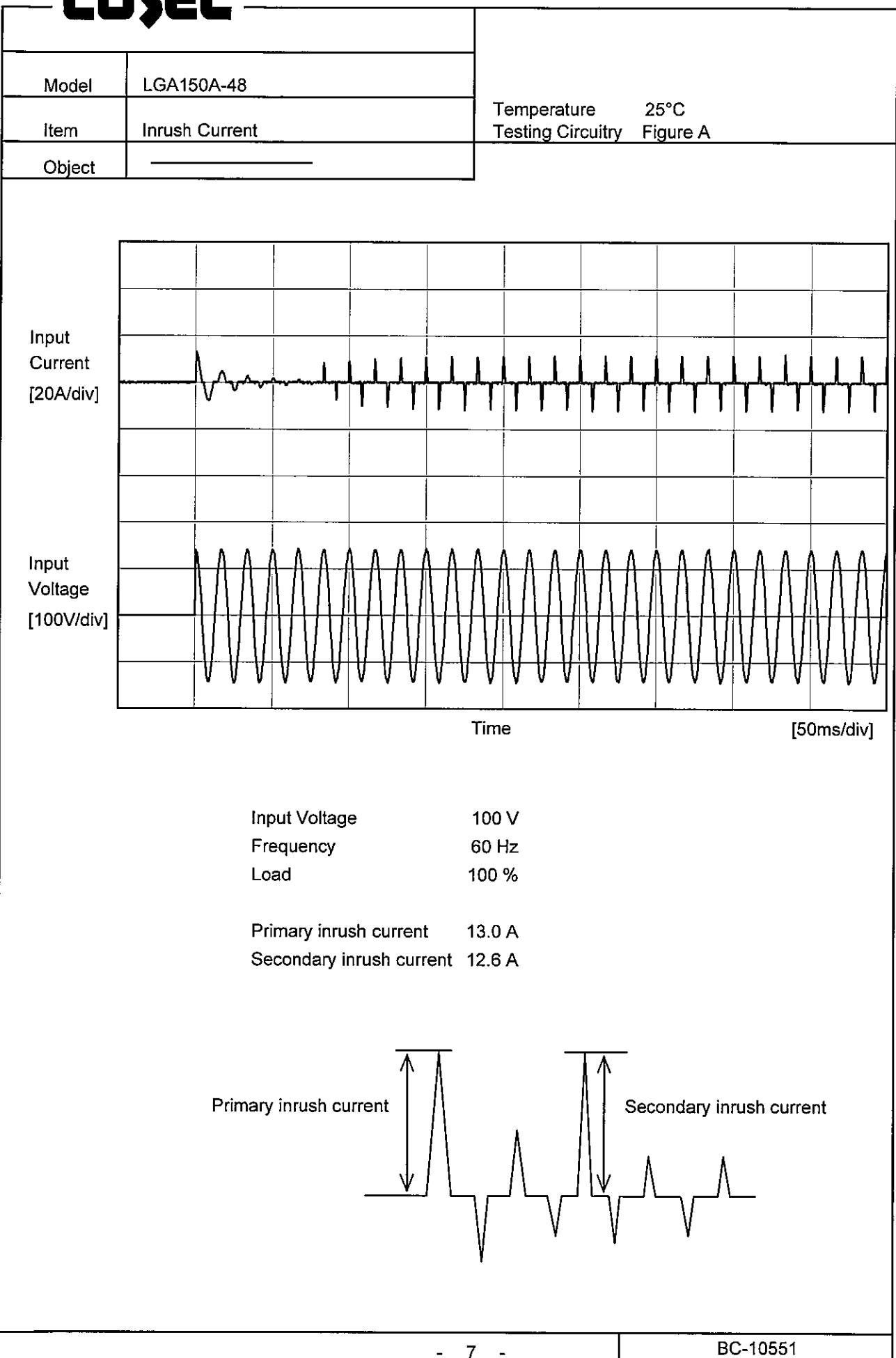
Note: Slanted line shows the range of the rated input voltage.

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Model	LGA150A-48			
Item	Power Factor (by Load Current)			
Object	<hr/>			
1. Graph	<p>Legend:</p> <ul style="list-style-type: none"> <li>Input Volt. 85V</li> <li>Input Volt. 100V</li> <li>Input Volt. 132V</li> </ul>			
2. Values	Load Current [A]	Power Factor		
		Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00		0.321	0.310	0.284
0.60		0.450	0.426	0.411
1.20		0.510	0.471	0.447
1.80		0.537	0.508	0.461
2.40		0.560	0.532	0.485
3.00		0.579	0.554	0.499
3.20		0.584	0.558	0.504
3.52		0.592	0.565	0.514
--		-	-	-
--		-	-	-
--		-	-	-

Note: Slanted line shows the range of the rated load current.

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Model	LGA150A-48	Temperature Testing Circuitry	25°C Figure B	
Item	Leakage Current			
Object	_____			

### 1. Results

Standards	Leakage Current [mA]		
	Input Volt. 100 [V]	Input Volt. 120 [V]	Input Volt. 132 [V]
(A)DEN-AN	0.30	0.39	0.43
(B)IEC60950-1	0.30	0.37	0.42

frequency 60Hz

### 2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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Item	Line Regulation	Temperature      25°C Testing Circuitry      Figure A																															
Object	+48V3.2A																																
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Model	LGA150A-48	Temperature Testing Circuitry Figure C	25°C
Item	Dynamic Load Response		
Object	+48V3.2A		

Input Volt. 100 V  
Cycle 1000 ms

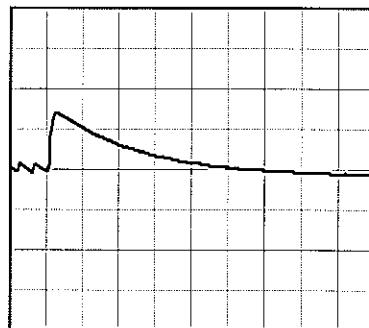
Response.  $t_1=t_2=50 \mu\text{s}$ . Typ



Min. Load (0A) ↔  
Load 100% (3.2A)

100 mV/div

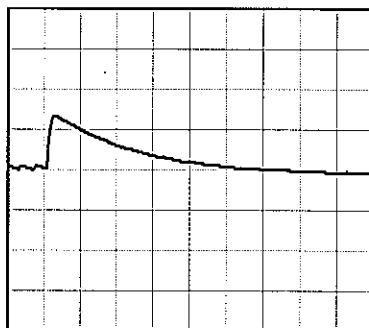
20 ms/div



Min. Load (0A) ↔  
Load 50% (1.6A)

100 mV/div

20 ms/div



**COSEL**

Model	LGA150A-48																																							
Item	Ripple Voltage (by Load Current)	Temperature 25°C Testing Circuitry Figure C																																						
Object	+48V3.2A																																							
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<p>Measured by 20 MHz Oscilloscope. Ripple Voltage is shown as p-p in the figure below. Note: Slanted line shows the range of the rated load current.</p>																																								
<p>T1: Due to AC Input Line T2: Due to Switching</p>																																								
Fig. Complex Ripple Wave Form																																								

**COSEL**

Model	LGA150A-48																																							
Item	Ripple-Noise	Temperature      25°C Testing Circuitry    Figure C																																						
Object	+48V3.2A																																							
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Load Current [A]	Ripple-Noise [mV]																																							
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<p>Fig. Complex Ripple Wave Form</p>																																								

**COSEL**

Model	LGA150A-48	Testing Circuitry Figure C																							
Item	Ripple Voltage (by Ambient Temp.)																								
Object	+48V3.2A																								
1.Graph	<p>Ambient Temperature [°C]</p> <p>Ripple Voltage [mV]</p> <table border="1"> <thead> <tr> <th>Ambient Temperature [°C]</th> <th>Ripple Voltage [mV]</th> </tr> </thead> <tbody> <tr><td>-30</td><td>340</td></tr> <tr><td>-10</td><td>125</td></tr> <tr><td>0</td><td>85</td></tr> <tr><td>25</td><td>55</td></tr> <tr><td>40</td><td>50</td></tr> <tr><td>50</td><td>45</td></tr> <tr><td>--</td><td>-</td></tr> <tr><td>--</td><td>-</td></tr> <tr><td>--</td><td>-</td></tr> <tr><td>--</td><td>-</td></tr> <tr><td>--</td><td>-</td></tr> </tbody> </table> <p>Input Volt. 100V Input Load. 100%</p> <p>Measured by 20 MHz Oscilloscope. Note: Slanted line shows the range of the rated ambient temperature.</p>	Ambient Temperature [°C]	Ripple Voltage [mV]	-30	340	-10	125	0	85	25	55	40	50	50	45	--	-	--	-	--	-	--	-	--	-
Ambient Temperature [°C]	Ripple Voltage [mV]																								
-30	340																								
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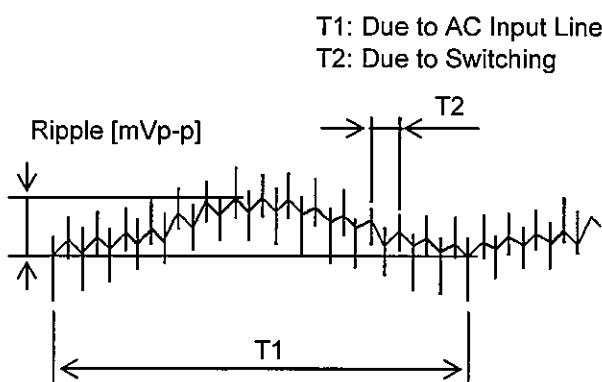


Fig. Complex Ripple Wave Form

**COSEL**

Model	LGA150A-48	Testing Circuitry Figure A																																																					
Item	Ambient Temperature Drift																																																						
Object	+48V3.2A																																																						
1.Graph	<p>Output Voltage [V]</p> <p>Ambient Temperature [°C]</p> <p>Load 100%</p> <p>Legend:</p> <ul style="list-style-type: none"> <li>Input Volt. 85V</li> <li>Input Volt. 100V</li> <li>Input Volt. 132V</li> </ul>	2.Values																																																					
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Note: Slanted line shows the range of the rated ambient temperature.



Model	LGA150A-48	
Item	Output Voltage Accuracy	Testing Circuitry Figure A
Object	+48V3.2A	

### 1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10 - 40°C

Input Voltage : 85 - 132V

Load Current : 0 - 3.2A

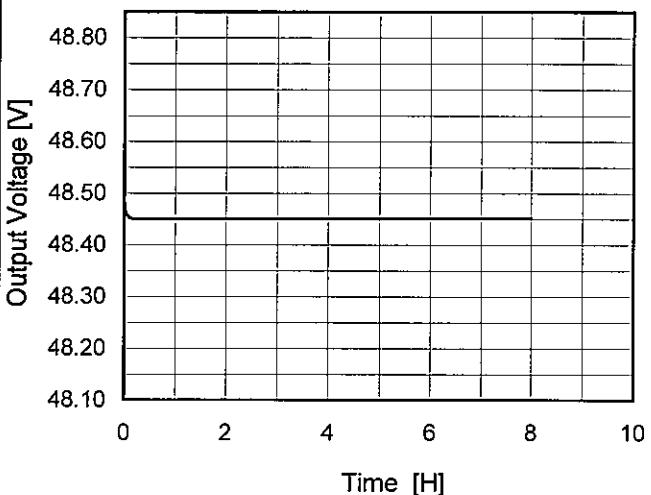
\* Output Voltage Accuracy =  $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

$$\text{* Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

### 2. Values

Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ration [%]
Maximum Voltage	-10	85	0	48.496	$\pm 37$	$\pm 0.1$
Minimum Voltage	40	132	3.2	48.423		

**COSEL**

Model	LGA150A-48	Temperature	25°C																						
Item	Time Lapse Drift	Testing Circuitry	Figure A																						
Object	+48V3.2A																								
1.Graph			2.Values																						
 <p>Output Voltage [V]</p> <p>Time [H]</p> <p>Input Volt. 100V Load 100%</p>			<table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>48.486</td></tr> <tr><td>0.5</td><td>48.451</td></tr> <tr><td>1.0</td><td>48.451</td></tr> <tr><td>2.0</td><td>48.451</td></tr> <tr><td>3.0</td><td>48.451</td></tr> <tr><td>4.0</td><td>48.451</td></tr> <tr><td>5.0</td><td>48.452</td></tr> <tr><td>6.0</td><td>48.452</td></tr> <tr><td>7.0</td><td>48.452</td></tr> <tr><td>8.0</td><td>48.453</td></tr> </tbody> </table>	Time since start [H]	Output Voltage [V]	0.0	48.486	0.5	48.451	1.0	48.451	2.0	48.451	3.0	48.451	4.0	48.451	5.0	48.452	6.0	48.452	7.0	48.452	8.0	48.453
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**COSEL**

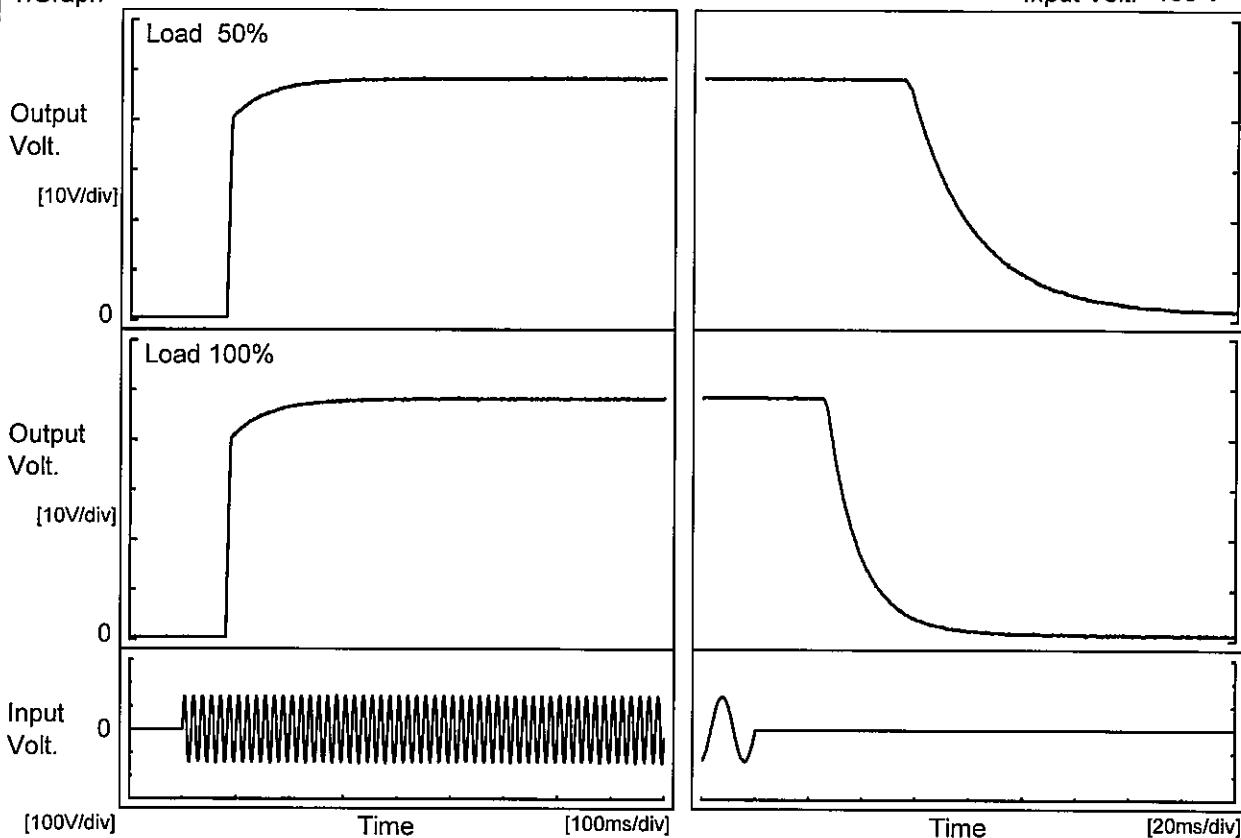
Model LGA150A-48

Item Rise and Fall Time

Temperature 25°C  
Testing Circuitry Figure A

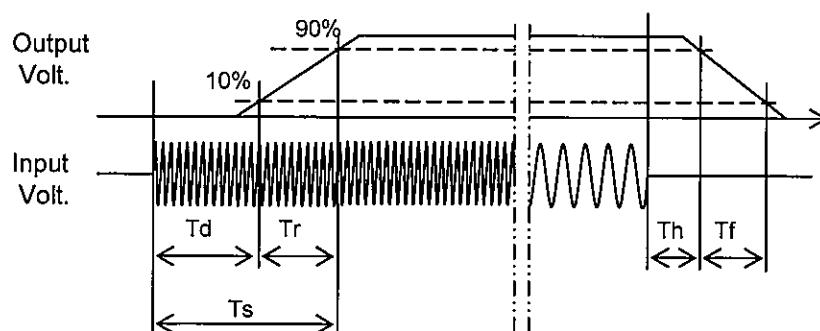
Object +48V3.2A

## 1. Graph



## 2. Values

Load	Time	Td	Tr	Ts	Th	Tf	[ms]
50 %		82.0	40.5	122.5	59.2	59.6	
100 %		81.5	44.0	125.5	27.7	29.3	



**COSEL**

Model	LGA150A-48	Temperature	25°C																																
Item	Hold-Up Time	Testing Circuitry	Figure A																																
Object	+48V3.2A																																		
1. Graph																																			
<p>Legend: ---□--- Load 50% —△— Load 100%</p> <p>Y-axis: Hold-Up Time [ms] X-axis: Input Voltage [V]</p>																																			
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Input Voltage [V]	Hold-Up Time [ms]																																		
	Load 50%	Load 100%																																	
75	21	8																																	
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90	41	18																																	
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110	73	35																																	
120	92	44																																	
132	116	57																																	
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<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.      Note: Slanted line shows the range of the rated input voltage.</p>																																			

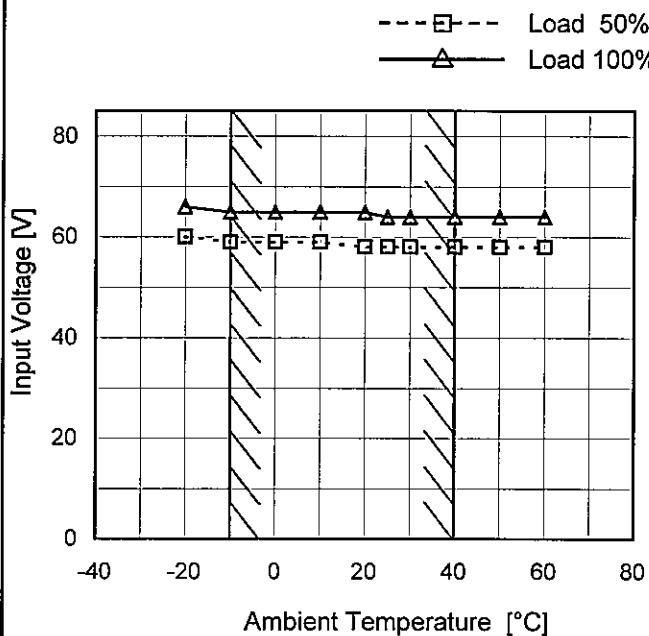
**COSEL**

Model	LGA150A-48	Temperature	25°C																																																			
Item	Instantaneous Interruption Compensation	Testing Circuitry	Figure A																																																			
Object	+48V3.2A																																																					
1.Graph	<p>Legend:</p> <ul style="list-style-type: none"> <li>Input Volt. 85V</li> <li>Input Volt. 100V</li> <li>Input Volt. 132V</li> </ul> <p>Y-axis: Instantaneous Compensation Time [ms]</p> <p>X-axis: Load Current [A]</p>																																																					
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Load Current [A]	Time [ms]																																																					
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Note:	Slanted line shows the range of the rated load current.																																																					

**COSEL**

Model	LGA150A-48
Item	Minimum Input Voltage for Regulated Output Voltage
Object	+48V3.2A

## 1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

## Testing Circuitry Figure A

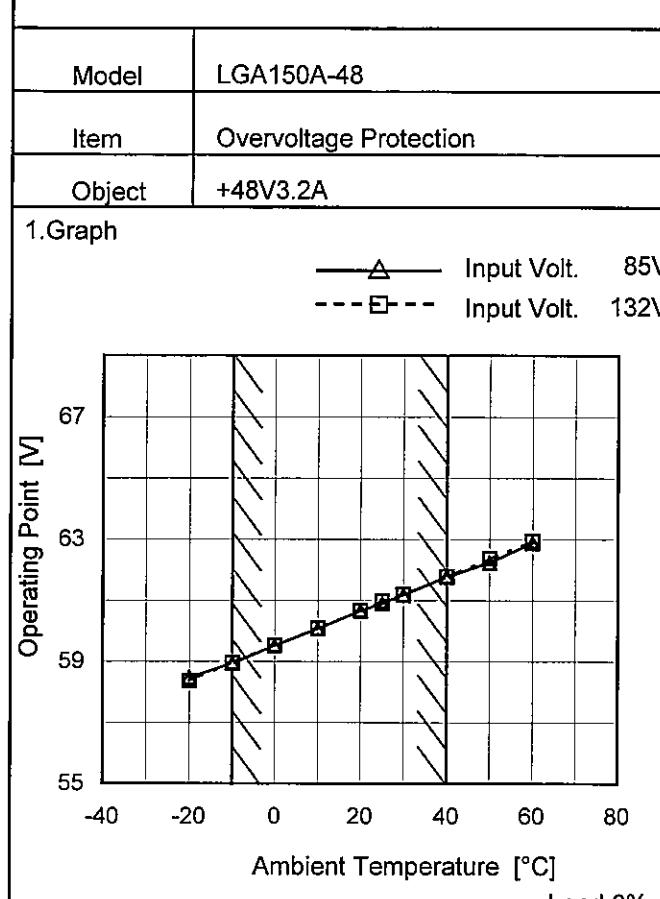
## 2. Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	60	66
-10	59	65
0	59	65
10	59	65
20	58	65
25	58	64
30	58	64
40	58	64
50	58	64
60	58	64
--	-	-

# COSEL

Model	LGA150A-48					
Item	Overcurrent Protection					
Object	+48V3.2A					
1.Graph						
<p>The graph plots Output Voltage [V] on the Y-axis (0 to 60) against Load Current [A] on the X-axis (0.0 to 5.0). Three horizontal lines represent different input voltages: 85V (top), 100V (middle), and 132V (bottom). A slanted line starts at approximately (3.5, 48) and ends at (4.0, 0), indicating the range of the rated load current.</p>						
Note: Slanted line shows the range of the rated load current.						
Temperature 25°C Testing Circuitry Figure A						
2.Values						
Output Voltage [V]	Load Current [A]					
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]			
48.0	3.78	3.75	3.76			
45.6	3.79	3.76	3.77			
43.2	3.78	3.75	3.76			
38.4	3.82	3.81	3.84			
33.6	3.84	3.84	3.87			
28.8	3.86	3.88	3.92			
24.0	3.88	3.88	3.93			
19.2	3.88	3.89	3.93			
14.4	3.91	3.91	3.97			
9.6	3.94	3.94	3.97			
4.8	3.84	3.85	3.87			
0.0	3.86	3.85	3.85			

**COSEL**

Model      LGA150A-48 Item      Overvoltage Protection Object    +48V3.2A	Testing Circuitry   Figure A																																						
	1.Graph	2.Values																																					
	 <p>Operating Point [V]</p> <p>Ambient Temperature [°C]</p> <p>Load 0%</p> <p>Legend: Input Volt. 85V (solid line with triangle markers), Input Volt. 132V (dashed line with square markers)</p>																																						
		<table border="1"> <thead> <tr> <th rowspan="2">Ambient Temperature [°C]</th> <th colspan="2">Operating Point [V]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>-20</td><td>58.48</td><td>58.36</td></tr> <tr><td>-10</td><td>58.95</td><td>58.95</td></tr> <tr><td>0</td><td>59.53</td><td>59.53</td></tr> <tr><td>10</td><td>60.08</td><td>60.08</td></tr> <tr><td>20</td><td>60.66</td><td>60.66</td></tr> <tr><td>25</td><td>60.90</td><td>60.96</td></tr> <tr><td>30</td><td>61.19</td><td>61.19</td></tr> <tr><td>40</td><td>61.78</td><td>61.78</td></tr> <tr><td>50</td><td>62.25</td><td>62.36</td></tr> <tr><td>60</td><td>62.89</td><td>62.95</td></tr> <tr><td>--</td><td>-</td><td>-</td></tr> </tbody> </table>	Ambient Temperature [°C]	Operating Point [V]		Input Volt. 85[V]	Input Volt. 132[V]	-20	58.48	58.36	-10	58.95	58.95	0	59.53	59.53	10	60.08	60.08	20	60.66	60.66	25	60.90	60.96	30	61.19	61.19	40	61.78	61.78	50	62.25	62.36	60	62.89	62.95	--	-
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Note: Slanted line shows the range of the rated ambient temperature.																																							

COSEL

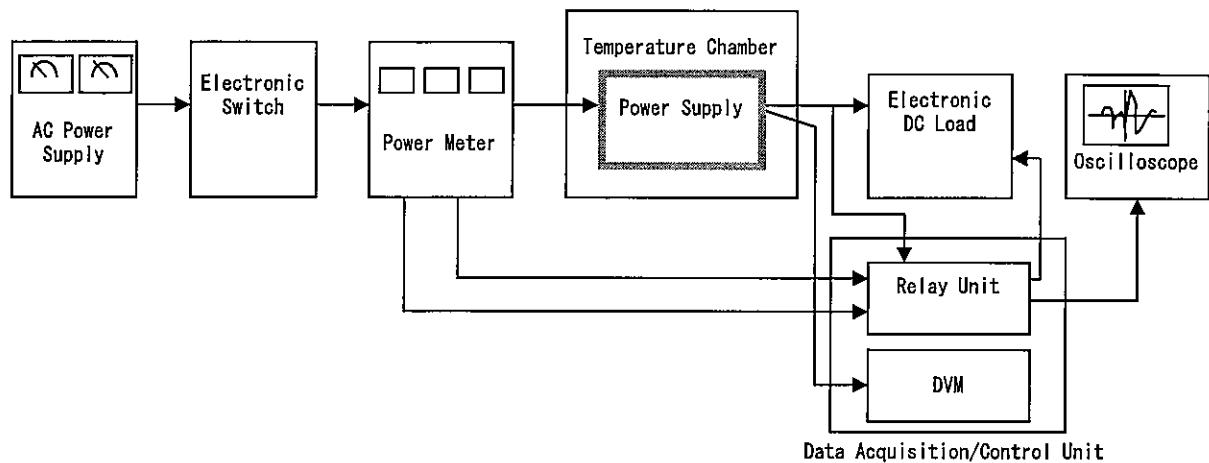


Figure A

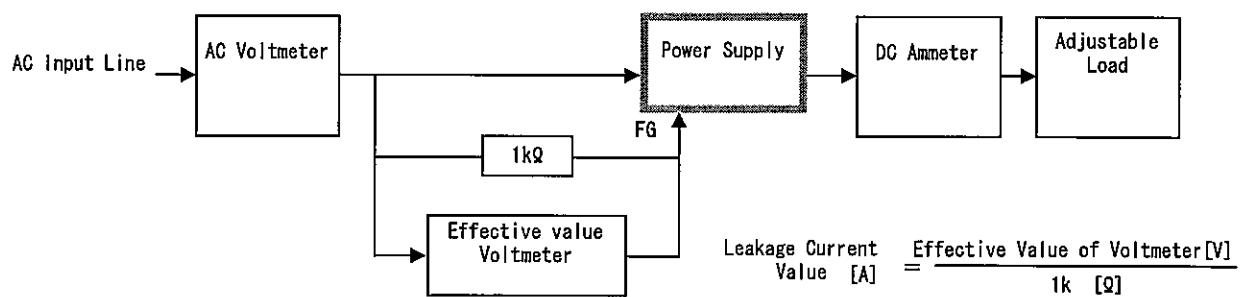


Figure B ( DEN-AN )

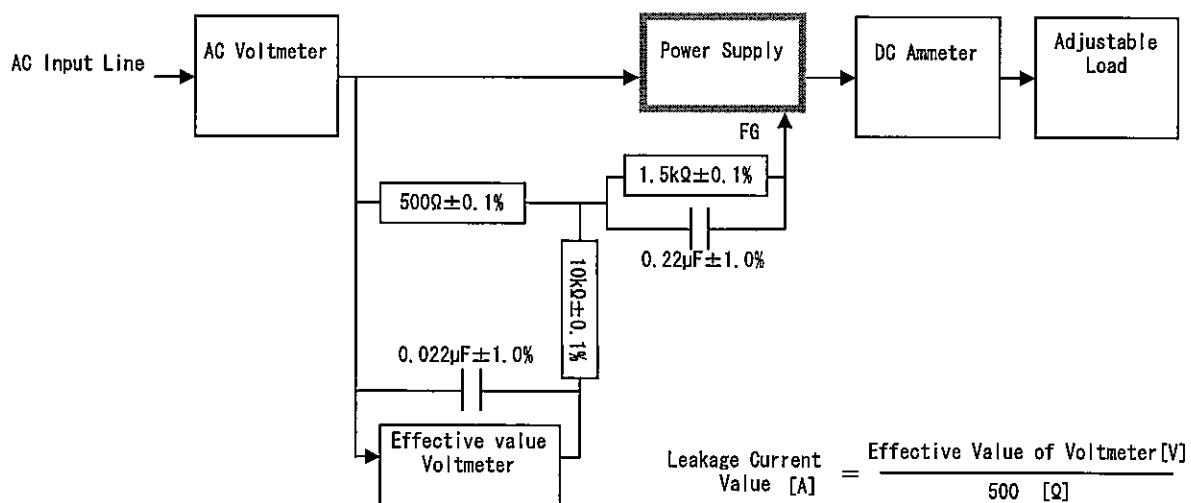


Figure B ( IEC60950-1 )

COSEL

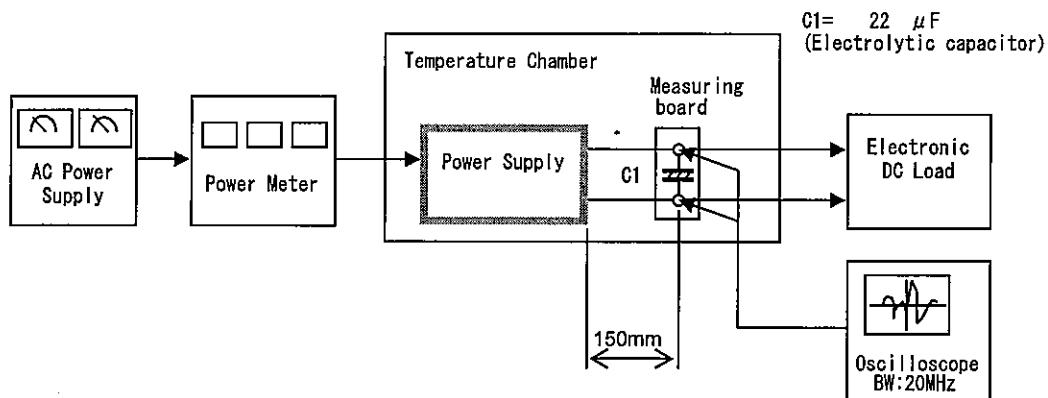


Figure C