



# TEST DATA OF LGA100A-5-Y

Regulated DC Power Supply  
April 15, 2008

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Yoshiaki Shimizu Design Manager

Prepared by : Kazuo Ishimura  
Kazuo Ishimura Design Engineer

**COSEL CO.,LTD.**

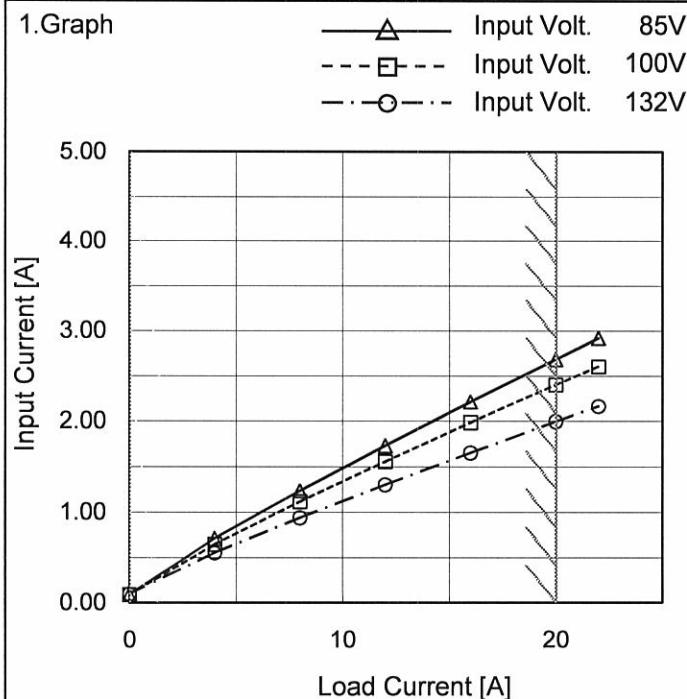
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Model	LGA100A-5-Y
Item	Input Current (by Load Current)
Object	_____



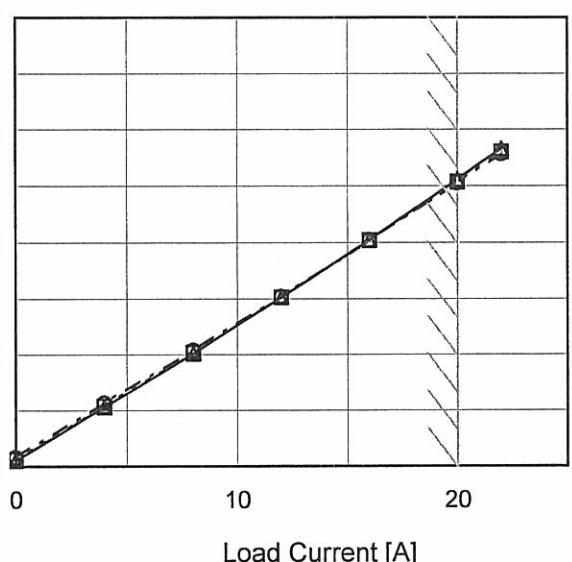
Temperature 25°C  
Testing Circuitry Figure A

## 2. Values

Load Current [A]	Input Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	0.089	0.091	0.098
4	0.711	0.647	0.556
8	1.235	1.115	0.937
12	1.730	1.556	1.301
16	2.208	1.982	1.652
20	2.683	2.399	1.992
22	2.918	2.606	2.162
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Note: Slanted line shows the range of the rated load current.

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Model	LGA100A-5-Y																																																					
Item	Input Power (by Load Current)																																																					
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1. Graph																																																						
—△— Input Volt. 85V - -□--- Input Volt. 100V - -○--- Input Volt. 132V																																																						
 <p>The graph plots Input Power [W] on the Y-axis (0 to 200) against Load Current [A] on the X-axis (0 to 20). Three data series are shown for input voltages of 85V, 100V, and 132V. All three series show a linear increase in power with load current. A slanted line is drawn through the data points, representing the rated load current range.</p>																																																						
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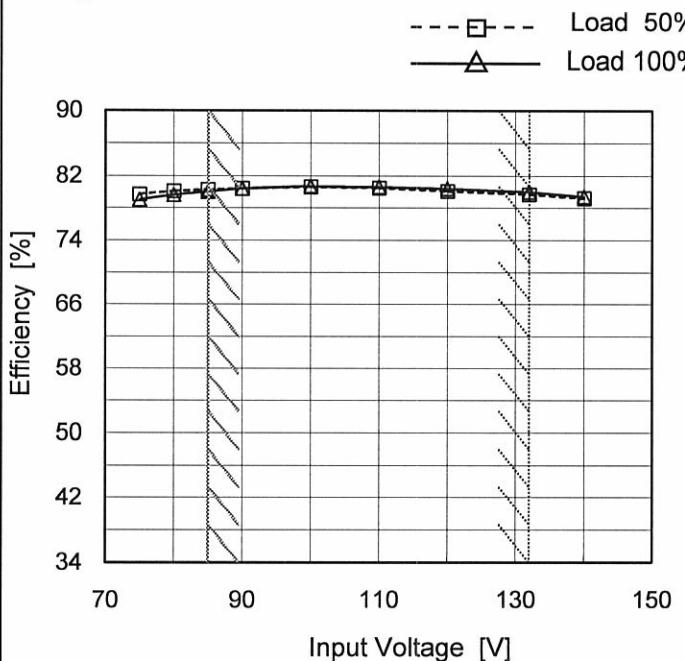
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Model LGA100A-5-Y

Item Efficiency (by Input Voltage)

Object \_\_\_\_\_

## 1. Graph



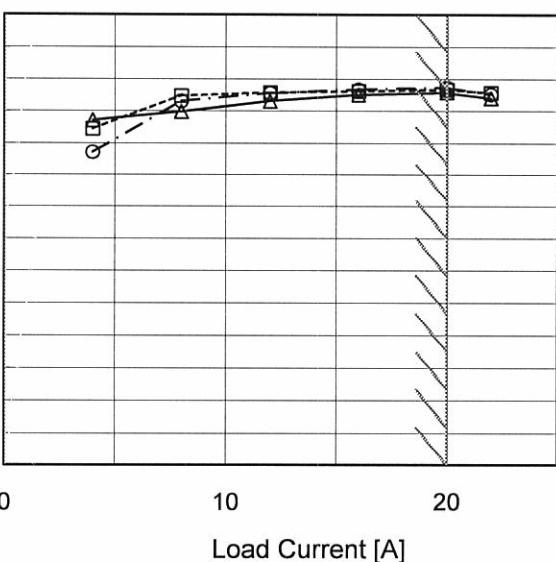
Note: Slanted line shows the range of the rated input voltage.

 Temperature 25°C  
 Testing Circuitry Figure A

## 2. Values

Input Voltage [V]	Efficiency [%]	
	Load 50%	Load 100%
75	79.7	79.0
80	80.1	79.6
85	80.3	80.0
90	80.4	80.4
100	80.6	80.7
110	80.5	80.6
120	80.1	80.4
132	79.7	80.0
140	79.2	79.4

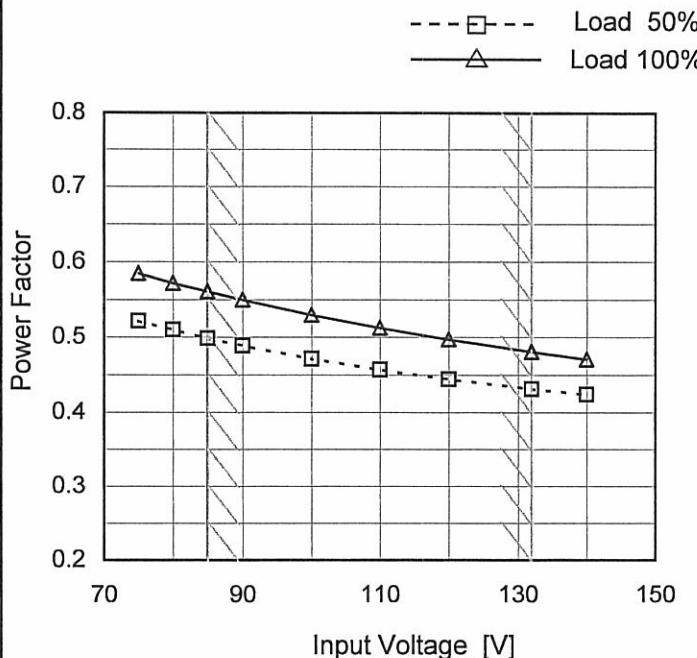
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Model	LGA100A-5-Y
Item	Power Factor (by Input Voltage)
Object	—

## 1.Graph



Note: Slanted line shows the range of the rated input voltage.

Temperature 25°C  
Testing Circuitry Figure A

## 2.Values

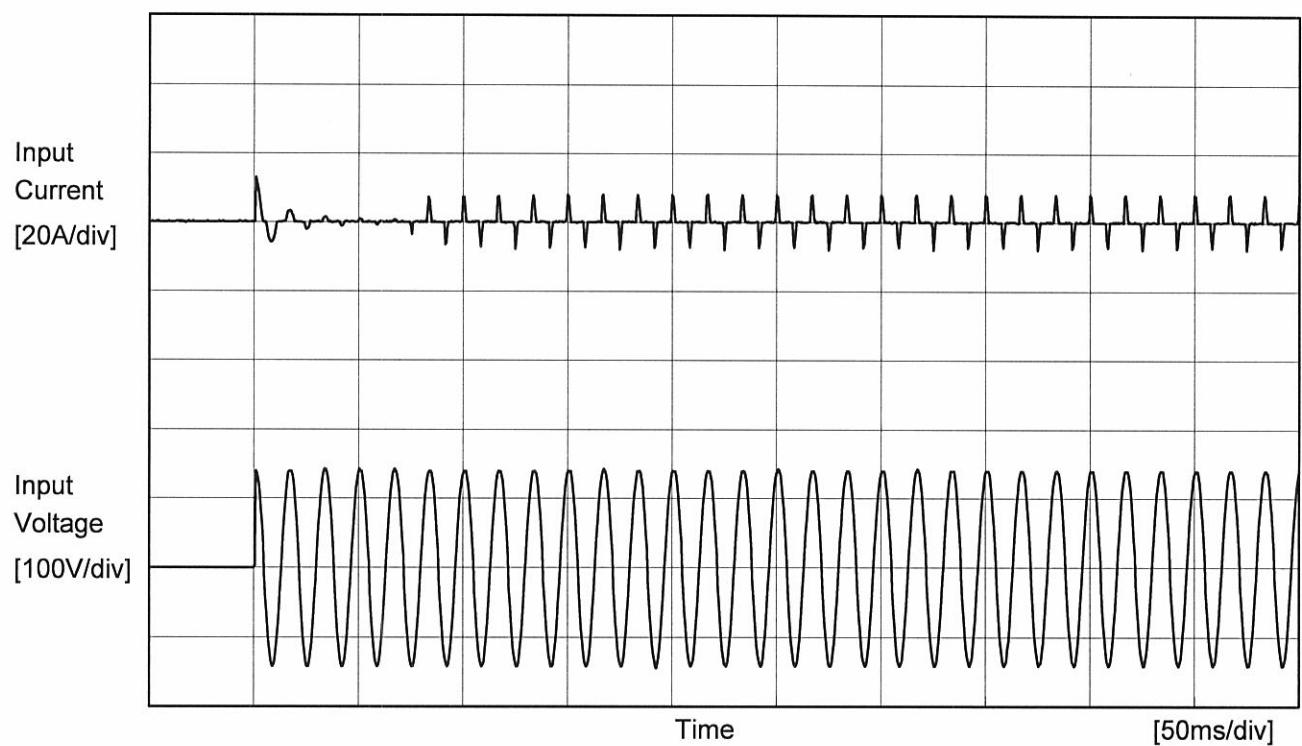
Input Voltage [V]	Power Factor	
	Load 50%	Load 100%
75	0.522	0.585
80	0.510	0.572
85	0.498	0.561
90	0.488	0.550
100	0.471	0.530
110	0.457	0.513
120	0.444	0.497
132	0.432	0.481
140	0.424	0.471

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Model	LGA100A-5-Y																																																					
Item	Power Factor (by Load Current)	Temperature Testing Circuitry	25°C Figure A																																																			
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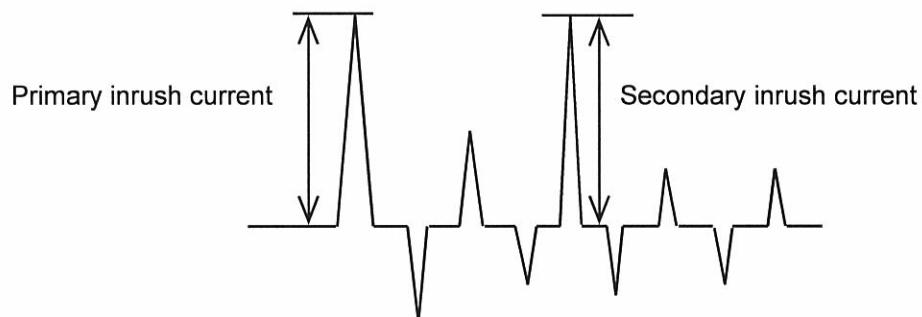
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Model	LGA100A-5-Y	Temperature Testing Circuitry 25°C Figure A
Item	Inrush Current	
Object	_____	



Input Voltage	100 V
Frequency	60 Hz
Load	100 %

Primary inrush current	12.3 A
Secondary inrush current	8.9 A





Model	LGA100A-5-Y	Temperature Testing Circuitry	25°C Figure B
Item	Leakage Current		
Object	<hr/>		

### 1. Results

Standards	Leakage Current [mA]		
	Input Volt. 100 [V]	Input Volt. 120 [V]	Input Volt. 132 [V]
(A)DEN-AN	0.29	0.37	0.41
(B)IEC60950	0.29	0.35	0.40

frequency 60Hz

### 2. Condition

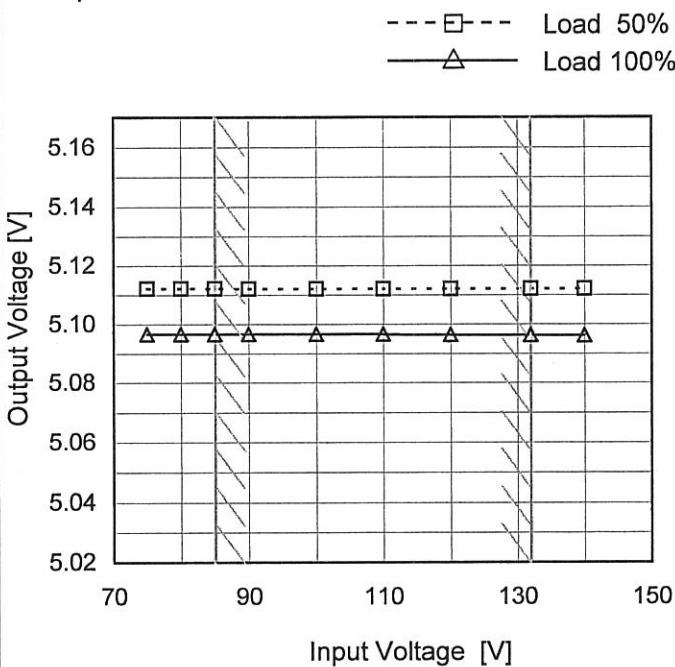
Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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Model	LGA100A-5-Y
Item	Line Regulation
Object	+5V20A

Temperature 25°C  
 Testing Circuitry Figure A

## 1. Graph



## 2. Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
75	5.112	5.097
80	5.112	5.097
85	5.112	5.097
90	5.112	5.097
100	5.112	5.097
110	5.112	5.097
120	5.112	5.097
132	5.112	5.096
140	5.112	5.096

Note: Slanted line shows the range of the rated input voltage.

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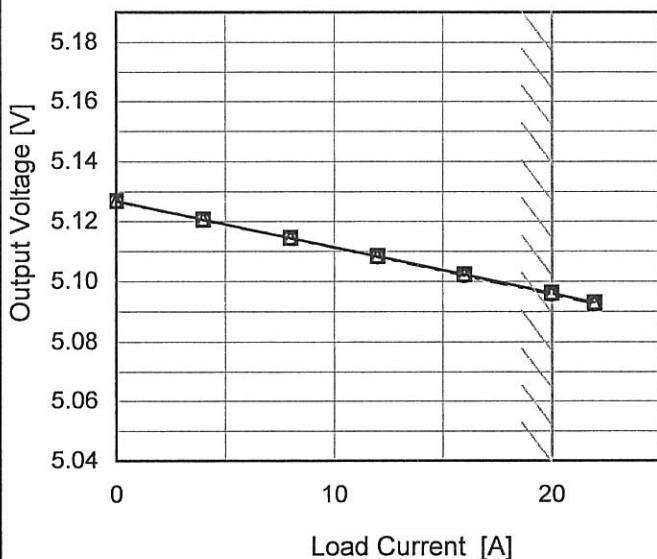
Model LGA100A-5-Y

Item Load Regulation

Object +5V20A

1.Graph

—△— Input Volt. 85V  
 - - -□--- Input Volt. 100V  
 - -○--- Input Volt. 132V



Note: Slanted line shows the range of the rated load current.

 Temperature 25°C  
 Testing Circuitry Figure A

2.Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	5.127	5.127	5.127
4	5.121	5.121	5.121
8	5.115	5.115	5.114
12	5.108	5.108	5.108
16	5.102	5.102	5.102
20	5.096	5.096	5.096
22	5.093	5.093	5.093
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--	-	-	-
--	-	-	-
--	-	-	-

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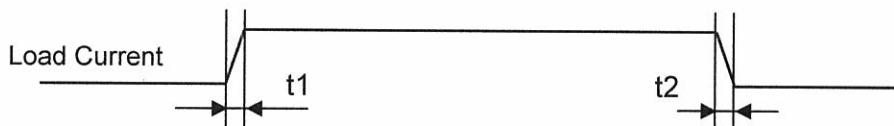
Model LGA100A-5-Y

Item Dynamic Load Response

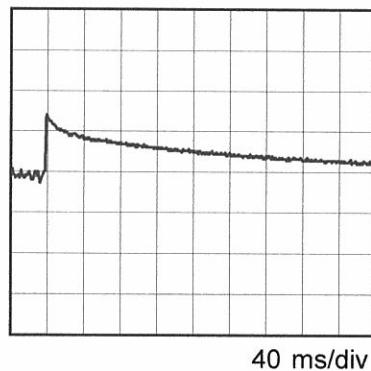
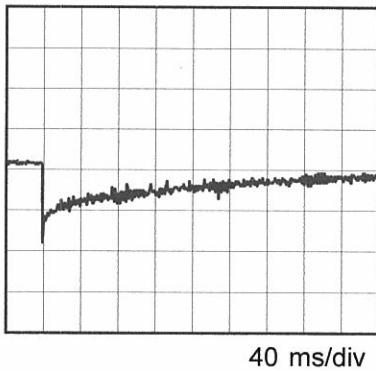
Object +5V20A

Temperature  
Testing Circuitry      25°C  
Figure A

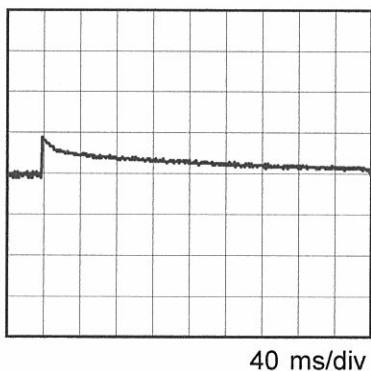
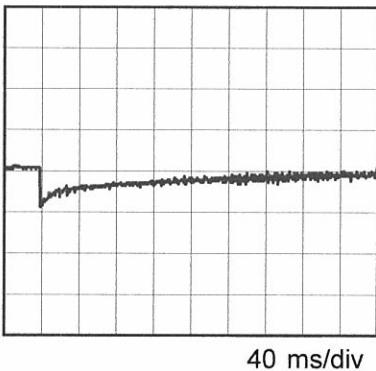
Input Volt. 100 V                  Response.  $t_1=t_2=50\mu s$ . Typ  
 Cycle 1000 ms



Min. Load (0A) ↔  
 Load 100% (20A)



Min. Load (0A) ↔  
 Load 50% (10A)



**COSEL**

Model	LGA100A-5-Y																																							
Item	Ripple Voltage (by Load Current)	Temperature 25°C Testing Circuitry Figure C																																						
Object	+5V20A																																							
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<p>Measured by 20 MHz Oscilloscope. Ripple Voltage is shown as p-p in the figure below. Note: Slanted line shows the range of the rated load current.</p> <p>T1: Due to AC Input Line T2: Due to Switching</p> <p>Fig. Complex Ripple Wave Form</p>																																								

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1. Graph																																							
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Fig. Complex Ripple Wave Form																																							

Model	LGA100A-5-Y	Testing Circuitry FigureC
Item	Ripple Voltage (by Ambient Temp.)	
Object	+5V20A	
1. Graph		2. Values
<p>Graph showing Ripple Voltage [mV] vs Ambient Temperature [°C]. The graph shows a decreasing trend of ripple voltage as ambient temperature increases from -30°C to 50°C. A slanted line indicates the range of rated ambient temperature between approximately -10°C and 25°C.</p> <p>Input Volt. 100V Input Load. 100%</p>		
<p>Measured by 20 MHz Oscilloscope. Ripple Voltage is shown as p-p in the figure below. Note: Slanted line shows the range of the rated ambient temperature.</p> <p>T1: Due to AC Input Line T2: Due to Switching</p> <p>Fig. Complex Ripple Wave Form</p>		
		BC-10156



Model	LGA100A-5-Y																																																					
Item	Ambient Temperature Drift																																																					
Object	+5V20A																																																					
1.Graph	<p style="text-align: center;"> <span style="color: black;">—△—</span> Input Volt. 85V  <span style="color: gray;">---□---</span> Input Volt. 100V  <span style="color: red;">---○---</span> Input Volt. 132V         </p> <p style="text-align: center;">Output Voltage [V]</p> <p style="text-align: center;">Ambient Temperature [°C]</p> <p style="text-align: center;">Load 100%</p>																																																					
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Model	LGA100A-5-Y	Testing Circuitry Figure A
Item	Output Voltage Accuracy	
Object	+5V20A	

### 1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10 - 50°C

Input Voltage : 85 - 132V

Load Current : 0 - 20A

\* Output Voltage Accuracy =  $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

$$\text{* Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

### 2. Values

Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ration [%]
Maximum Voltage	40	85	0	5.128	$\pm 16$	$\pm 0.3$
Minimum Voltage	50	132	20	5.096		

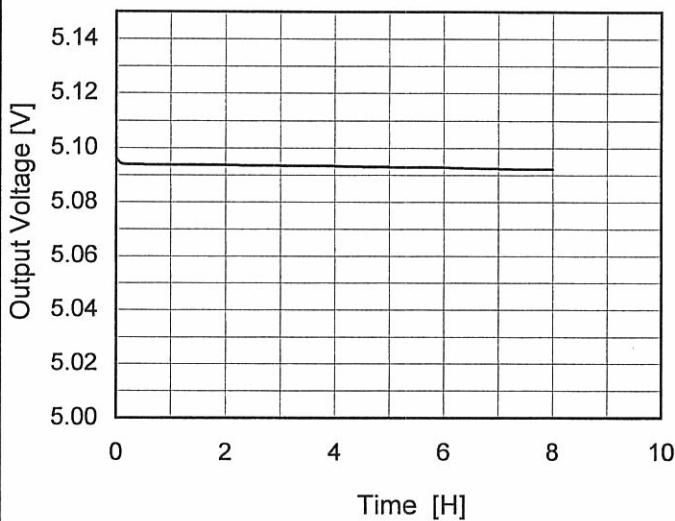
**COSEL**

Model LGA100A-5-Y

Item Time Lapse Drift

Object +5V20A

## 1. Graph



Input Volt. 100V  
Load 100%

Temperature 25°C  
Testing Circuitry Figure A

## 2. Values

Time since start [H]	Output Voltage [V]
0.0	5.096
0.5	5.094
1.0	5.094
2.0	5.094
3.0	5.094
4.0	5.093
5.0	5.093
6.0	5.093
7.0	5.093
8.0	5.092

**COSEL**

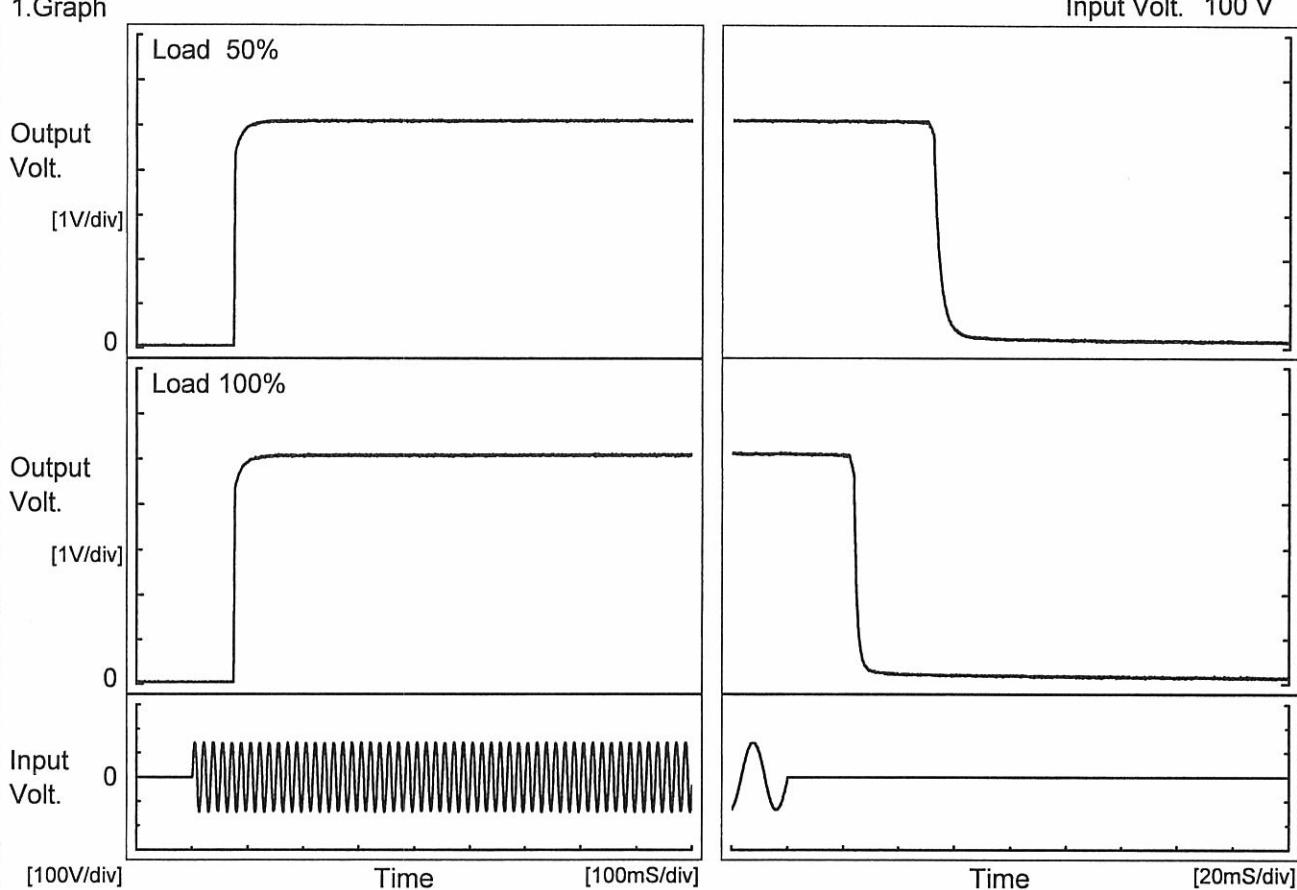
Model LGA100A-5-Y

Item Rise and Fall Time

Object +5V20A

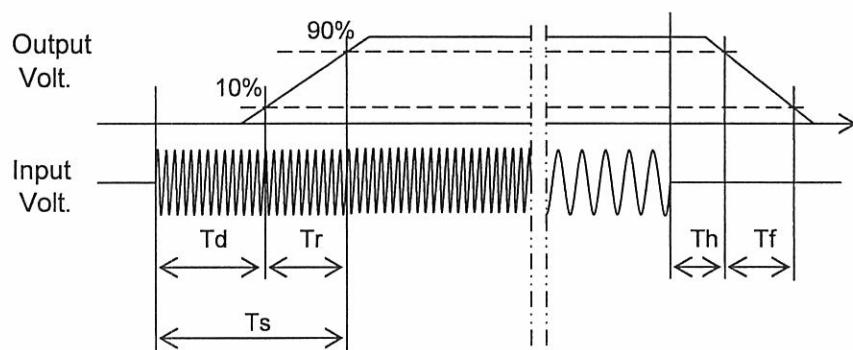
Temperature 25°C  
Testing Circuitry Figure A

## 1. Graph



## 2. Values

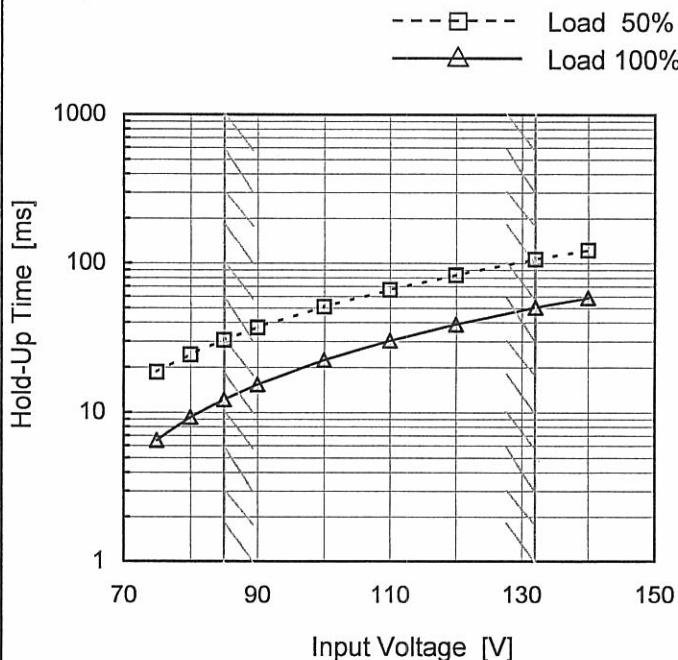
Load	Time	Td	Tr	Ts	Th	Tf	[mS]
50 %		75.5	5.0	80.5	52.2	6.3	
100 %		75.0	5.5	80.5	23.8	3.1	





Model	LGA100A-5-Y
Item	Hold-Up Time
Object	+5V20A

## 1. Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.  
Note: Slanted line shows the range of the rated input voltage.

Temperature 25°C  
Testing Circuitry Figure A

## 2. Values

Input Voltage [V]	Hold-Up Time [ms]	
	Load 50%	Load 100%
75	19	7
80	24	9
85	30	12
90	37	15
100	51	22
110	66	30
120	83	39
132	106	50
140	122	59

**COSEL**

Model	LGA100A-5-Y																																																				
Item	Instantaneous Interruption Compensation																																																				
Object	+5V20A																																																				
1.Graph	<p>Legend:</p> <ul style="list-style-type: none"> <li>Input Volt. 85V</li> <li>Input Volt. 100V</li> <li>Input Volt. 132V</li> </ul> <p>Note: Slanted line shows the range of the rated load current.</p>																																																				
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<p>Model      LGA100A-5-Y</p> <p>Item      Overcurrent Protection</p> <p>Object    +5V20A</p> <p>1.Graph</p> <p>Note: Slanted line shows the range of the rated load current.</p> <p>Intermittent operation occurs when the output voltage is from 2.9V to 0V.</p>	<p>Temperature      25°C</p> <p>Testing Circuitry      Figure A</p> <p>2.Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Output Voltage [V]</th><th colspan="3">Load Current [A]</th></tr> <tr> <th>Input Volt. 85[V]</th><th>Input Volt. 100[V]</th><th>Input Volt. 132[V]</th></tr> </thead> <tbody> <tr><td>5.00</td><td>21.65</td><td>21.62</td><td>21.59</td></tr> <tr><td>4.75</td><td>23.06</td><td>23.07</td><td>23.07</td></tr> <tr><td>4.50</td><td>23.06</td><td>23.09</td><td>23.09</td></tr> <tr><td>4.00</td><td>23.06</td><td>23.12</td><td>23.12</td></tr> <tr><td>3.50</td><td>23.15</td><td>23.23</td><td>23.09</td></tr> <tr><td>3.00</td><td>23.43</td><td>23.54</td><td>23.46</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> </tbody> </table>	Output Voltage [V]	Load Current [A]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	5.00	21.65	21.62	21.59	4.75	23.06	23.07	23.07	4.50	23.06	23.09	23.09	4.00	23.06	23.12	23.12	3.50	23.15	23.23	23.09	3.00	23.43	23.54	23.46	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-
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Model	LGA100A-5-Y
Item	Overvoltage Protection
Object	+5V20A
1.Graph	
<p style="text-align: center;"> <span style="display: inline-block; width: 15px; height: 15px; background-color: black; border: 1px solid black;"></span> Input Volt. 85V  <span style="display: inline-block; width: 15px; height: 15px; border: 1px dashed black;"></span> Input Volt. 100V  <span style="display: inline-block; width: 15px; height: 15px; border: 1px dashed black; border-radius: 50%;"></span> Input Volt. 132V     </p> <p style="text-align: center;">Operating Point [V]</p> <p style="text-align: center;">Ambient Temperature [°C]</p> <p style="text-align: center;">Load 0%</p>	

## Testing Circuitry Figure A

## 2.Values

Ambient Temperature [°C]	Operating Point [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
-20	6.42	6.42	6.42
-10	6.42	6.42	6.42
0	6.42	6.42	6.42
10	6.42	6.42	6.42
20	6.42	6.42	6.42
25	6.41	6.42	6.42
30	6.42	6.42	6.42
40	6.41	6.42	6.42
50	6.41	6.42	6.42
60	6.41	6.42	6.42
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Note: Slanted line shows the range of the rated ambient temperature.

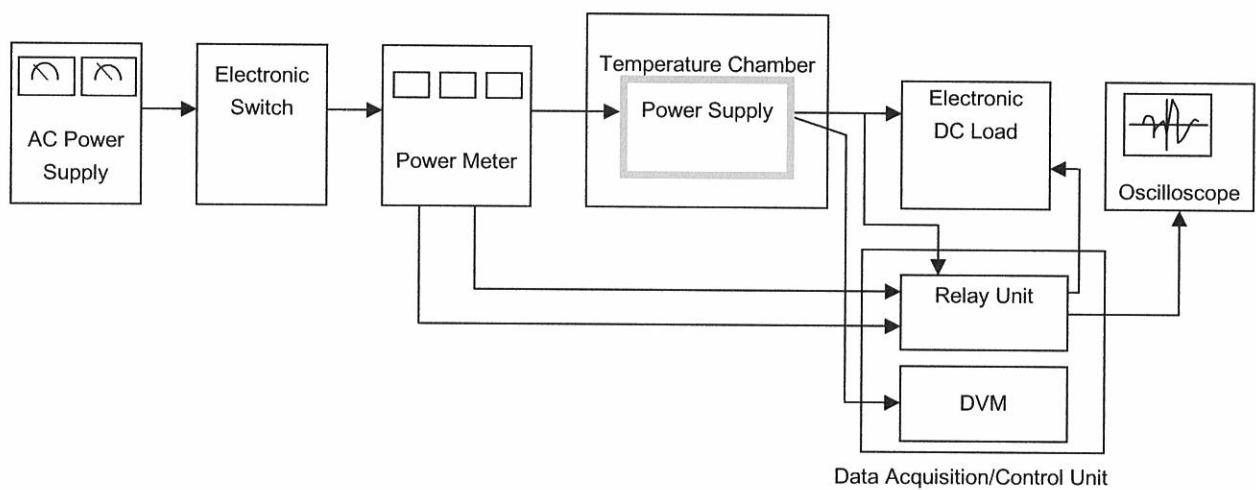


Figure A

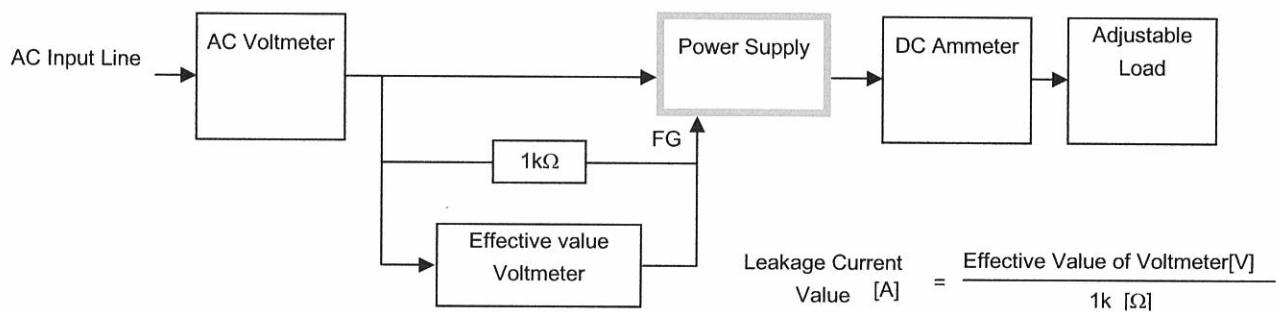


Figure B ( DEN-AN )

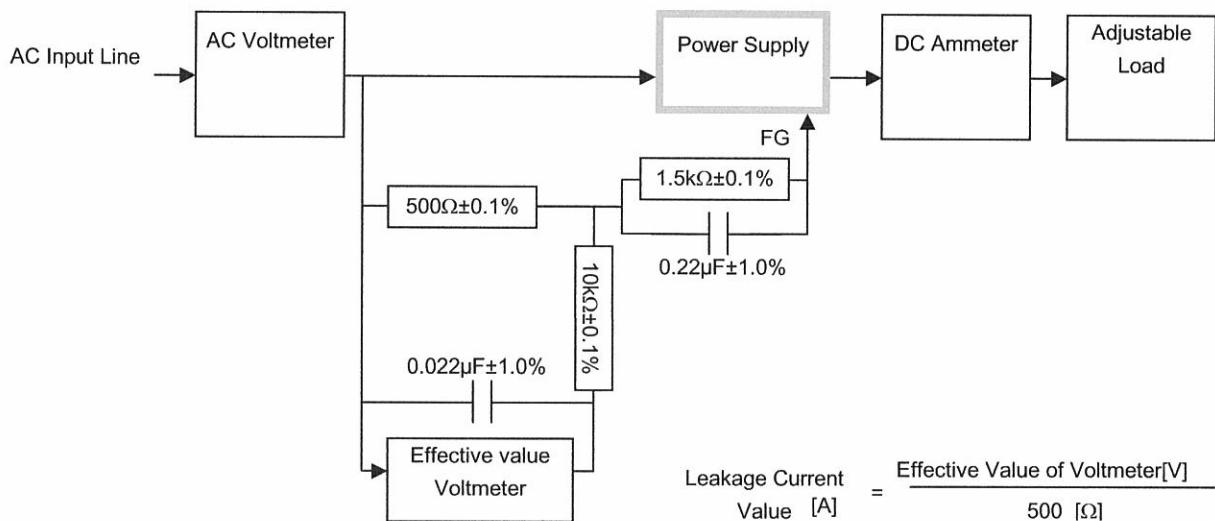


Figure B ( IEC60950-1 )

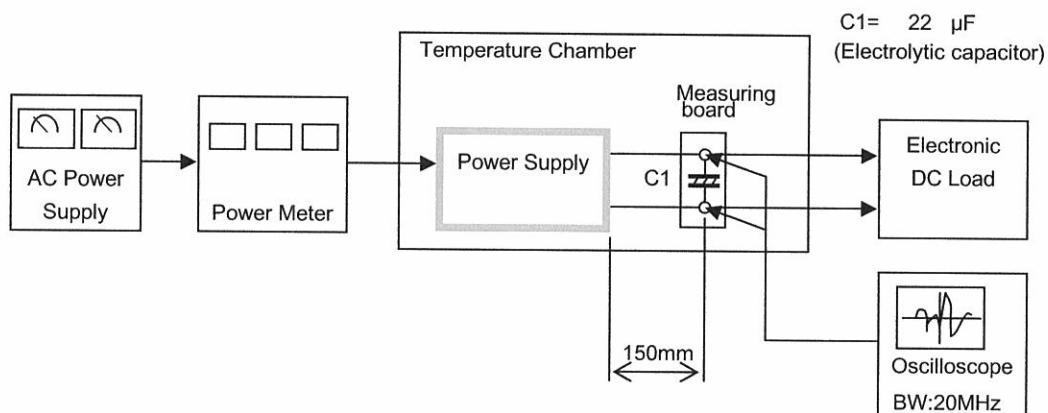


Figure C