



TEST DATA OF LFA15F-3R3-Y

Regulated DC Power Supply
June 26, 2009

Approved by : Yoshiaki Shimizu
Yoshiaki Shimizu Design Manager

Prepared by : Yuki Nakamura
Yuki Nakamura Design Engineer

COSEL CO.,LTD.

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<p>The graph plots Efficiency [%] on the y-axis (30 to 86) against Input Voltage [V] on the x-axis (50 to 300). Two data series are shown: Load 50% (dashed line with square markers) and Load 100% (solid line with triangle markers). Both series show efficiency increasing with input voltage, with the Load 100% curve generally higher than the Load 50% curve. A slanted line on the graph indicates the rated input voltage range.</p> <table border="1"> <thead> <tr> <th>Input Voltage [V]</th> <th>Efficiency Load 50% [%]</th> <th>Efficiency Load 100% [%]</th> </tr> </thead> <tbody> <tr><td>75</td><td>68.2</td><td>66.9</td></tr> <tr><td>85</td><td>68.8</td><td>68.6</td></tr> <tr><td>100</td><td>69.4</td><td>70.3</td></tr> <tr><td>120</td><td>69.7</td><td>71.4</td></tr> <tr><td>200</td><td>68.7</td><td>72.0</td></tr> <tr><td>230</td><td>67.0</td><td>71.5</td></tr> <tr><td>264</td><td>65.3</td><td>70.6</td></tr> <tr><td>280</td><td>64.2</td><td>70.3</td></tr> <tr><td>--</td><td>-</td><td>-</td></tr> </tbody> </table>			Input Voltage [V]	Efficiency Load 50% [%]	Efficiency Load 100% [%]	75	68.2	66.9	85	68.8	68.6	100	69.4	70.3	120	69.7	71.4	200	68.7	72.0	230	67.0	71.5	264	65.3	70.6	280	64.2	70.3	--	-	-		
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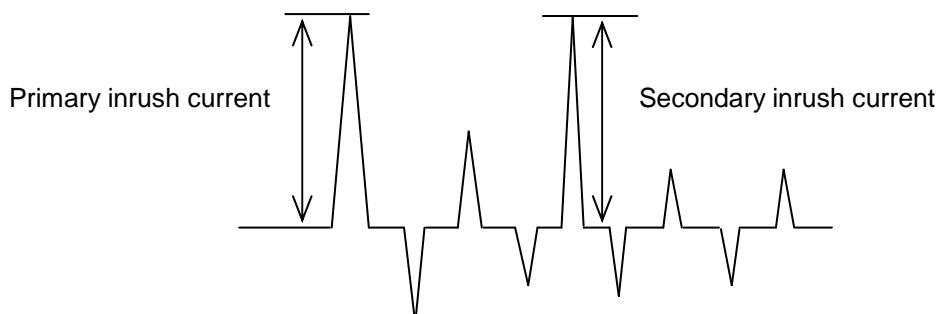
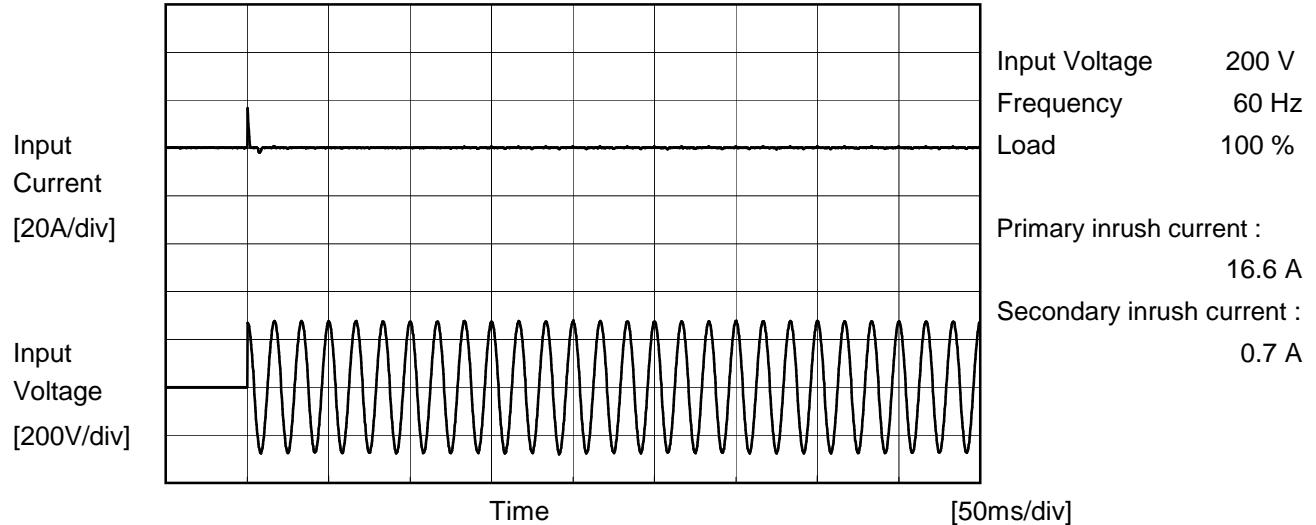
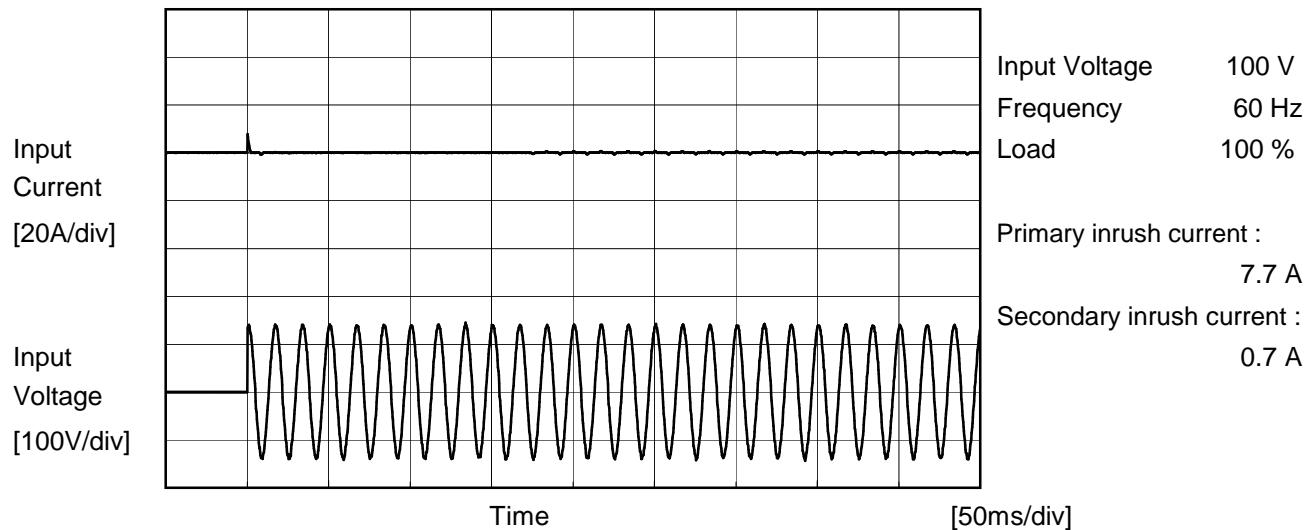
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Model	LFA15F-3R3-Y	Temperature	25°C
Item	Inrush Current	Testing Circuitry	Figure A
Object	<hr/>		





Model	LFA15F-3R3-Y	Temperature	25°C
Item	Leakage Current	Testing Circuitry	Figure B
Object	_____		

1. Results

[mA]

Standards		Input Volt.			Note
		100 [V]	200 [V]	240 [V]	
DEN-AN	Both phases	0.07	0.14	0.16	Operation
	One of phase	0.13	0.27	0.33	stand by
IEC60950-1	Both phases	0.09	0.19	0.20	Operation
	One of phase	0.13	0.28	0.31	stand by

The value for "One phase" is the reference value only.

2. Condition

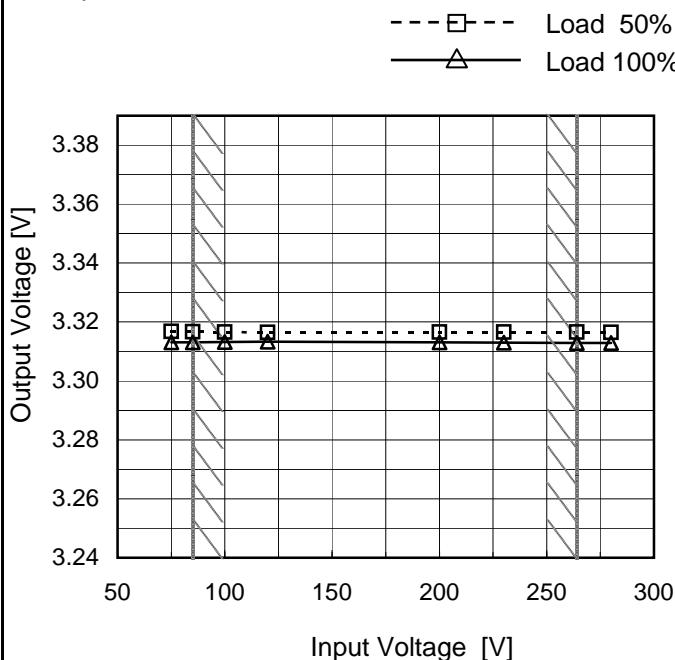
Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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Model	LFA15F-3R3-Y
Item	Line Regulation
Object	+3.3V3A

 Temperature 25°C
 Testing Circuitry Figure A

1.Graph



2.Values

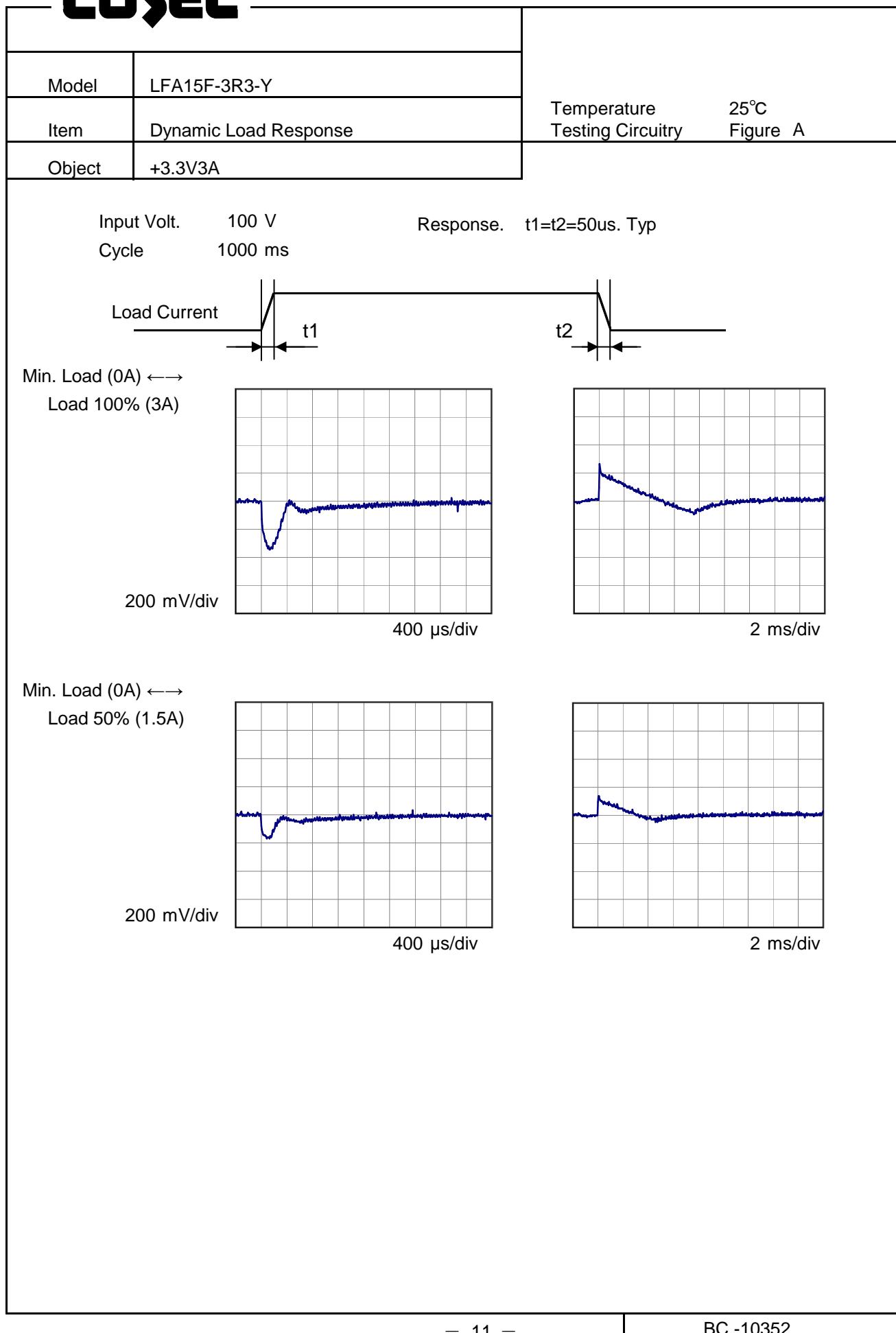
Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
75	3.317	3.313
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	<p>Output Voltage [V]</p> <p>Load Current [A]</p>	<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Output Voltage [V]</th> </tr> <tr> <th>Input Volt. 100[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 230[V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>3.319</td><td>3.319</td><td>3.319</td></tr> <tr><td>0.6</td><td>3.317</td><td>3.318</td><td>3.317</td></tr> <tr><td>1.2</td><td>3.316</td><td>3.316</td><td>3.316</td></tr> <tr><td>1.8</td><td>3.315</td><td>3.314</td><td>3.314</td></tr> <tr><td>2.4</td><td>3.313</td><td>3.313</td><td>3.313</td></tr> <tr><td>3.0</td><td>3.312</td><td>3.312</td><td>3.312</td></tr> <tr><td>3.3</td><td>3.311</td><td>3.311</td><td>3.311</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> </tbody> </table>	Load Current [A]	Output Voltage [V]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	0.0	3.319	3.319	3.319	0.6	3.317	3.318	3.317	1.2	3.316	3.316	3.316	1.8	3.315	3.314	3.314	2.4	3.313	3.313	3.313	3.0	3.312	3.312	3.312	3.3	3.311	3.311	3.311	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-	
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	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]																																																			
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Note: Slanted line shows the range of the rated load current.

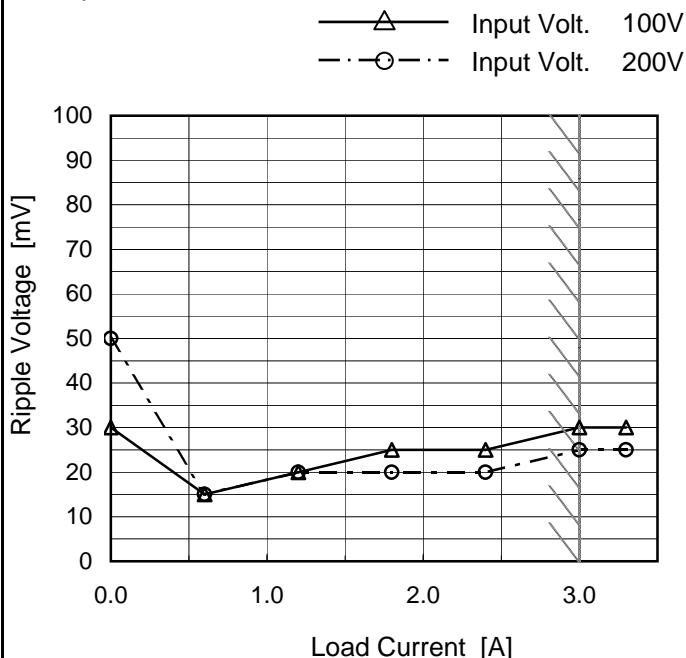
COSEL

COSEL

Model	LFA15F-3R3-Y
Item	Ripple Voltage (by Load Current)
Object	+3.3V3A

 Temperature 25°C
 Testing Circuitry Figure C

1.Graph



2.Values

Load Current [A]	Ripple Voltage [mV]	
	Input Volt. 100 [V]	Input Volt. 200 [V]
0.0	30	50
0.6	15	15
1.2	20	20
1.8	25	20
2.4	25	20
3.0	30	25
3.3	30	25
--	-	-
--	-	-
--	-	-
--	-	-

Measured by 20 MHz Oscilloscope.

Ripple Voltage is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.
 T1: Due to AC Input Line
 T2: Due to Switching

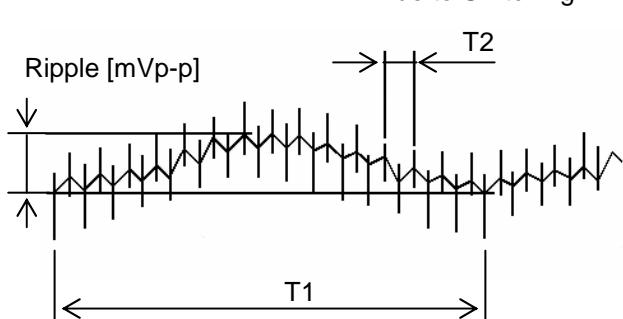


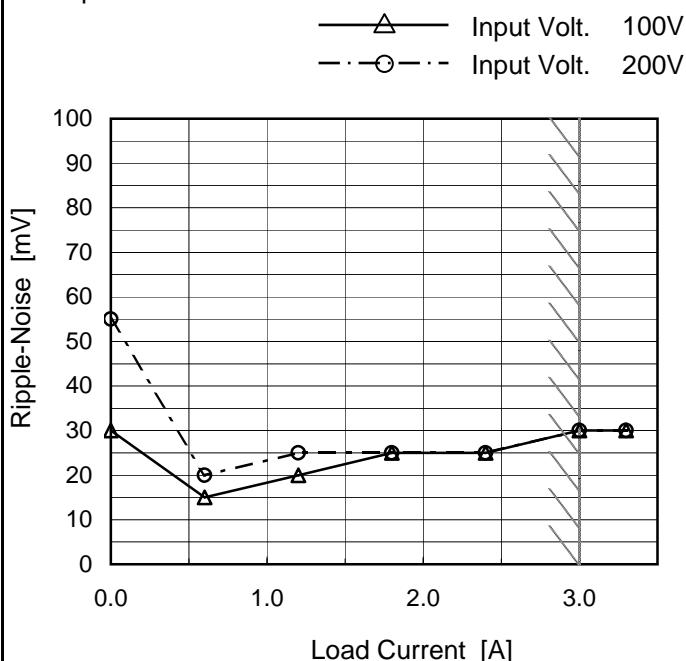
Fig. Complex Ripple Wave Form

COSEL

Model	LFA15F-3R3-Y
Item	Ripple-Noise
Object	+3.3V3A

Temperature 25°C
Testing Circuitry Figure C

1.Graph



Measured by 20 MHz Oscilloscope.

Ripple-Noise is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

2.Values

Load Current [A]	Ripple-Noise [mV]	
	Input Volt. 100 [V]	Input Volt. 200 [V]
0.0	30	55
0.6	15	20
1.2	20	25
1.8	25	25
2.4	25	25
3.0	30	30
3.3	30	30
--	-	-
--	-	-
--	-	-
--	-	-

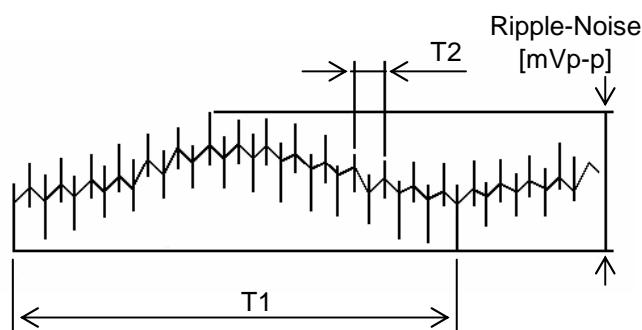
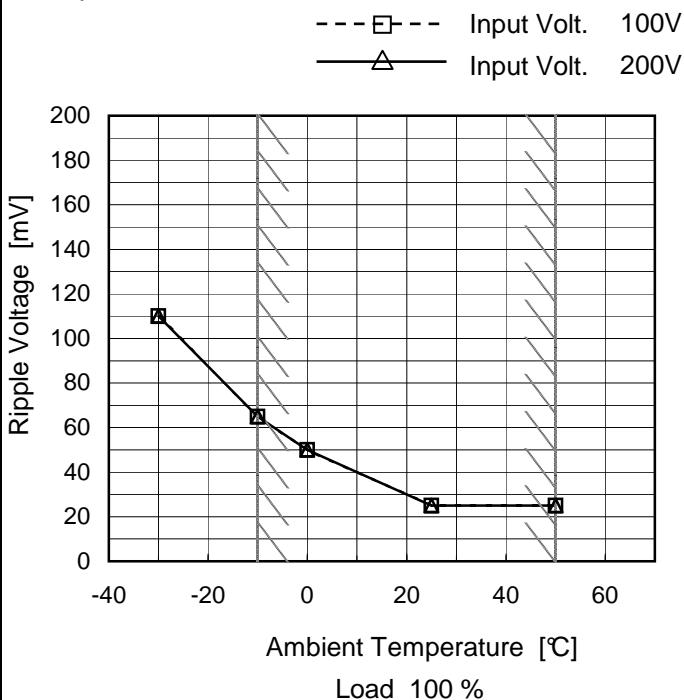
T1: Due to AC Input Line
T2: Due to Switching

Fig. Complex Ripple Wave Form

COSEL

Model	LFA15F-3R3-Y
Item	Ripple Voltage (by Ambient Temp.)
Object	+3.3V3A

1. Graph



Testing Circuitry Figure C

2. Values

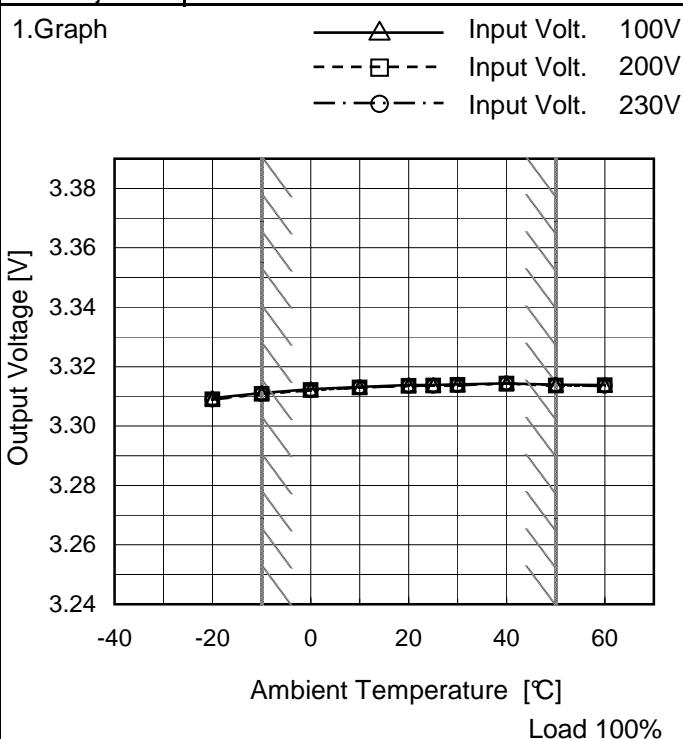
Ambient Temperature [°C]	Ripple Voltage [mV]	
	Input Volt. 100 [V]	Input Volt. 200 [V]
-30	110	110
-10	65	65
0	50	50
25	25	25
50	25	25
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-

Measured by 20 MHz Oscilloscope.

Note: Slanted line shows the range of the rated ambient temperature.

COSEL

Model	LFA15F-3R3-Y
Item	Ambient Temperature Drift
Object	+3.3V3A



Testing Circuitry Figure A

2.Values

Ambient Temperature [°C]	Output Voltage [V]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
-20	3.309	3.309	3.309
-10	3.311	3.311	3.311
0	3.312	3.312	3.312
10	3.313	3.313	3.313
20	3.314	3.314	3.314
25	3.314	3.314	3.314
30	3.314	3.314	3.314
40	3.314	3.314	3.314
50	3.314	3.314	3.314
60	3.314	3.314	3.314
--	-	-	-

Note: Slanted line shows the range of the rated ambient temperature.



Model	LFA15F-3R3-Y	Testing Circuitry Figure A
Item	Output Voltage Accuracy	
Object	+3.3V3A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10 - 50°C

Input Voltage : 85 - 264V

Load Current : 0 - 3A

* Output Voltage Accuracy = $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

$$\text{* Output Voltage Accuracy (Ratio)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

2. Values

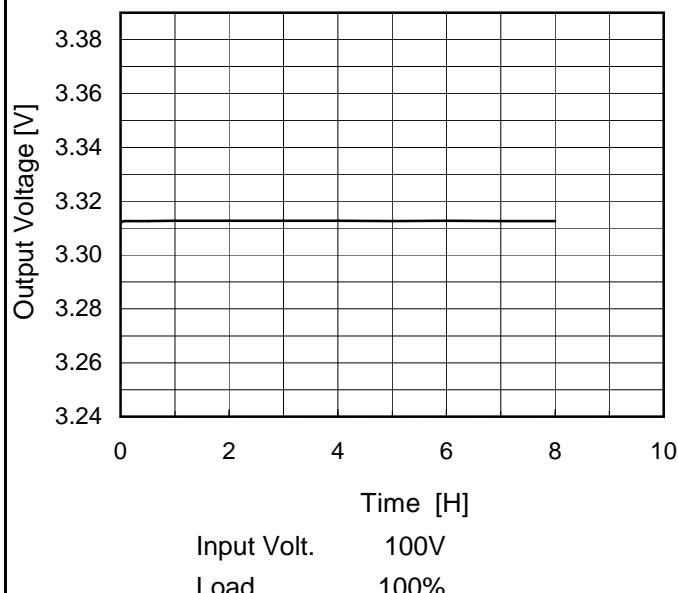
Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ration [%]
Maximum Voltage	40	85	0	3.322	± 6	± 0.2
Minimum Voltage	-10	264	3	3.311		

COSEL

Model	LFA15F-3R3-Y
Item	Time Lapse Drift
Object	+3.3V3A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



2.Values

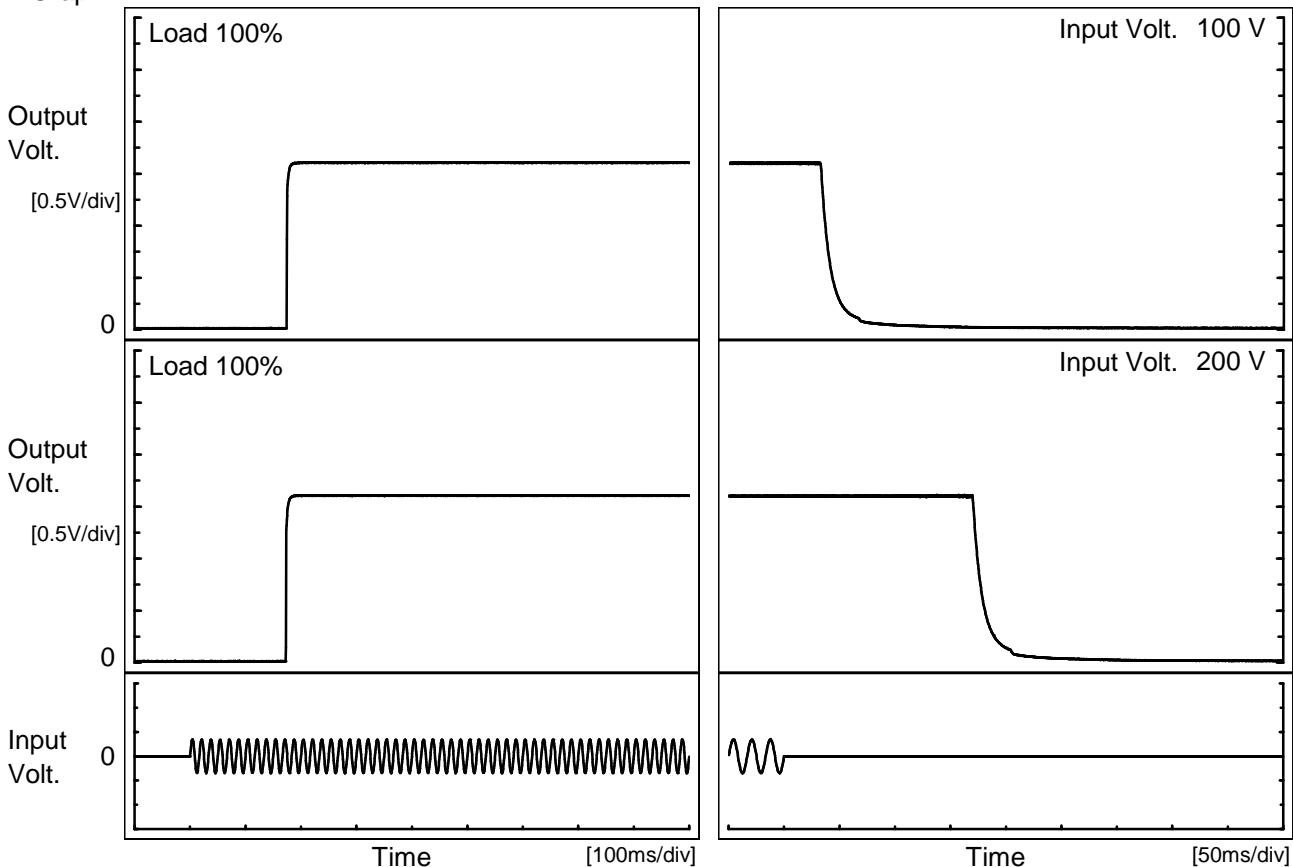
Time since start [H]	Output Voltage [V]
0.0	3.312
0.5	3.313
1.0	3.313
2.0	3.313
3.0	3.313
4.0	3.313
5.0	3.313
6.0	3.313
7.0	3.313
8.0	3.313

* The characteristic of AC200V is equal.

COSEL

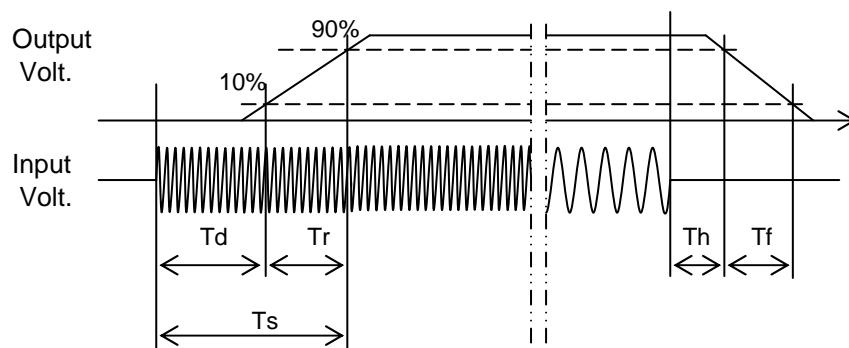
Model	LFA15F-3R3-Y	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+3.3V3A		

1. Graph



2. Values

Input Volt.	Time	Td	Tr	Ts	Th	Tf	[ms]
100 V		174.5	3.5	178.0	33.0	23.3	
200 V		173.5	3.0	176.5	170.5	25.0	

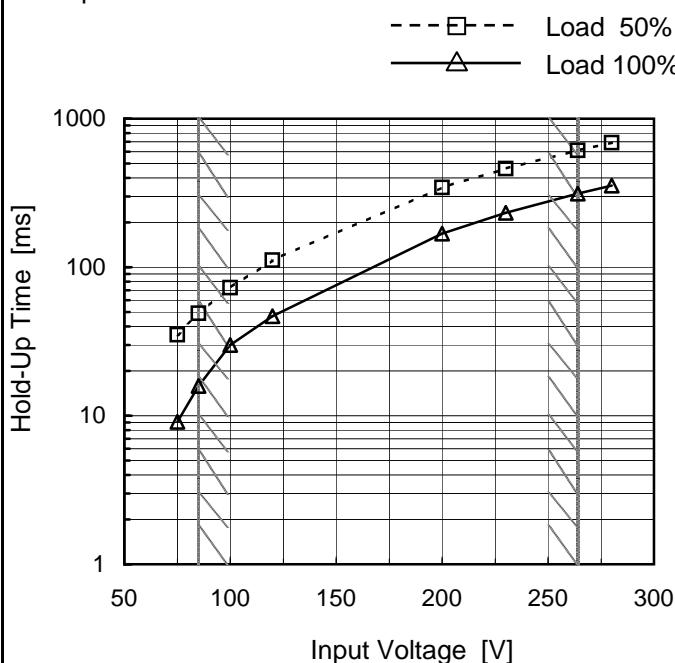


COSEL

Model	LFA15F-3R3-Y
Item	Hold-Up Time
Object	+3.3V3A

 Temperature 25°C
 Testing Circuitry Figure A

1.Graph



2.Values

Input Voltage [V]	Hold-Up Time [ms]	
	Load 50%	Load 100%
75	35	9
85	49	16
100	73	30
120	112	47
200	344	168
230	460	233
264	612	313
280	691	355
--	-	-

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.
 Note: Slanted line shows the range of the rated input voltage.

COSEL

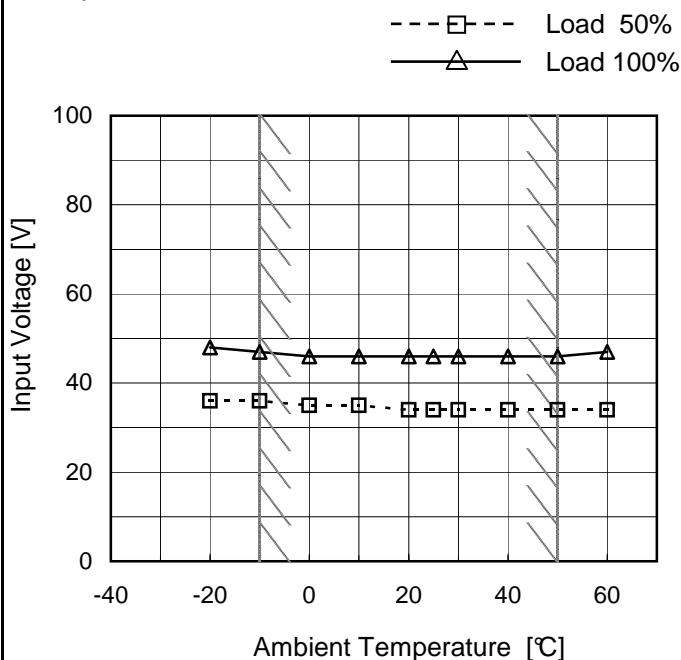
Model	LFA15F-3R3-Y																																																				
Item	Instantaneous Interruption Compensation	Temperature Testing Circuitry	25°C Figure A																																																		
Object	+3.3V3A																																																				
1.Graph	<p style="text-align: center;"> —△— Input Volt. 100V - -□- Input Volt. 200V - -○- Input Volt. 230V </p> <table border="1"> <caption>Data points estimated from Graph 1</caption> <thead> <tr> <th>Load Current [A]</th> <th>100V [ms]</th> <th>200V [ms]</th> <th>230V [ms]</th> </tr> </thead> <tbody> <tr><td>0.6</td><td>176</td><td>761</td><td>990</td></tr> <tr><td>1.2</td><td>95</td><td>424</td><td>567</td></tr> <tr><td>1.8</td><td>60</td><td>291</td><td>397</td></tr> <tr><td>2.4</td><td>39</td><td>220</td><td>290</td></tr> <tr><td>3.0</td><td>30</td><td>168</td><td>230</td></tr> </tbody> </table>	Load Current [A]	100V [ms]	200V [ms]	230V [ms]	0.6	176	761	990	1.2	95	424	567	1.8	60	291	397	2.4	39	220	290	3.0	30	168	230	2.Values																											
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			<p>Note: Slanted line shows the range of the rated load current.</p>																																																		

COSEL

Model	LFA15F-3R3-Y
Item	Minimum Input Voltage for Regulated Output Voltage
Object	+3.3V3A

Testing Circuitry Figure A

1.Graph



2.Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	36	48
-10	36	47
0	35	46
10	35	46
20	34	46
25	34	46
30	34	46
40	34	46
50	34	46
60	34	47
--	-	-

Note: Slanted line shows the range of the rated ambient temperature.

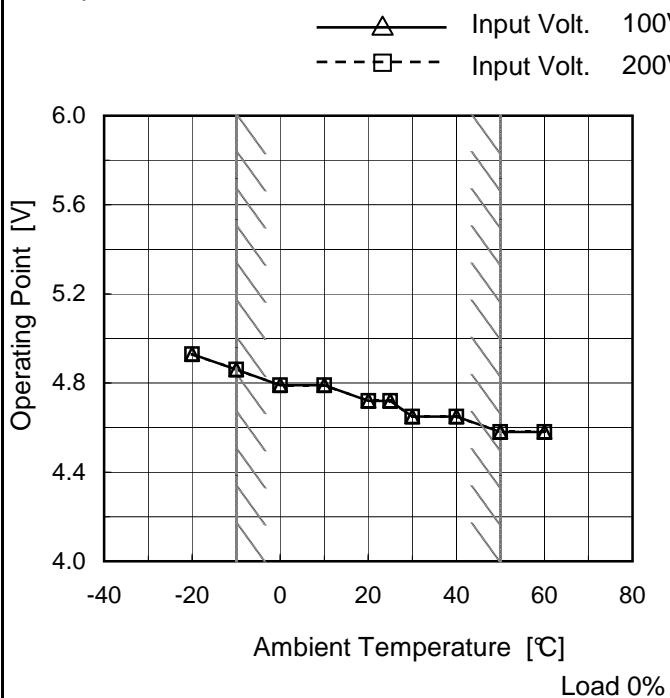
COSEL

Model	LFA15F-3R3-Y																																										
Item	Overcurrent Protection	Temperature 25°C Testing Circuitry Figure A																																									
Object	+3.3V3A																																										
1.Graph																																											
<p>Output Voltage [V]</p> <p>Load Current [A]</p> <p>Input Volt. 100V</p> <p>Input Volt. 200V</p>																																											
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0.000	-	-																																									

COSEL

Model	LFA15F-3R3-Y
Item	Overvoltage Protection
Object	+3.3V3A

1.Graph



Testing Circuitry Figure A

2.Values

Ambient Temperature [°C]	Operating Point [V]	
	Input Volt. 100[V]	Input Volt. 200[V]
-20	4.93	4.93
-10	4.86	4.86
0	4.79	4.79
10	4.79	4.79
20	4.72	4.72
25	4.72	4.72
30	4.65	4.65
40	4.65	4.65
50	4.58	4.58
60	4.58	4.58
--	-	-

Note: Slanted line shows the range of the rated ambient temperature.

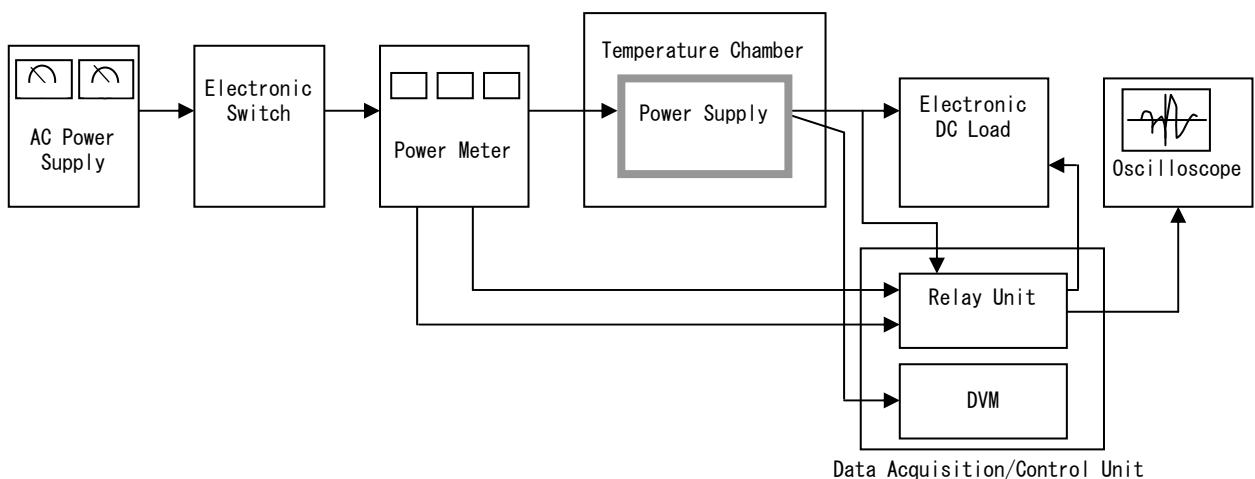


Figure A

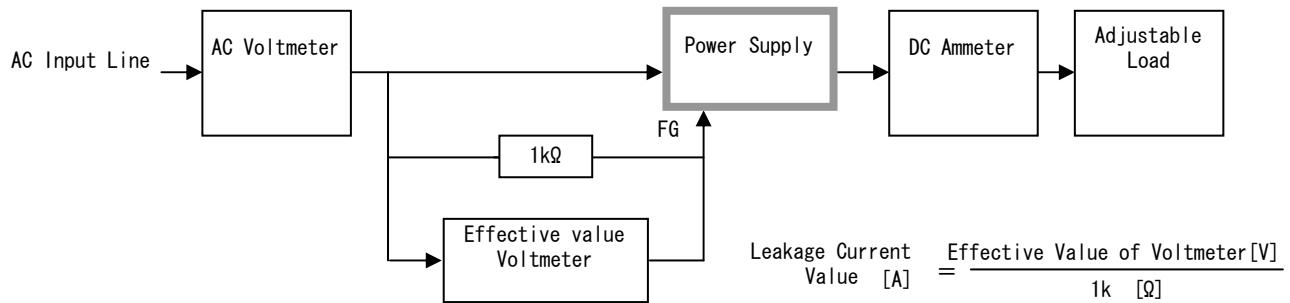


Figure B (DEN-AN)

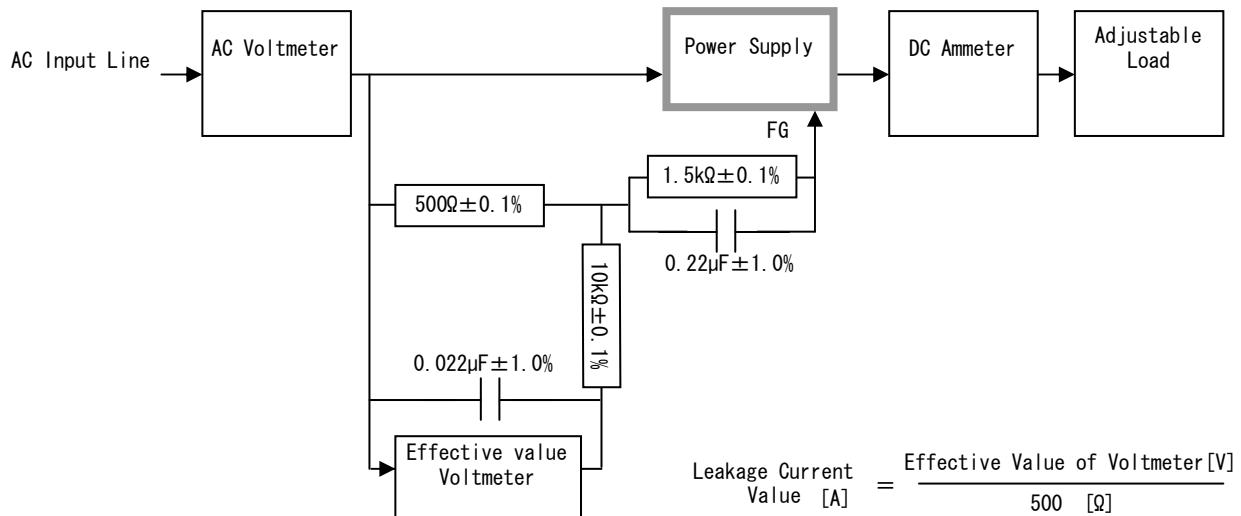


Figure B (IEC60950-1)

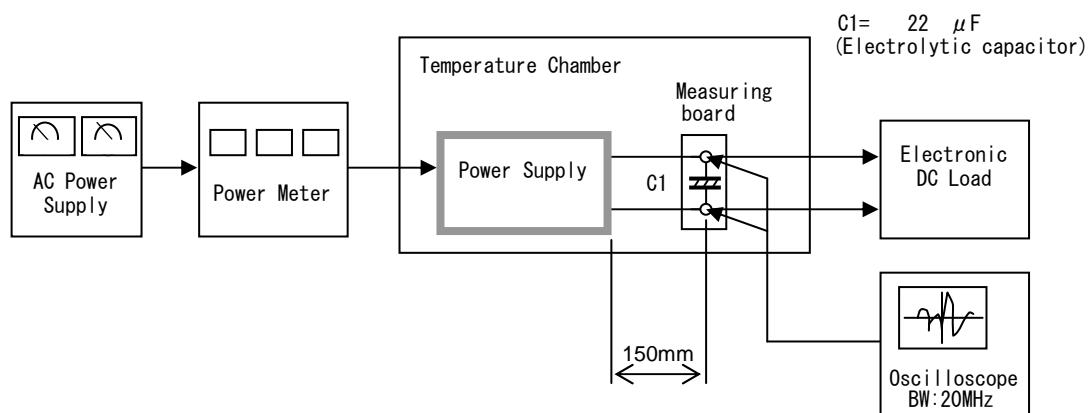


Figure C