



## TEST DATA OF LEA50F-5 (100V INPUT)

Regulated DC Power Supply

Date : Feb. 15. 1999

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Design Manager

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Design Engineer

コーセル株式会社

COSEL CO., LTD.



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Model	LEA50F-5	Temperature Testing Circuitry	25°C Figure A																															
Item	Line Regulation 静的入力変動																																	
Object	+5V10A																																	
1. Graph	<p>Load 50%      □</p> <p>Load 100%     △</p>																																	
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Input Voltage [V]	Load 50%	Load 100%																																
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Note: Slanted line shows the range of the rated input voltage.

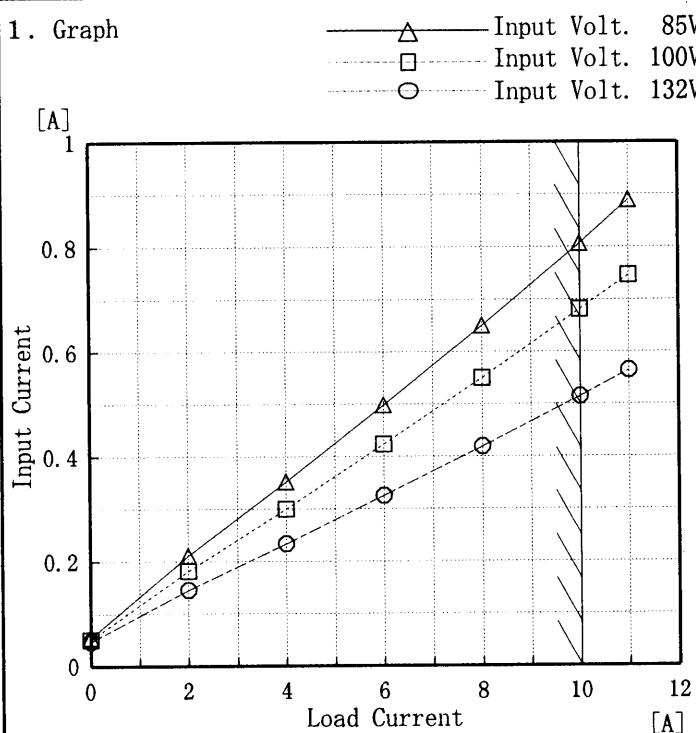
(注)斜線は定格入力電圧範囲を示す。

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Model	LEA50F-5
Item	Input Current (by Load Current) 入力電流（負荷特性）
Output	—

Temperature 25°C  
Testing Circuitry Figure A

## 1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

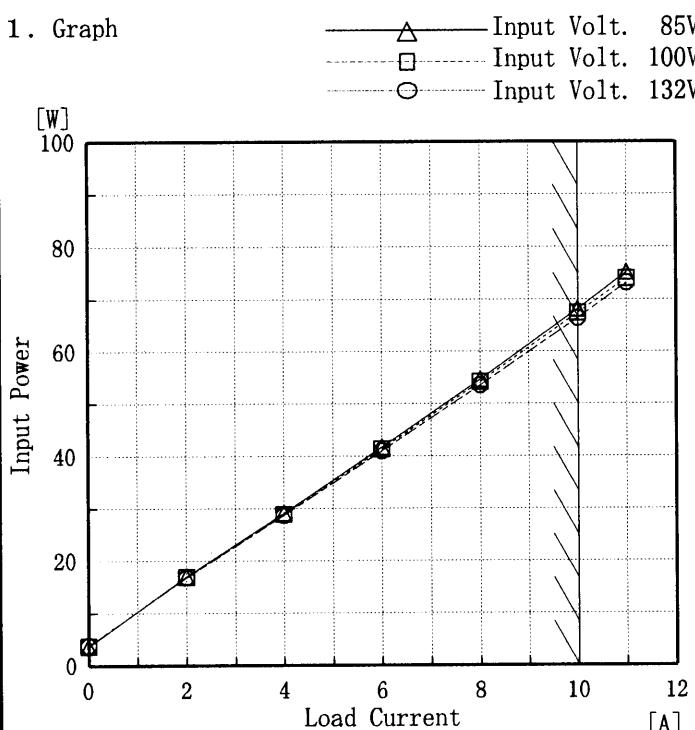
## 2. Values

Load Current [A]	Input Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	0.054	0.050	0.047
2	0.211	0.182	0.145
4	0.352	0.300	0.234
6	0.498	0.423	0.325
8	0.650	0.551	0.419
10	0.806	0.681	0.515
11	0.889	0.746	0.565
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

**COSEL**

Model	LEA50F-5
Item	Input Power (by Load Current) 入力電力 (負荷特性)
Output	_____

## 1. Graph



Temperature 25°C  
Testing Circuitry Figure A

## 2. Values

Load Current [A]	Input Power [W]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	3.59	3.69	3.79
2	16.98	16.89	16.85
4	29.10	28.91	28.74
6	41.71	41.40	41.03
8	54.68	54.23	53.60
10	68.10	67.37	66.40
11	75.10	74.00	73.10
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

Note: Slanted line shows the range of the rated load current

(注) 斜線は定格負荷電流範囲を示す。

**COSEL**

Model	LEA50F-5	
Item	Efficiency (by Input Voltage) 効率(入力電圧特性)	Temperature 25°C Testing Circuitry Figure A
Object	<hr/>	
1. Graph		
[%]	<p>The graph plots Efficiency [%] on the Y-axis (62 to 86) against Input Voltage [V] on the X-axis (0 to 150). Two data series are shown: Load 50% (dashed line with square markers) and Load 100% (solid line with triangle markers). Both series show efficiency increasing slightly with input voltage. A slanted line indicates the rated input voltage range.</p>	
Efficiency	<span style="margin-right: 20px;">Load 50%</span> <span>Load 100%</span>	
Input Voltage	<span style="margin-right: 20px;">[V]</span> <span>[V]</span>	
2. Values		
Input Voltage [V]	Load 50% [%]	Load 100% [%]
75	71.8	73.2
80	72.0	74.1
85	72.2	74.6
90	72.4	74.9
100	72.6	75.4
110	72.8	75.8
120	73.1	76.1
132	73.2	76.5
140	73.3	76.6

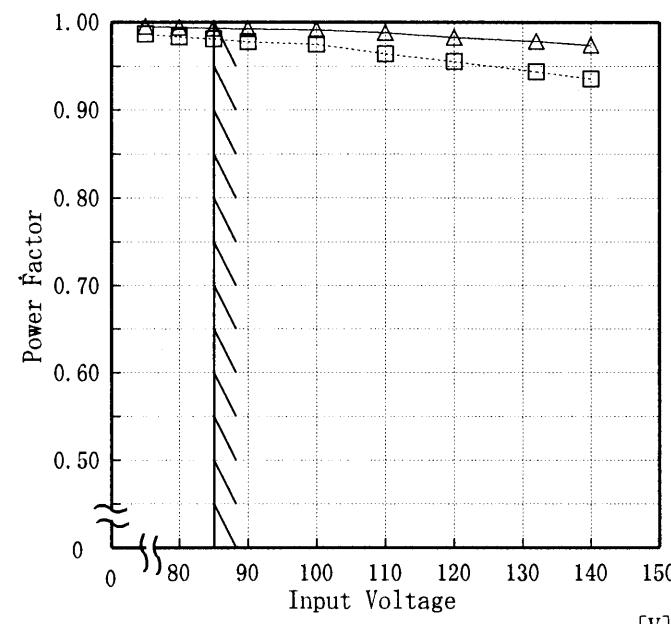
Note: Slanted line shows the range of the rated input voltage.

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Model	LEA50F-5																																	
Item	Power Factor (by Input Voltage) 力率 (入力電圧特性)	Temperature 25°C Testing Circuitry Figure A																																
Object	_____																																	
1. Graph																																		
1.00 0.90 0.80 0.70 0.60 0.50 0 																																		
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Model

LEA50F-5

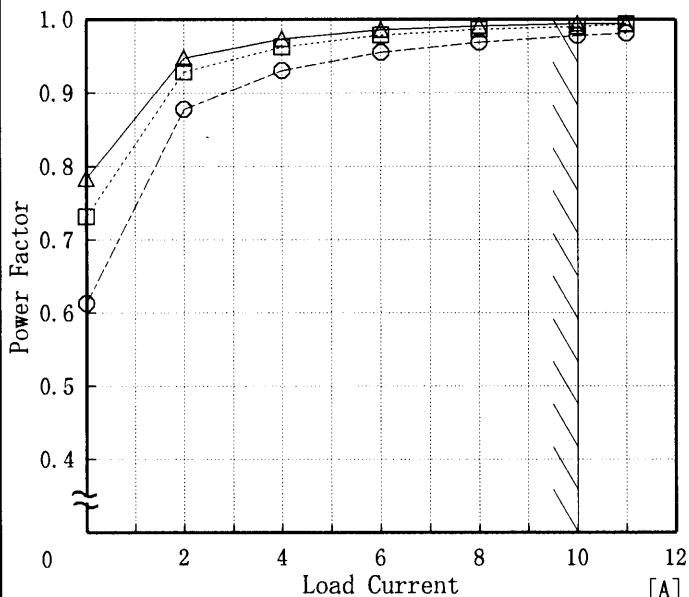
Item

Power Factor (by Load Current)  
力率(負荷電流特性)

Output

1. Graph

—▲— Input Volt. 85V  
 -□- Input Volt. 100V  
 -○- Input Volt. 132V



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

Temperature 25°C  
Testing Circuitry Figure A

2. Values

Load Current [A]	Power Factor		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	0.78	0.73	0.61
2	0.95	0.93	0.88
4	0.97	0.96	0.93
6	0.99	0.98	0.96
8	0.99	0.99	0.97
10	0.99	0.99	0.98
11	0.99	0.99	0.98
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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Model	LEA50F-5	Temperature Testing Circuitry	25°C Figure A																																
Item	Hold-Up Time 出力保持時間																																		
Object	+5V10A																																		
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<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.</p> <p>Note: Slanted line shows the range of the rated input voltage.</p> <p>出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。 (注)斜線は定格入力電圧範囲を示す。</p>																																			

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Model	LEA50F-5	Temperature 25°C Testing Circuitry Figure A		
Item	Instantaneous Interruption Compensation 瞬時停電保障			
Object	+5V10A			
1. Graph				
2. Values	Load Current [A]	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
	Time [mS]			
0	—	—	—	
2	153	162	172	
4	71	79	85	
6	39	47	56	
8	30	36	39	
10	24	29	32	
11	21	26	29	
—	—	—	—	
—	—	—	—	
—	—	—	—	
—	—	—	—	

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated load current.

瞬時停電保障時間とは、出力電圧が定電圧精度の規格範囲を保持している瞬時停電時間をいう。

(注)斜線は定格負荷電流範囲を示す。

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Model	LEA50F-5	Temperature Testing Circuitry      25°C Figure A																																																	
Item	Load Regulation 靜的負荷変動																																																		
Object	+5V10A																																																		
1. Graph	<p>Legend:</p> <ul style="list-style-type: none"> <li>Input Volt. 85V (solid line with triangle)</li> <li>Input Volt. 100V (dashed line with square)</li> <li>Input Volt. 132V (dotted line with circle)</li> </ul> <table border="1"> <thead> <tr> <th>Load Current [A]</th> <th>Output Volt. 85V [V]</th> <th>Output Volt. 100V [V]</th> <th>Output Volt. 132V [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>5.110</td><td>5.110</td><td>5.110</td></tr> <tr><td>2.0</td><td>5.098</td><td>5.098</td><td>5.098</td></tr> <tr><td>4.0</td><td>5.093</td><td>5.093</td><td>5.093</td></tr> <tr><td>6.0</td><td>5.088</td><td>5.088</td><td>5.088</td></tr> <tr><td>8.0</td><td>5.082</td><td>5.082</td><td>5.082</td></tr> <tr><td>10.0</td><td>5.077</td><td>5.077</td><td>5.077</td></tr> <tr><td>11.0</td><td>5.074</td><td>5.074</td><td>5.074</td></tr> </tbody> </table>	Load Current [A]	Output Volt. 85V [V]	Output Volt. 100V [V]	Output Volt. 132V [V]	0.0	5.110	5.110	5.110	2.0	5.098	5.098	5.098	4.0	5.093	5.093	5.093	6.0	5.088	5.088	5.088	8.0	5.082	5.082	5.082	10.0	5.077	5.077	5.077	11.0	5.074	5.074	5.074																		
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Load Current [A]	Input Volt. 85[V]		Input Volt. 100[V]	Input Volt. 132[V]																																															
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—	—	—	—																																																
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Note: Slanted line shows the range of the rated load current.

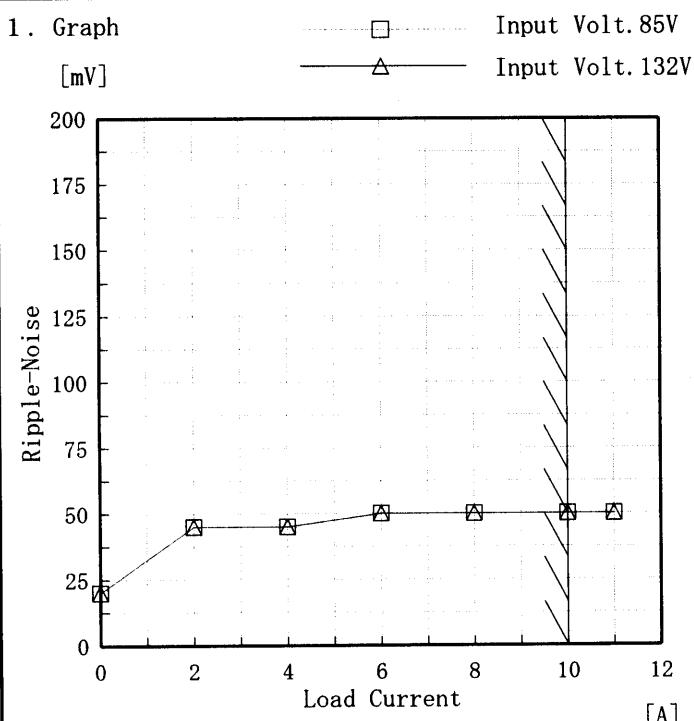
(注)斜線は定格負荷電流範囲を示す。

**COSEL**

Model	LEA50F-5	Temperature Testing Circuitry	25°C Figure A																																			
Item	Ripple Voltage(by Load Current) リップル電圧(負荷電流特性)																																					
Object	+5V10A	2. Values																																				
1. Graph																																						
<p>Legend: □ Input Volt. 85V [mV]      △ Input Volt. 132V [mV]</p> <table border="1"> <thead> <tr> <th>Load Current [A]</th> <th>Ripple Output Volt. 85V [mV]</th> <th>Ripple Output Volt. 132V [mV]</th> </tr> </thead> <tbody> <tr><td>0</td><td>10</td><td>10</td></tr> <tr><td>2</td><td>35</td><td>35</td></tr> <tr><td>4</td><td>35</td><td>35</td></tr> <tr><td>6</td><td>35</td><td>35</td></tr> <tr><td>8</td><td>35</td><td>35</td></tr> <tr><td>10</td><td>35</td><td>35</td></tr> <tr><td>11</td><td>35</td><td>35</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>			Load Current [A]	Ripple Output Volt. 85V [mV]	Ripple Output Volt. 132V [mV]	0	10	10	2	35	35	4	35	35	6	35	35	8	35	35	10	35	35	11	35	35	—	—	—	—	—	—	—	—	—	—	—	—
Load Current [A]	Ripple Output Volt. 85V [mV]	Ripple Output Volt. 132V [mV]																																				
0	10	10																																				
2	35	35																																				
4	35	35																																				
6	35	35																																				
8	35	35																																				
10	35	35																																				
11	35	35																																				
—	—	—																																				
—	—	—																																				
—	—	—																																				
—	—	—																																				
<p>Ripple Voltage is shown as p-p in the figure below.</p> <p>Note: Slanted line shows the range of the rated load current.</p> <p>リップル電圧は、下図 p - p 値で示される。 (注)斜線は定格負荷電流範囲を示す。</p> <p>T1: Due to AC Input Line T2: Due to Switching</p> <p>Ripple [mVp-p]</p> <p>Fig. Complex Ripple Wave Form 図 リップル波形詳細図</p>																																						

COSEL

Model	LEA50F-5
Item	Ripple-Noise リップルノイズ
Object	+5V10A

Temperature 25°C  
Testing Circuitry Figure A

## 2. Values

Load current [A]	Input Volt. 85 [V]	Input Volt. 132 [V]
	Ripple-Noise [mV]	Ripple-Noise [mV]
0	20	20
2	45	45
4	45	45
6	50	50
8	50	50
10	50	50
11	50	50
—	—	—
—	—	—
—	—	—
—	—	—

Ripple-Noise is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

リップルノイズは、下図 p - p 値で示される。

(注)斜線は定格負荷電流範囲を示す。

- T1: Due to AC Input Line  
入力商用周期  
T2: Due to Switching  
スイッチング周期

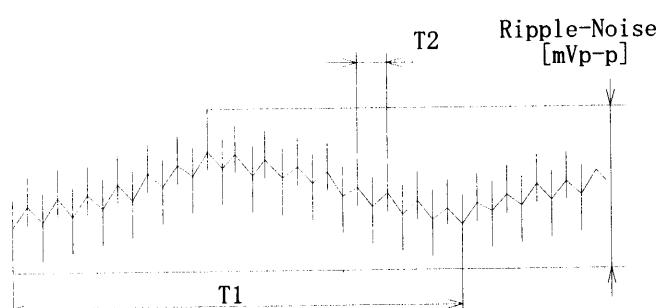


Fig. Complex Ripple Wave Form

図 リップル波形詳細図

**COSEL**

Model	LEA50F-5																								
Item	Overcurrent Protection 過電流保護																								
Object	+5V10A																								
1. Graph																									
<p>Legend: Input Volt. 85 V (top), Input Volt. 100 V (middle), Input Volt. 132 V (bottom)</p> <table border="1"> <thead> <tr> <th>Output Voltage [V]</th> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>8.0</td><td></td><td></td><td></td></tr> <tr><td>6.0</td><td></td><td></td><td></td></tr> <tr><td>4.0</td><td></td><td></td><td></td></tr> <tr><td>2.0</td><td></td><td></td><td></td></tr> <tr><td>0.0</td><td></td><td></td><td></td></tr> </tbody> </table>		Output Voltage [V]	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	8.0				6.0				4.0				2.0				0.0			
Output Voltage [V]	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]																						
8.0																									
6.0																									
4.0																									
2.0																									
0.0																									

Temperature 25°C  
Testing Circuitry Figure A

## 2. Values

Output Voltage [V]	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
	Load Current [A]	Load Current [A]	Load Current [A]
5.00	12.27	12.24	12.24
4.75	12.32	12.31	12.32
4.50	12.39	12.38	12.39
4.00	12.54	12.53	12.54
3.50	12.71	12.71	12.71
3.00	12.81	12.80	12.80
2.50	12.92	12.91	12.90
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

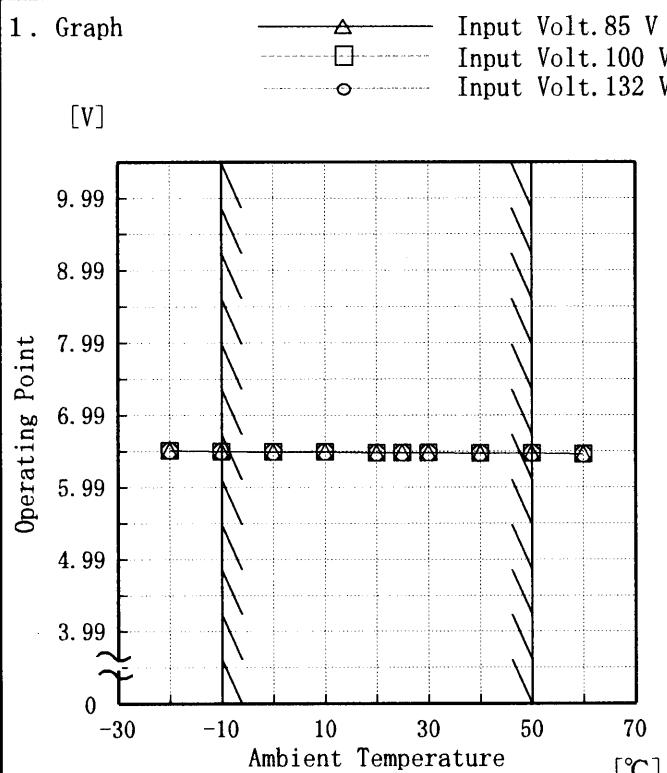
2. 5V以下は間欠状態。

**COSEL**

Model	LEA50F-5
Item	Overvoltage Protection 過電圧保護
Object	+5V10A

Testing Circuitry

Figure A



## 2. Values

Ambient Temp. [°C]	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
	Operating Point [V]		
-20	6.50	6.50	6.50
-10	6.49	6.49	6.49
0	6.48	6.48	6.48
10	6.48	6.48	6.48
20	6.47	6.47	6.47
25	6.47	6.47	6.47
30	6.47	6.47	6.47
40	6.46	6.46	6.46
50	6.46	6.46	6.46
60	6.45	6.45	6.45
—	—	—	—

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

**COSEL**

Model

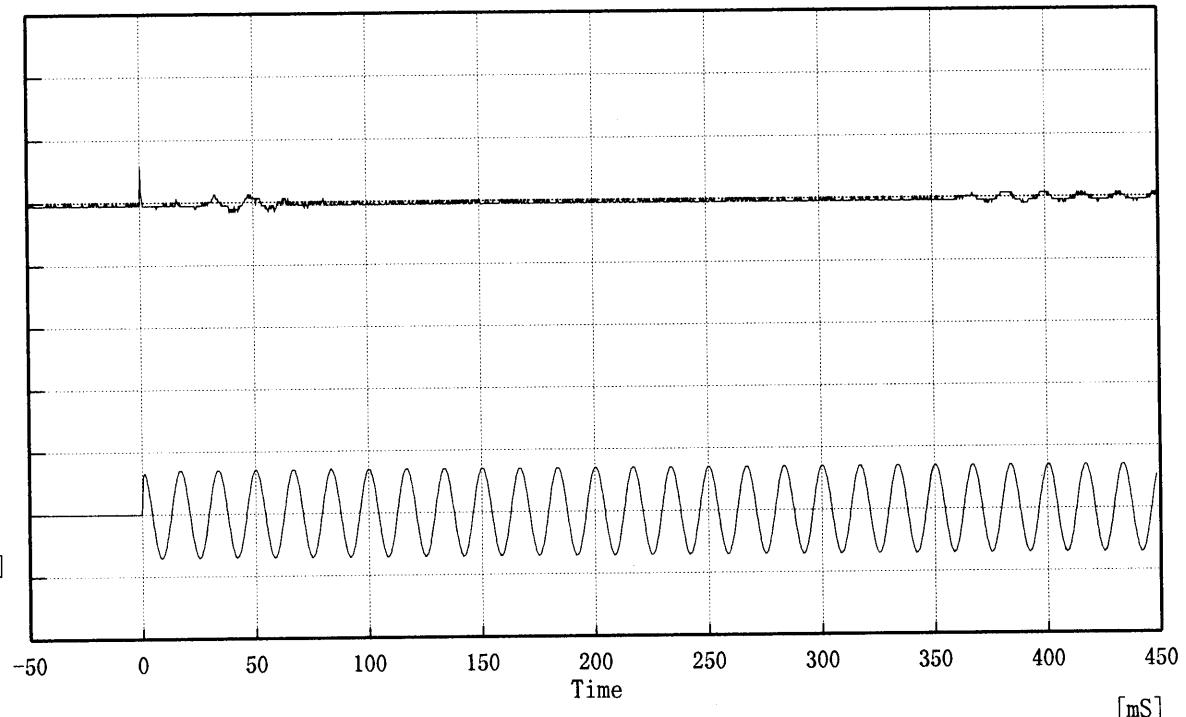
LEA50F-5

Item

Inrush Current 突入電流

Temperature 25°C  
Testing Circuitry Figure A

Object

Input  
Current  
[20A/div]Input  
Voltage  
[200V/div]

Input Voltage 100 V

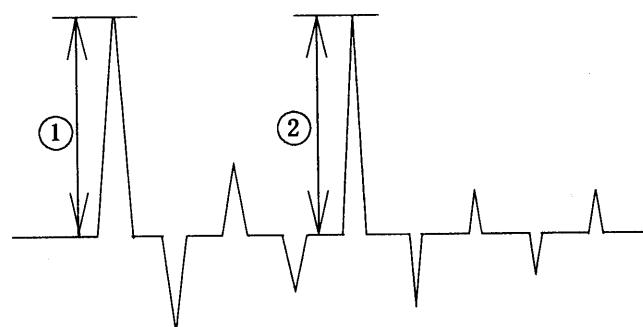
Frequency 60 Hz

Load 100 %

Inrush Current

① 11.38 [A]

② 3.20 [A]



**COSEL**

Model	LEA50F-5	Temperature Testing Circuitry	25°C Figure A
Item	Dynamic Load Response 動的負荷變動		
Object	+5V10A		

Input Volt. 100 V

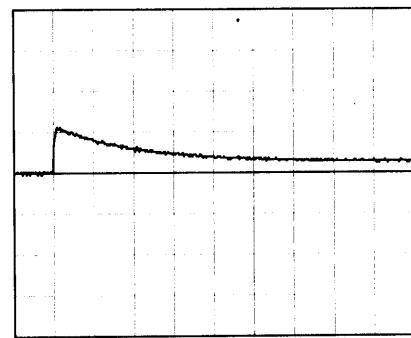
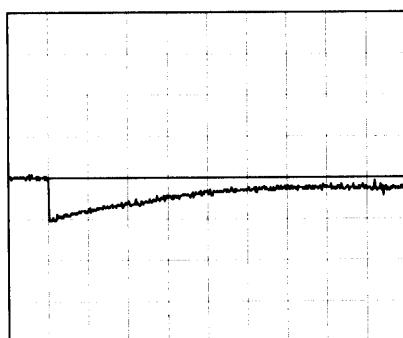
Cycle 1000 mS

Load Current



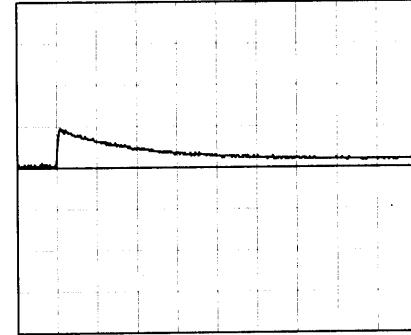
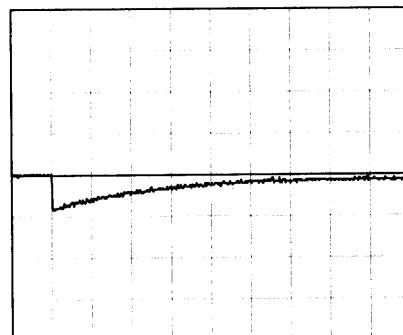
Min. Load ↔

Load 100 %



Min. Load ↔

Load 50 %



100 mV/div

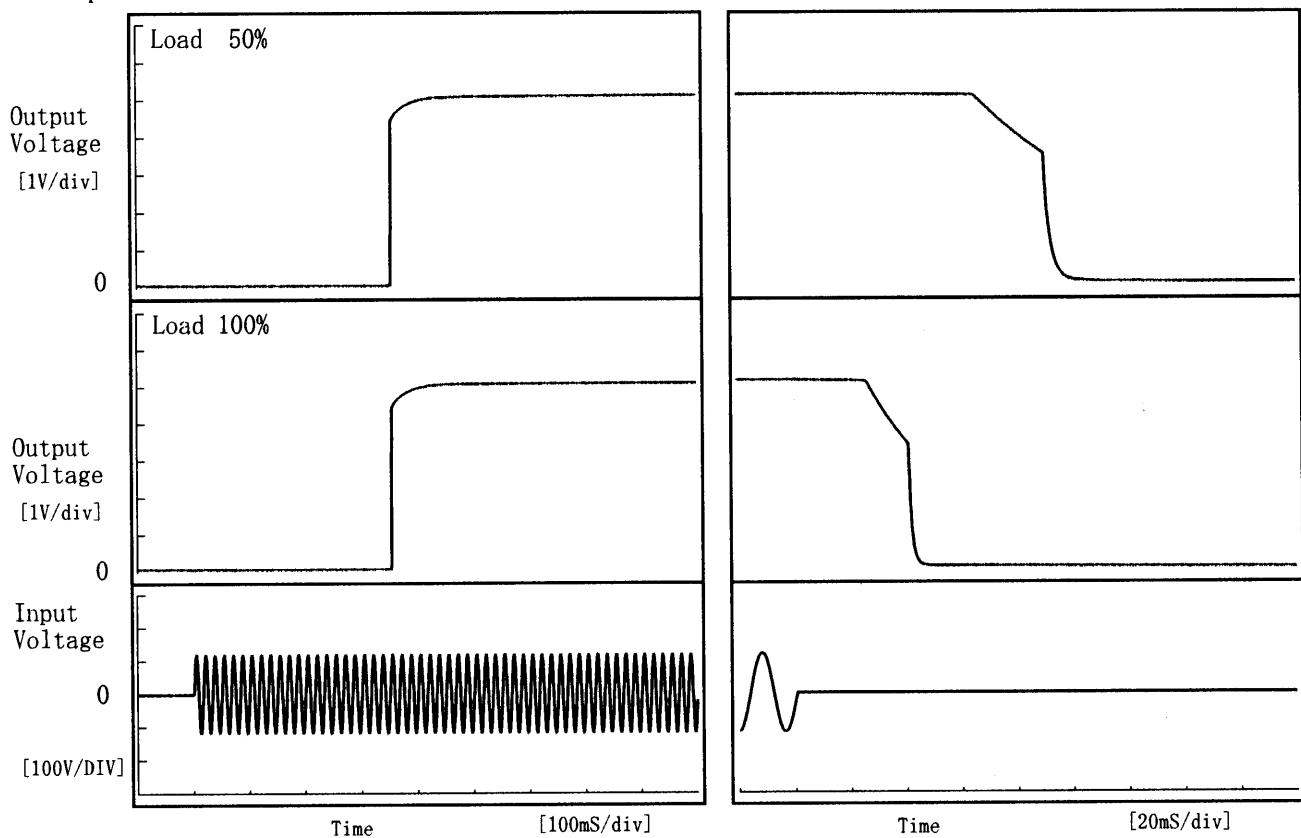
10 ms/div

**COSEL**

Model	LEA50F-5
Item	Rise and Fall Time 立上り、立下り時間
Object	+5V10A

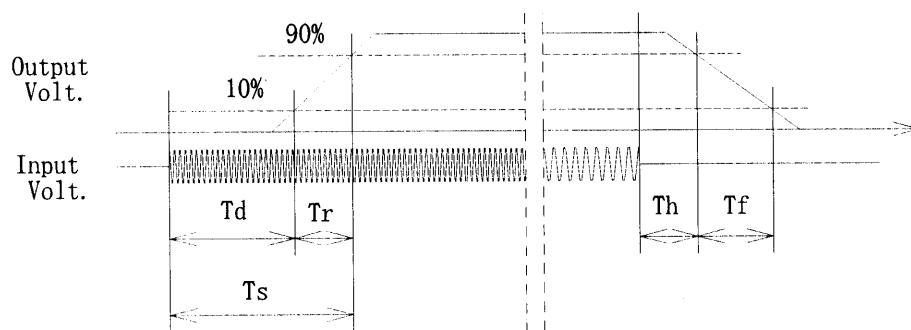
Temperature 25°C  
Testing Circuitry Figure A

## 1. Graph



## 2. Values

Load	Time	T <sub>d</sub>	T <sub>r</sub>	T <sub>s</sub>	T <sub>h</sub>	T <sub>f</sub>	[mS]
50 %		353.5	4.0	357.5	72.8	22.1	
100 %		353.5	5.0	358.5	30.2	13.4	





Model	LEA50F-5	Testing Circuitry    Figure A		
Item	Ambient Temperature Drift 周囲温度変動			
Object	+5V10A			

**1. Graph**

Output Voltage [V]

Ambient Temperature [°C]

Load 100%

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

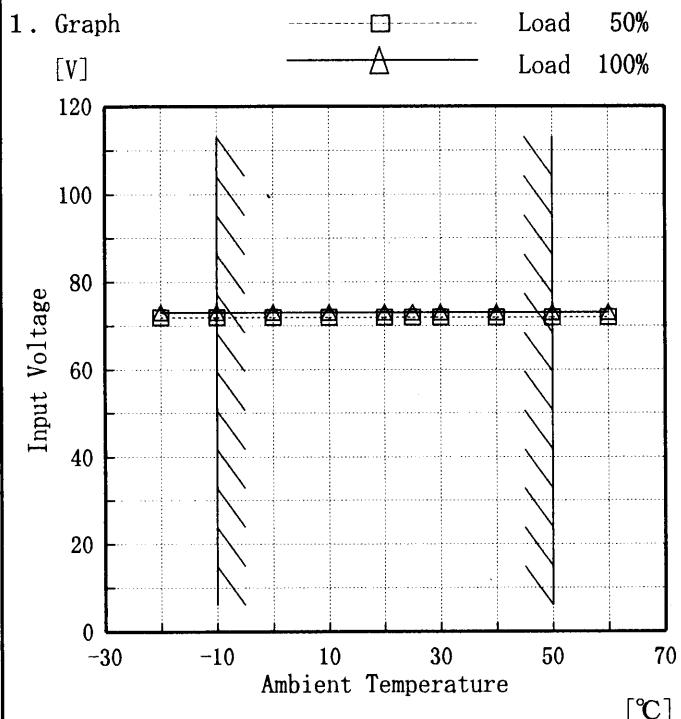
**2. Values**

Temperature [°C]	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
	Output Volt. [V]	Output Volt. [V]	Output Volt. [V]
-20	5.070	5.071	5.071
-10	5.071	5.072	5.072
0	5.072	5.073	5.073
10	5.073	5.073	5.073
20	5.074	5.074	5.074
25	5.075	5.075	5.075
30	5.077	5.077	5.077
40	5.075	5.075	5.075
50	5.074	5.074	5.074
60	5.071	5.071	5.071
—	—	—	—

**COSEL**

Model	LEA50F-5
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+5V10A

Testing Circuitry Figure A



## 2. Values

Ambient Temp. [°C]	Load 50%	Load 100%
	Input Volt. [V]	Input Volt. [V]
-20	72	73
-10	72	73
0	72	73
10	72	73
20	72	73
25	72	73
30	72	73
40	72	73
50	72	73
60	72	73
—	—	—

Note: Slanted line shows the range of the rated ambient temperature.

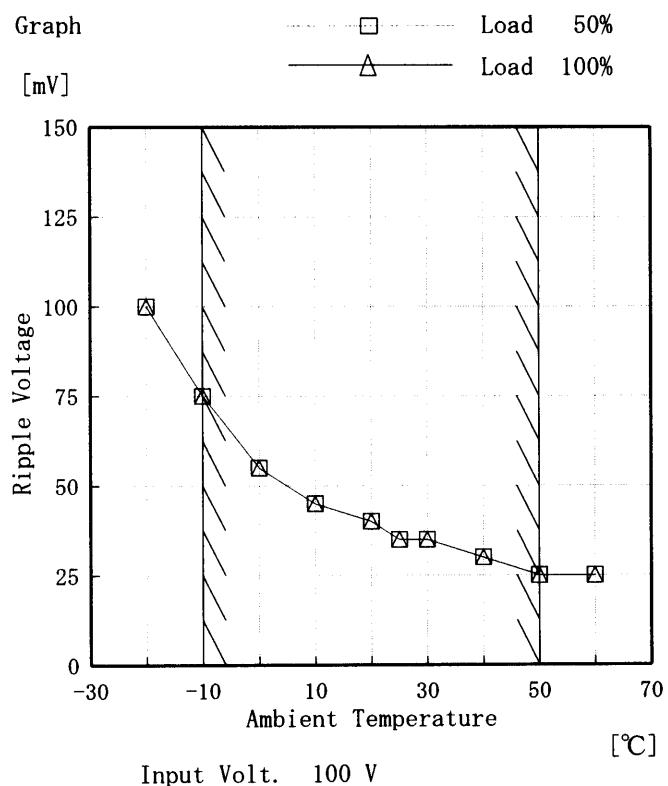
(注)斜線は定格周囲温度範囲を示す。

**COSEL**

Model	LEA50F-5
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)
Object	+5V10A

Testing Circuitry Figure A

## 1. Graph



Input Volt. 100 V

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

## 2. Values

Ambient Temp. [°C]	Load 50%	Load 100%
	Ripple Output Volt. [mV]	Ripple Output Volt. [mV]
-20	100	100
-10	75	75
0	55	55
10	45	45
20	40	40
25	35	35
30	35	35
40	30	30
50	25	25
60	25	25
—	—	—

**COSEL**

Model	LEA50F-5	Temperature Testing Circuitry 25 °C Figure A																					
Item	Time Lapse Drift 経時ドリフト																						
Object	+5V10A																						
1. Graph		2. Values																					
<p>[V]</p> <table border="1"> <caption>Data points estimated from Figure A graph</caption> <thead> <tr> <th>Time [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>5.077</td></tr> <tr><td>0.5</td><td>5.075</td></tr> <tr><td>1.0</td><td>5.075</td></tr> <tr><td>2.0</td><td>5.075</td></tr> <tr><td>3.0</td><td>5.075</td></tr> <tr><td>4.0</td><td>5.075</td></tr> <tr><td>5.0</td><td>5.075</td></tr> <tr><td>6.0</td><td>5.075</td></tr> <tr><td>7.0</td><td>5.075</td></tr> <tr><td>8.0</td><td>5.075</td></tr> </tbody> </table>		Time [H]	Output Voltage [V]	0.0	5.077	0.5	5.075	1.0	5.075	2.0	5.075	3.0	5.075	4.0	5.075	5.0	5.075	6.0	5.075	7.0	5.075	8.0	5.075
Time [H]	Output Voltage [V]																						
0.0	5.077																						
0.5	5.075																						
1.0	5.075																						
2.0	5.075																						
3.0	5.075																						
4.0	5.075																						
5.0	5.075																						
6.0	5.075																						
7.0	5.075																						
8.0	5.075																						
<p>Output Voltage</p> <p>Input Volt. 100V</p> <p>Load 100%</p>																							



Model	LEA50F-5	Testing Circuitry Figure A
Item	Output Voltage Accuracy 定電圧精度	
Object	+5V10A	

#### Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10~50 °C

Input Voltage : 85~132 V

Load Current : 0.00~10 A

\* Output Voltage Accuracy = ±(Maximum of Output Voltage — Minimum of Output Voltage) / 2

$$* \text{ Output Voltage Accuracy (Ration)} = \frac{\text{Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

#### 定電圧精度

周囲温度、入力電圧、負荷を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 -10~50 °C

入力電圧 85~132 V

負荷電流 0.00~10 A

\* 定電圧精度(変動値) = ±(出力電圧の最高値—出力電圧の最低値) / 2

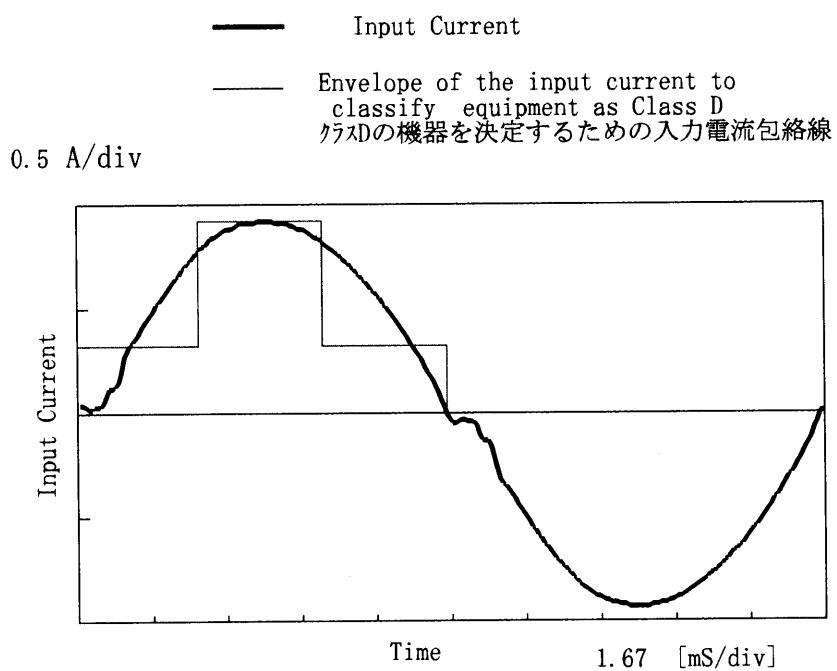
$$* \text{ 定電圧精度(変動率)} = \frac{\text{変動値}}{\text{定格出力電圧}} \times 100$$

Item	Temperature [°C]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy(Ration) [%]
Maximum Voltage	25	132	0.00	5.104		
Minimum Voltage	-10	85	10.00	5.072	±16	±0.4

**COSEL**

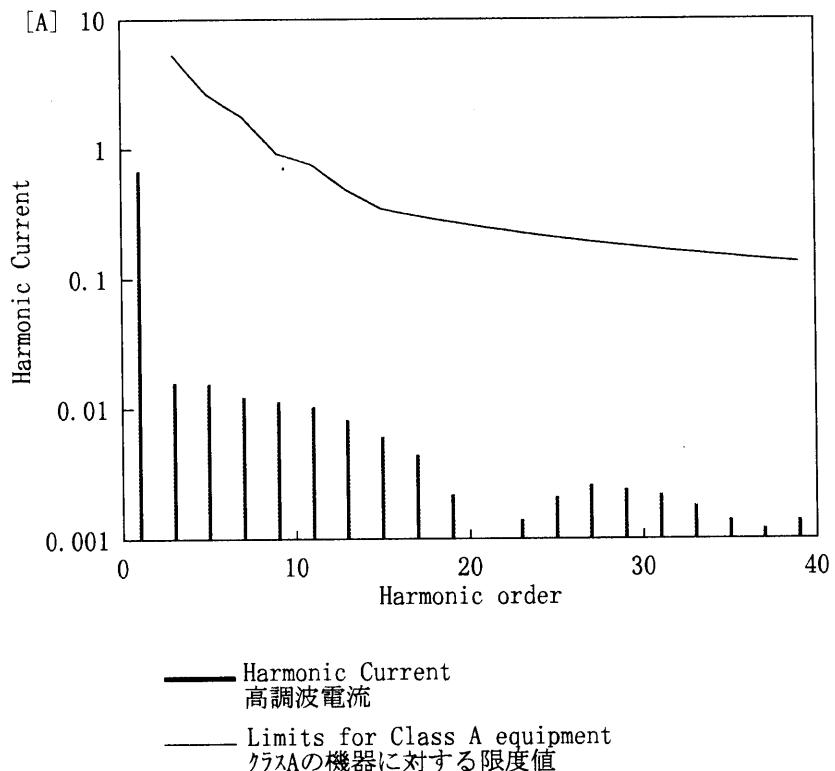
Model	LEA50F-5	Temperature Testing Circuitry	25°C Figure E
Item	Harmonic Current 高調波電流		
Object	—		

## 1. Input Current Waveform



Conditions	Values
Input Voltage [V]	100.1
Input Current [A]	0.685
Active Power [W]	68.4
Apparent Power [VA]	68.6
Frequency [Hz]	60
Power Factor	0.997
Output Power [W]	50

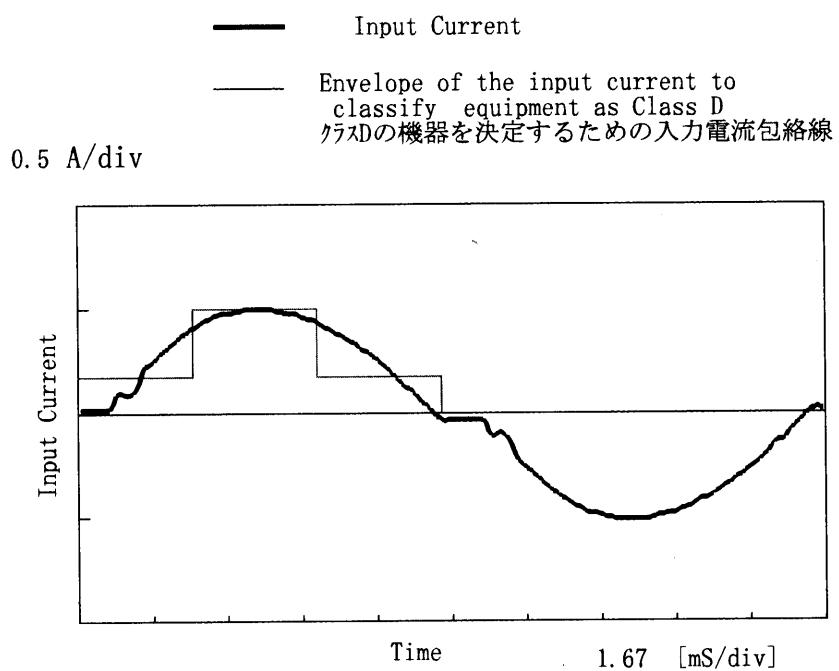
## 2. Harmonic Current



**COSEL**

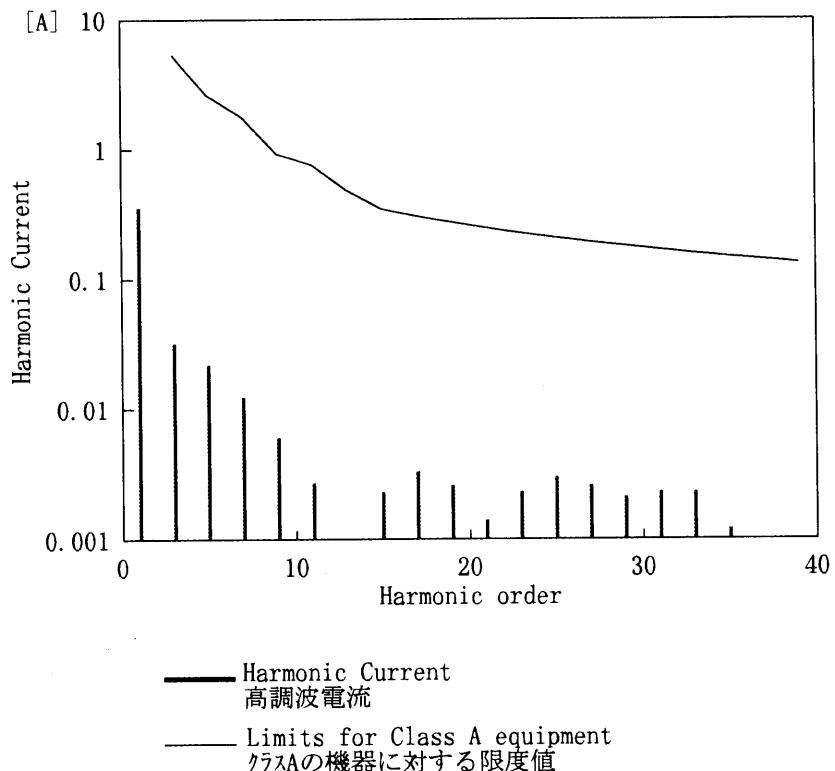
Model	LEA50F-5	Temperature	25°C
Item	Harmonic Current 高調波電流	Testing Circuitry	Figure E
Object	—		

## 1. Input Current Waveform



Conditions	Values
Input Voltage [V]	100.2
Input Current [A]	0.359
Active Power [W]	35.6
Apparent Power [VA]	36.1
Frequency [Hz]	60
Power Factor	0.986
Output Power [W]	25

## 2. Harmonic Current



Harmonics order	Limits 限度値 [A]	Values 測定値 [A]
1	—	0.35720
2	—	0.00030
3	5.27944	0.03210
4	—	0.00010
5	2.61677	0.02200
6	—	0.00010
7	1.76747	0.01230
8	—	0.00000
9	0.91816	0.00600
10	—	0.00000
11	0.75749	0.00270
12	—	0.00010
13	0.48204	0.00040
14	—	0.00010
15	0.34431	0.00230
16	—	0.00000
17	0.30380	0.00330
18	—	0.00000
19	0.27182	0.00260
20	—	0.00010
21	0.24594	0.00140
22	—	0.00000
23	0.22455	0.00230
24	—	0.00010
25	0.20659	0.00300
26	—	0.00000
27	0.19128	0.00260
28	—	0.00000
29	0.17809	0.00210
30	—	0.00000
31	0.16660	0.00230
32	—	0.00000
33	0.15651	0.00230
34	—	0.00000
35	0.14756	0.00120
36	—	0.00010
37	0.13959	0.00070
38	—	0.00010
39	0.13243	0.00100
40	—	0.00000



Model	LEA50F-5	
Item	Condensation 結露特性	Testing Circuitry Figure A
Object	+5V10A	

### 1. Condensation test

Testing procedure is as follows.

- ① Keeping and cooling the unit in a tank at -10°C for an hour with the input off.
- ② Taking it out of the tank and dewing itself in a room where the temperature is 25°C and the humidity is 40%RH.
- ③ Testing electrical characteristics of the unit to confirm there be no fault.

### 1. 結露特性試験

入力を切った状態で、恒温槽で-10°Cに冷却しておき、約1時間後に恒温槽から取り出し、室温25°C、湿度40%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

### 2. Values

Item	Data	Testing Conditions
Output Voltage [V]	5.096	Input Volt.: 100V, Load Current:10A
Line Regulation [mV]	1	Input Volt.: 85~132V, Load Current:10A
Load Regulation [mV]	27	Input Volt.: 100V, Load Current:0~10A



Model	LEA50F-5	Temperature	25°C
Item	Leakage Current 漏洩電流	Testing Circuitry	Figure B
Object	_____		

### 1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DENTORI	0.16	0.18	0.24
(B) IEC60950	0.16	0.18	0.24

### 2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力の両相について測定し、その大きい方を漏洩電流測定値とする。

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B) IEC60950	—	—	—



Model	LEA50F-5	Temperature Testing Circuitry Figure C	25°C
Item	Line Noise Tolerance 入力雑音耐量		
Object	+5V10A		

### 1. Results

Pulse Width [nS]	MODE	No protection failure should occur 保護回路の誤動作がない	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation
1000	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation

#### Conditions

Input Voltage : 100 V  
 Pulse Voltage : 2000 V  
 Pulse Cycle : 10 mS  
 Pulse Input Duration: 1 min. or more  
 Load : 100 %

COSEL

Model	LEA50F-5	Temperature Testing Circuitry	25°C Figure D
Item	Conducted Emission 雜音端子電壓		
Object	_____		

## 1. Graph

## Remarks

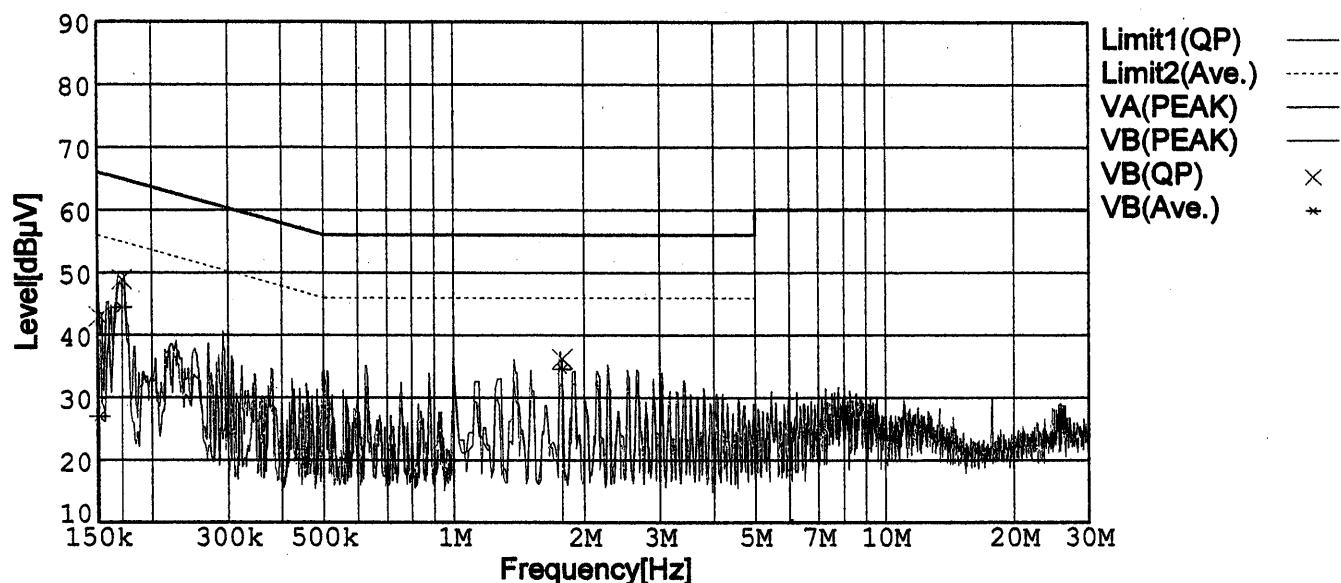
Input Volt. 100V ( VCCI Class B )

120V ( FCC Class B )

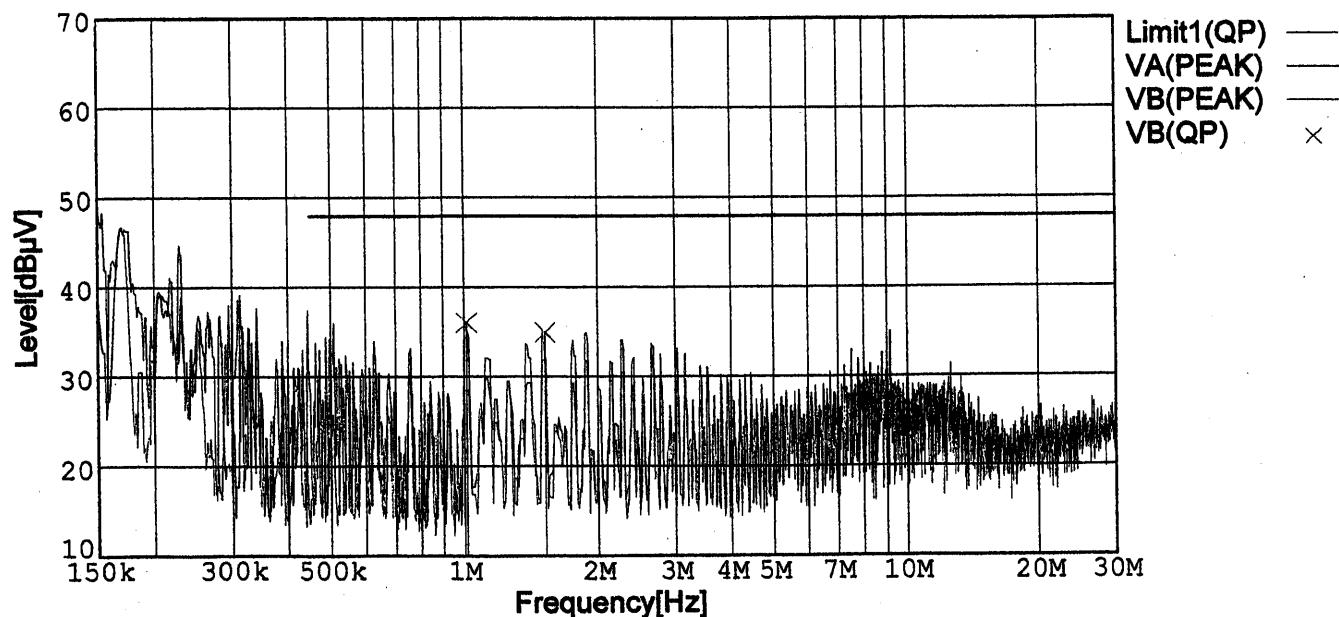
Load 100 %

Limit1: [VCCI] Class B(QP)

Limit2: [VCCI] Class B(Ave.)



Limit1: [FCC Part15] Class B



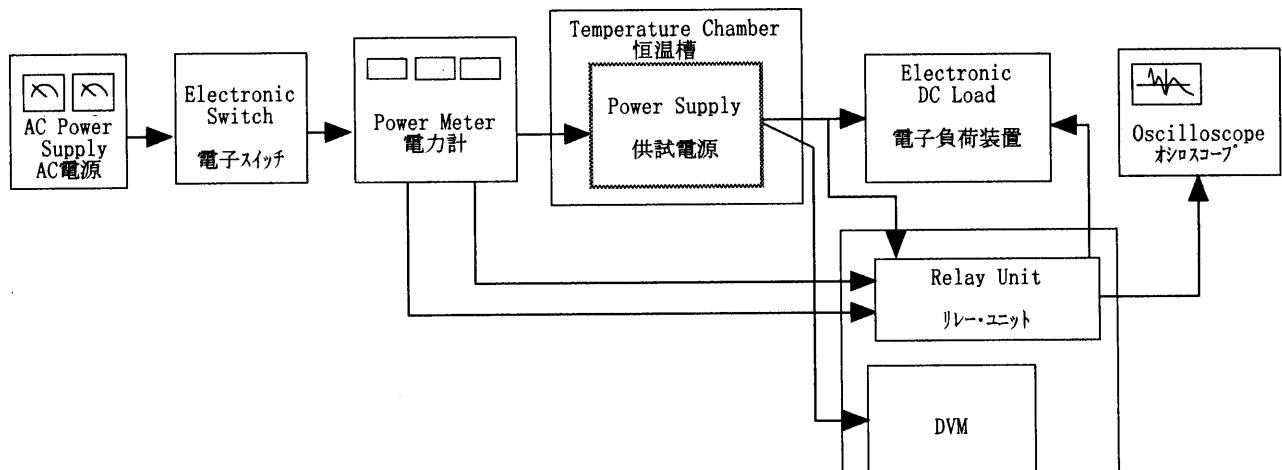


Figure A

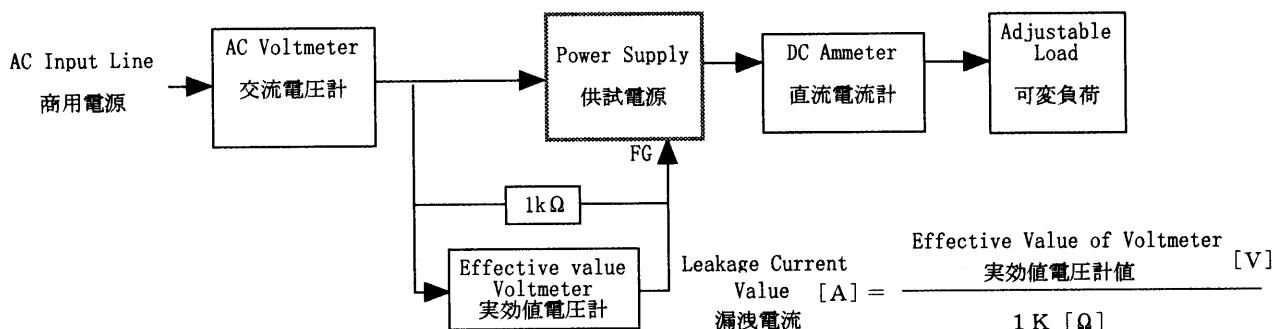
Data Acquisition/Control Unit  
データ集録システム

Figure B (DENTORI)

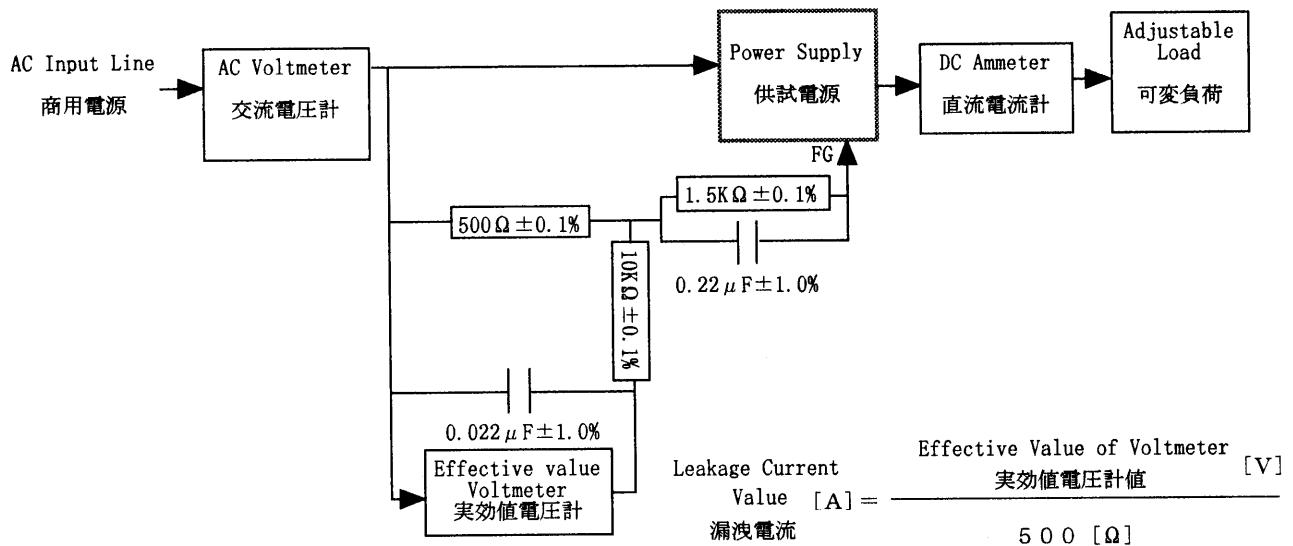


Figure B (IEC60950)

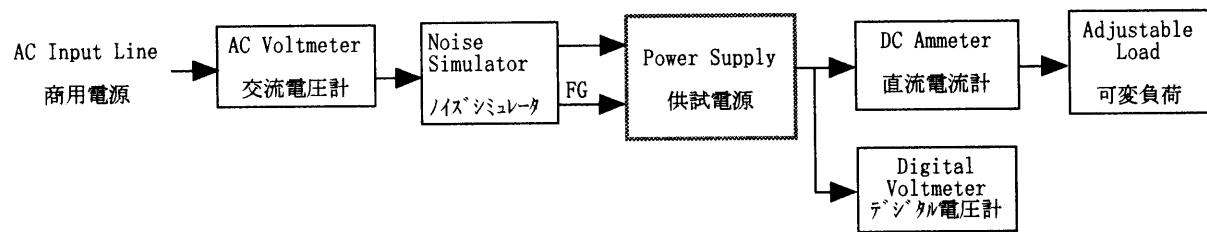


Figure C

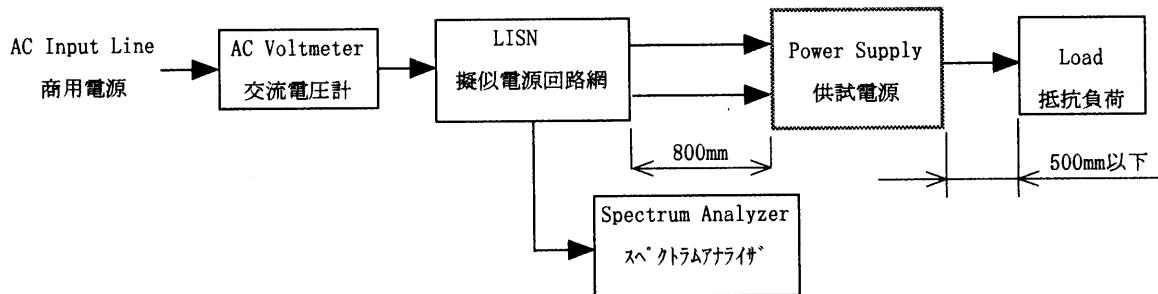


Figure D

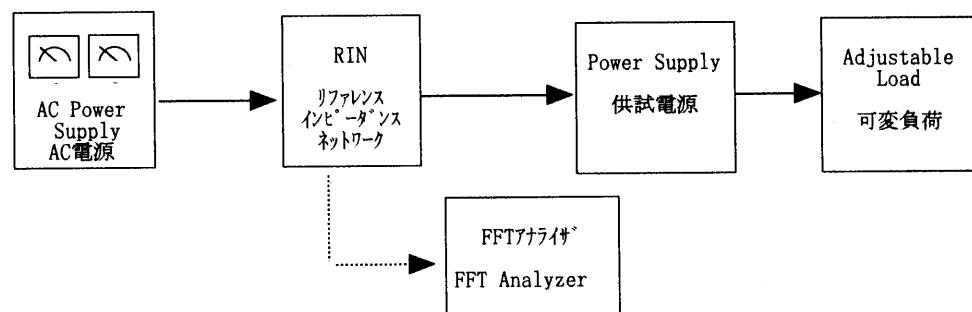


Figure E