

COSEL

TEST DATA OF LEA100F-5
(100V INPUT)

Regulated DC Power Supply

Date : Feb. 9. 1999

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Design Manager

Prepared by : T. Miura
Design Engineer

コーセル株式会社

COSEL CO., LTD.

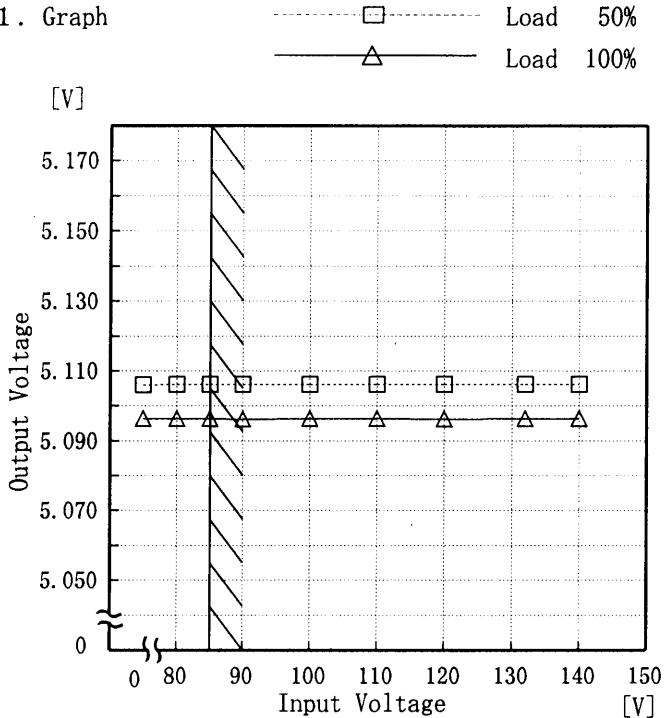


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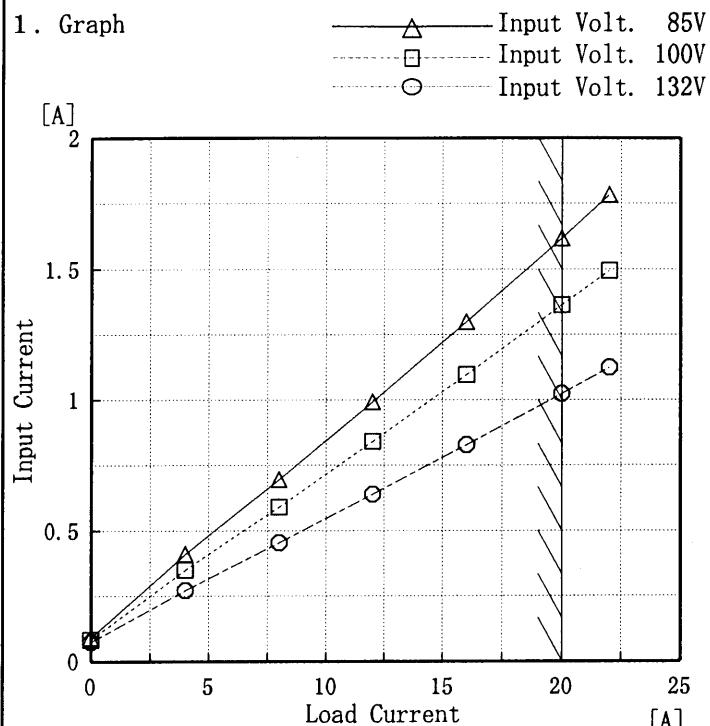
Model	LEA100F-5	Temperature	25°C
Item	Line Regulation 静的入力変動	Testing Circuitry	Figure A
Object	+5V20A		
1. Graph		Load 50% 	
2. Values			
Input Voltage [V]	Load 50%	Load 100%	
	Output Volt. [V]	Output Volt. [V]	
75	5.106	5.096	
80	5.106	5.096	
85	5.106	5.096	
90	5.106	5.096	
100	5.106	5.096	
110	5.106	5.096	
120	5.106	5.096	
132	5.106	5.096	
140	5.106	5.096	

Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

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Model	LEA100F-5
Item	Input Current (by Load Current) 入力電流 (負荷特性)
Output	—

Temperature 25°C
Testing Circuitry Figure A

2. Values

Load Current [A]	Input Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	0.091	0.084	0.074
4	0.410	0.350	0.273
8	0.697	0.591	0.454
12	0.994	0.841	0.640
16	1.299	1.097	0.829
20	1.618	1.362	1.024
22	1.782	1.494	1.124
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

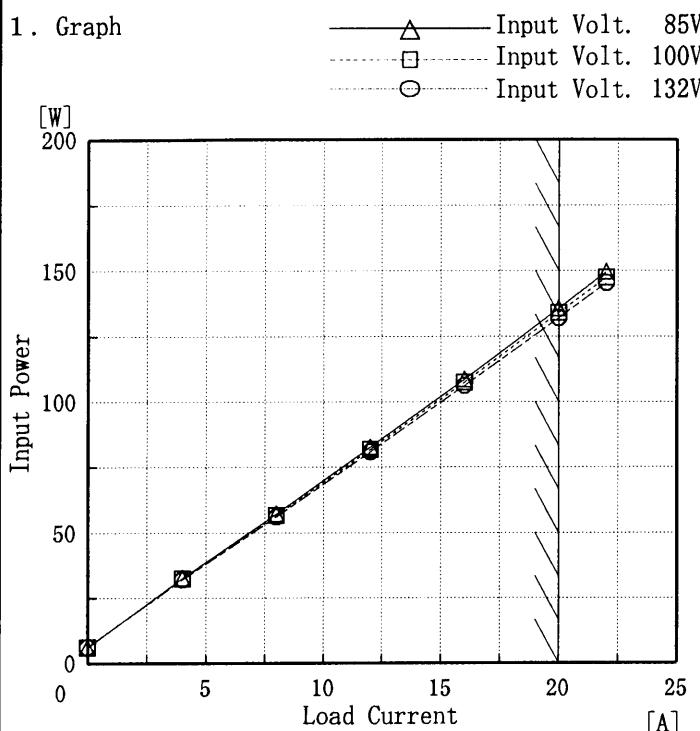
Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

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Model	LEA100F-5
Item	Input Power (by Load Current) 入力電力 (負荷特性)
Output	_____

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

Temperature 25°C
Testing Circuitry Figure A

2. Values

Load Current [A]	Input Power [W]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	6.08	6.24	6.31
4	32.66	32.34	32.06
8	57.13	56.67	56.10
12	82.60	81.80	80.90
16	108.60	107.50	106.10
20	135.70	134.10	132.10
22	149.70	147.50	145.40
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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Model	LEA100F-5																																	
Item	Efficiency (by Input Voltage) 効率(入力電圧特性)	Temperature 25°C Testing Circuitry Figure A																																
Object	<hr/>																																	
1. Graph																																		
<p style="text-align: center;">-----□----- Load 50%</p> <p style="text-align: center;">——△—— Load 100%</p> <table border="1"> <caption>Data points estimated from the graph</caption> <thead> <tr> <th>Input Voltage [V]</th> <th>Load 50% Efficiency [%]</th> <th>Load 100% Efficiency [%]</th> </tr> </thead> <tbody> <tr><td>80</td><td>73.5</td><td>74.0</td></tr> <tr><td>90</td><td>73.5</td><td>74.5</td></tr> <tr><td>100</td><td>73.8</td><td>75.0</td></tr> <tr><td>110</td><td>74.0</td><td>75.5</td></tr> <tr><td>120</td><td>74.0</td><td>76.0</td></tr> <tr><td>130</td><td>74.2</td><td>76.5</td></tr> <tr><td>140</td><td>74.2</td><td>77.0</td></tr> </tbody> </table>		Input Voltage [V]	Load 50% Efficiency [%]	Load 100% Efficiency [%]	80	73.5	74.0	90	73.5	74.5	100	73.8	75.0	110	74.0	75.5	120	74.0	76.0	130	74.2	76.5	140	74.2	77.0									
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Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

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Model	LEA100F-5	Temperature 25°C Testing Circuitry Figure A																																																									
Item	Efficiency (by Load Current) 効率(負荷電流特性)																																																										
Output	—																																																										
1. Graph	—△— Input Volt. 85V —□— Input Volt. 100V —○— Input Volt. 132V	2. Values																																																									
<p>The graph shows efficiency increasing with load current for all input voltages. The 85V curve starts at ~64% at 3A and rises to ~76% at 22A. The 100V curve starts at ~68% at 4A and rises to ~77% at 22A. The 132V curve starts at ~72% at 8A and rises to ~77% at 22A. A slanted line from the origin marks the rated load current range.</p>																																																											
<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Efficiency [%]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>4</td><td>62.8</td><td>63.5</td><td>64.2</td></tr> <tr><td>8</td><td>71.6</td><td>72.2</td><td>73.3</td></tr> <tr><td>12</td><td>74.3</td><td>75.1</td><td>76.1</td></tr> <tr><td>16</td><td>75.1</td><td>76.1</td><td>77.3</td></tr> <tr><td>20</td><td>75.2</td><td>76.2</td><td>77.6</td></tr> <tr><td>22</td><td>74.9</td><td>76.1</td><td>77.4</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>					Load Current [A]	Efficiency [%]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	4	62.8	63.5	64.2	8	71.6	72.2	73.3	12	74.3	75.1	76.1	16	75.1	76.1	77.3	20	75.2	76.2	77.6	22	74.9	76.1	77.4	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
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Note: Slanted line shows the range of the rated load current

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Model	LEA100F-5																																	
Item	Power Factor (by Input Voltage) 力率(入力電圧特性)	Temperature 25°C Testing Circuitry Figure A																																
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Note: Slanted line shows the range of the rated input voltage.

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Model

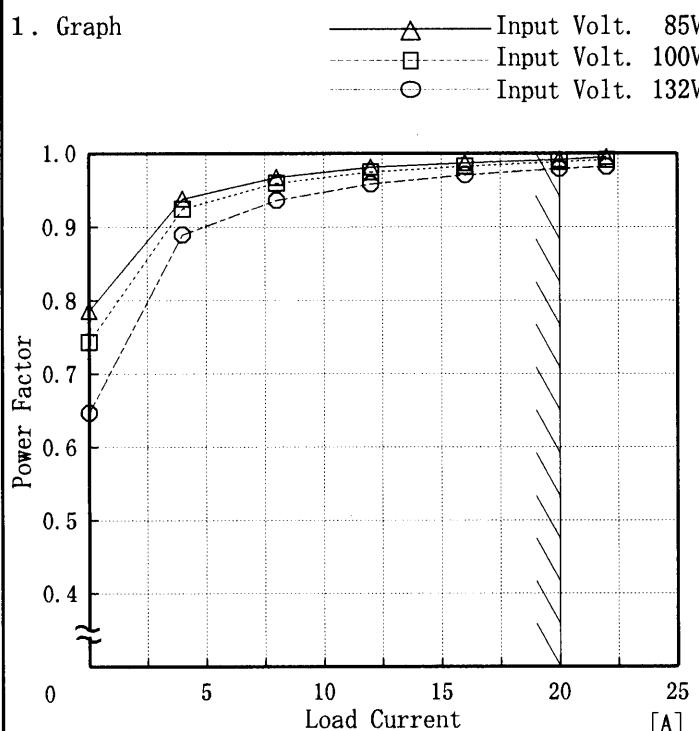
LEA100F-5

Item

Power Factor (by Load Current)
力率 (負荷電流特性)

Output

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

Temperature 25°C
Testing Circuitry Figure A

2. Values

Load Current [A]	Power Factor		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	0.79	0.74	0.65
4	0.94	0.92	0.89
8	0.97	0.96	0.94
12	0.98	0.97	0.96
16	0.99	0.98	0.97
20	0.99	0.99	0.98
22	0.99	0.99	0.98
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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Model	LEA100F-5	Temperature Testing Circuitry	25°C Figure A																																
Item	Hold-Up Time 出力保持時間																																		
Object	+5V20A																																		
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This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated input voltage.

出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。

(注)斜線は定格入力電圧範囲を示す。

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Model	LEA100F-5																																																					
Item	Instantaneous Interruption Compensation 瞬時停電保障																																																					
Object	+5V 20A																																																					
1. Graph		Temperature 25°C Testing Circuitry Figure A																																																				
<p>Instantaneous Compensation Time [mS]</p> <p>Load Current [A]</p> <p>Legend:</p> <ul style="list-style-type: none"> △ Input Volt. 85 V □ Input Volt. 100 V ○ Input Volt. 132 V 		2. Values																																																				
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		Time [mS]																																																				

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated load current.

瞬時停電保障時間とは、出力電圧が定電圧精度の規格範囲を保持している瞬時停電時間をいう。

(注)斜線は定格負荷電流範囲を示す。

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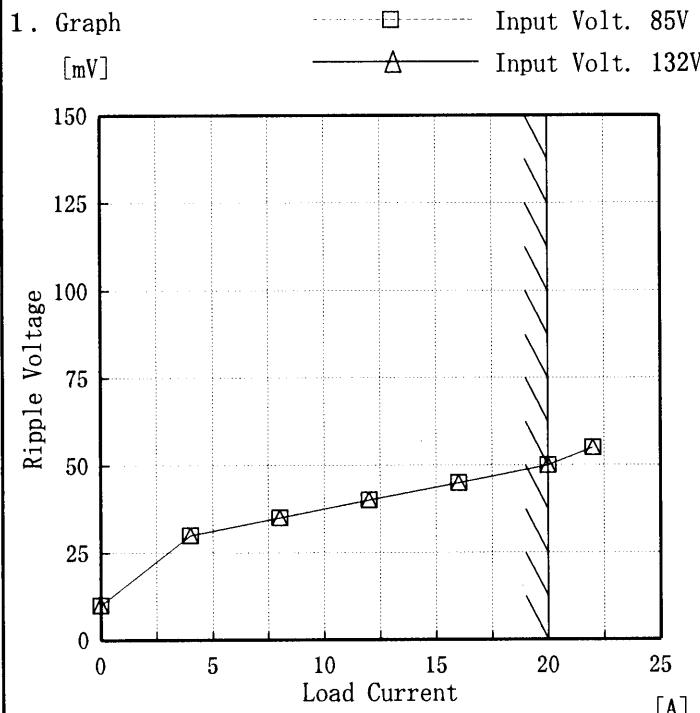
Model	LEA100F-5	Temperature 25°C Testing Circuitry Figure A		
Item	Load Regulation 靜的負荷変動			
Object	+5V 20A			
1. Graph	<p>—△— Input Volt. 85V -□- Input Volt. 100V -○- Input Volt. 132V</p>			
2. Values	Load Current [A]	Input Volt. 85[V] Output Volt. [V]	Input Volt. 100[V] Output Volt. [V]	Input Volt. 132[V] Output Volt. [V]
0.0	5.116	5.117	5.117	
4.0	5.112	5.112	5.112	
8.0	5.108	5.108	5.108	
12.0	5.104	5.104	5.104	
16.0	5.101	5.101	5.101	
20.0	5.097	5.097	5.097	
22.0	5.095	5.095	5.095	
—	—	—	—	
—	—	—	—	
—	—	—	—	

Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

COSEL

Model	LEA100F-5
Item	Ripple Voltage(by Load Current) リップル電圧(負荷電流特性)
Object	+5V20A

Temperature 25°C
Testing Circuitry Figure A

2. Values

Load Current [A]	Input Volt. 85 [V]	Input Volt. 132 [V]
	Ripple Output Volt. [mV]	Ripple Output Volt. [mV]
0.0	10	10
4.0	30	30
8.0	35	35
12.0	40	40
16.0	45	45
20.0	50	50
22.0	55	55
—	—	—
—	—	—
—	—	—
—	—	—

Ripple Voltage is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

リップル電圧は、下図 p-p 値で示される。

(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line

入力商用周期

T2: Due to Switching

スイッチング周期

— T2

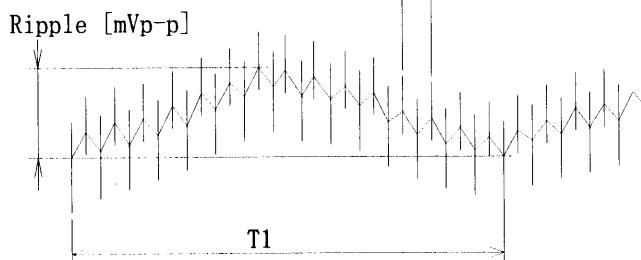


Fig. Complex Ripple Wave Form

図 リップル波形詳細図

COSEL

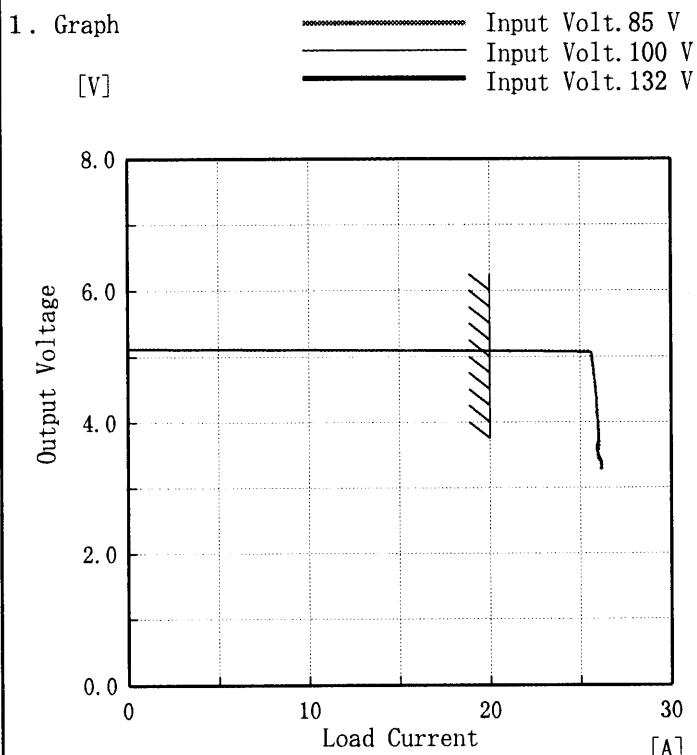
Model	LEA100F-5	Temperature Testing Circuitry 25°C Figure A																																						
Item	Ripple-Noise リップルノイズ																																							
Object	+5V 20A																																							
1. Graph	<p style="text-align: center;">□ Input Volt. 85V [mV] △ Input Volt. 132V</p>	2. Values																																						
		<table border="1"> <thead> <tr> <th rowspan="2">Load current [A]</th> <th>Input Volt. 85 [V]</th> <th>Input Volt. 132 [V]</th> </tr> <tr> <th>Ripple-Noise [mV]</th> <th>Ripple-Noise [mV]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>20</td><td>20</td></tr> <tr><td>4.0</td><td>40</td><td>40</td></tr> <tr><td>8.0</td><td>50</td><td>50</td></tr> <tr><td>12.0</td><td>55</td><td>55</td></tr> <tr><td>16.0</td><td>65</td><td>65</td></tr> <tr><td>20.0</td><td>70</td><td>70</td></tr> <tr><td>22.0</td><td>75</td><td>75</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>	Load current [A]	Input Volt. 85 [V]	Input Volt. 132 [V]	Ripple-Noise [mV]	Ripple-Noise [mV]	0.0	20	20	4.0	40	40	8.0	50	50	12.0	55	55	16.0	65	65	20.0	70	70	22.0	75	75	—	—	—	—	—	—	—	—	—	—	—	—
Load current [A]	Input Volt. 85 [V]	Input Volt. 132 [V]																																						
	Ripple-Noise [mV]	Ripple-Noise [mV]																																						
0.0	20	20																																						
4.0	40	40																																						
8.0	50	50																																						
12.0	55	55																																						
16.0	65	65																																						
20.0	70	70																																						
22.0	75	75																																						
—	—	—																																						
—	—	—																																						
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<p>リップルノイズは、下図 p - p 値で示される。</p> <p>(注)斜線は定格負荷電流範囲を示す。</p>																																								
<p>T1: Due to AC Input Line 入力商用周期</p> <p>T2: Due to Switching スイッチング周期</p>																																								
<p>Fig. Complex Ripple Wave Form 図 リップル波形詳細図</p>																																								

COSEL

Model LEA100F-5

Item Overcurrent Protection
過電流保護

Object +5V20A



Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

3.5V以下は間欠状態。

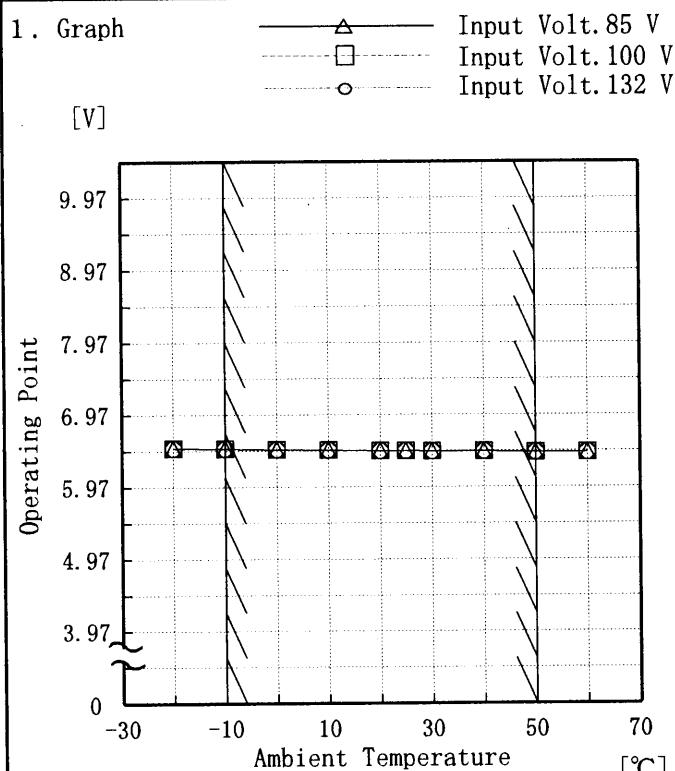
Temperature 25°C
Testing Circuitry Figure A

2. Values

Output Voltage [V]	Input Volt. 85[V] Load Current [A]	Input Volt. 100[V] Load Current [A]	Input Volt. 132[V] Load Current [A]
5.00	25.62	25.62	25.64
4.75	25.73	25.74	25.76
4.50	25.83	25.83	25.84
4.00	25.99	25.95	25.94
3.50	25.95	25.92	25.98
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

COSEL

Model	LEA100F-5
Item	Overvoltage Protection 過電圧保護
Object	+5V20A



Testing Circuitry Figure A

2. Values

Ambient Temp. [°C]	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
	Operating Point [V]		
-20	6.51	6.51	6.51
-10	6.50	6.50	6.50
0	6.49	6.49	6.49
10	6.48	6.48	6.48
20	6.47	6.47	6.47
25	6.47	6.47	6.47
30	6.46	6.46	6.46
40	6.46	6.46	6.46
50	6.45	6.45	6.45
60	6.45	6.45	6.45
—	—	—	—

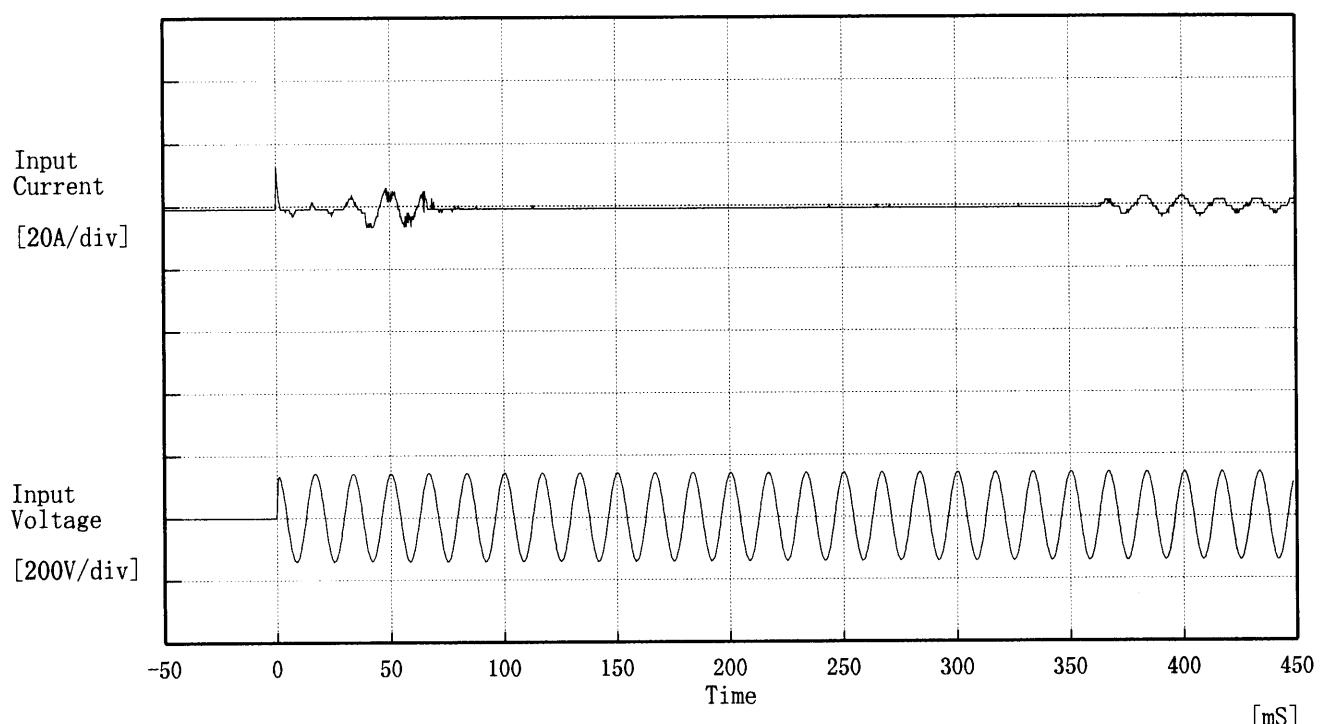
Load 0%

Note: Slanted line shows the range of the rated ambient temperature.

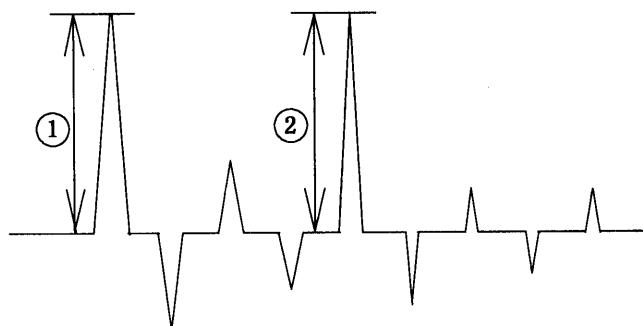
(注)斜線は定格周囲温度範囲を示す。

COSEL

Model	LEA100F-5
Item	Inrush Current 突入電流
Object	_____

Temperature 25°C
Testing Circuitry Figure A

Input Voltage 100 V
 Frequency 60 Hz
 Load 100 %
 Inrush Current
 ① 12.62 [A]
 ② 6.44 [A]



COSEL

Model	LEA100F-5	Temperature Testing Circuitry Figure A
Item	Dynamic Load Response 動的負荷變動	
Object	+5V20A	

Input Volt. 100 V

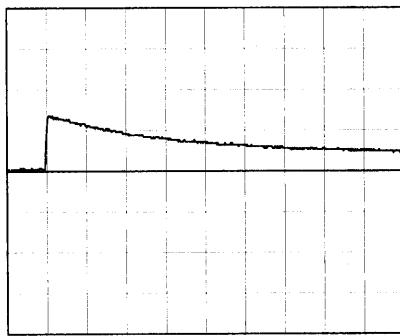
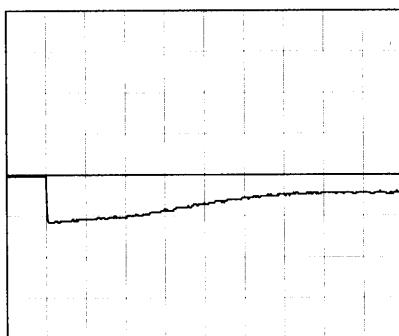
Cycle 1000 mS

Load Current



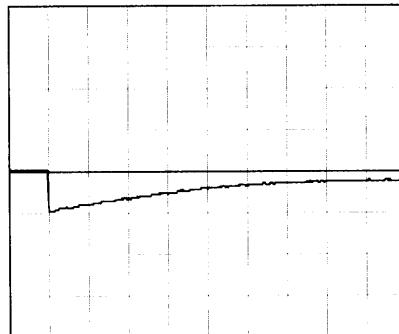
Min. Load ↔

Load 100 %

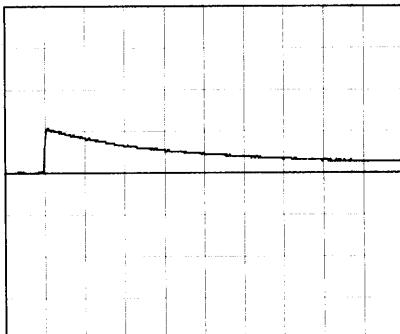


Min. Load ↔

Load 50 %



50 mV/div



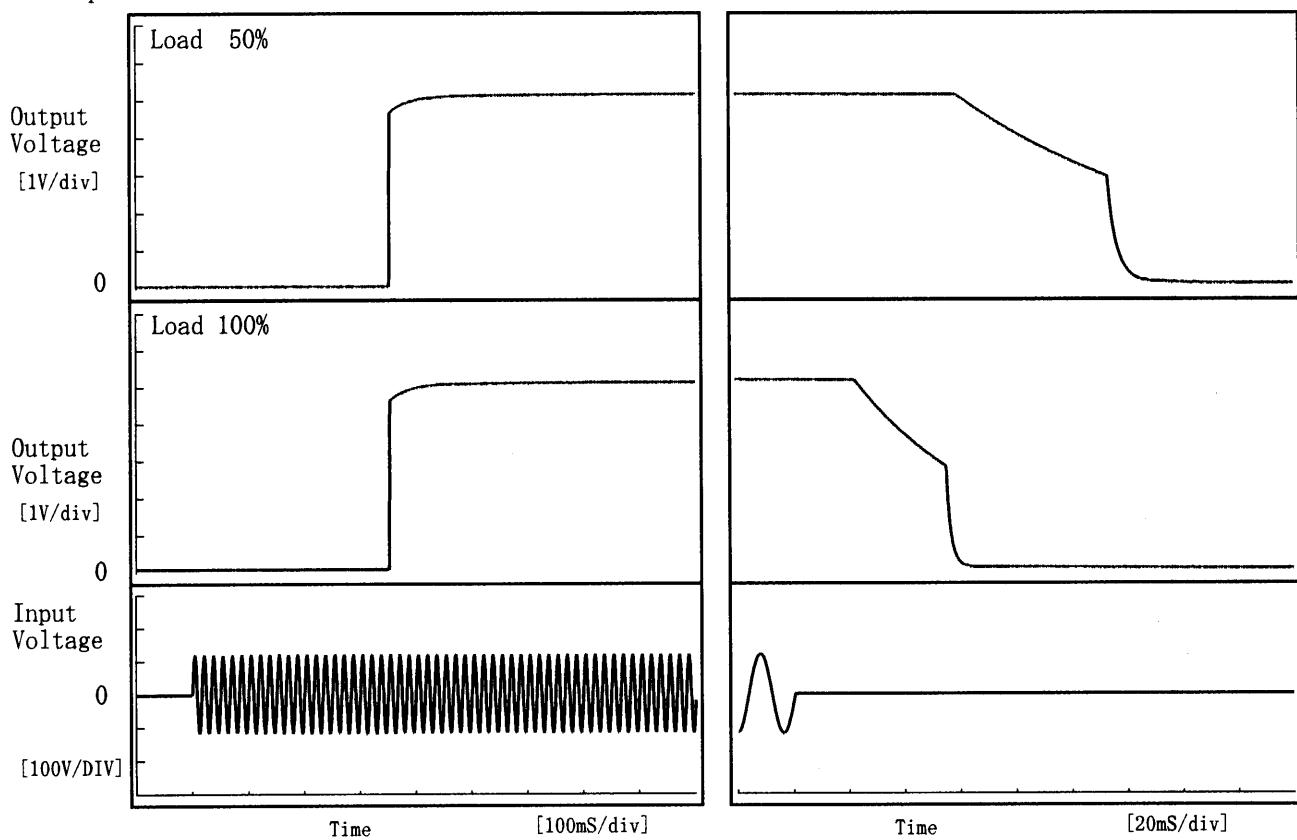
10 ms/div

COSEL

Model	LEA100F-5
Item	Rise and Fall Time 立上り、立下り時間
Object	+5V 20A

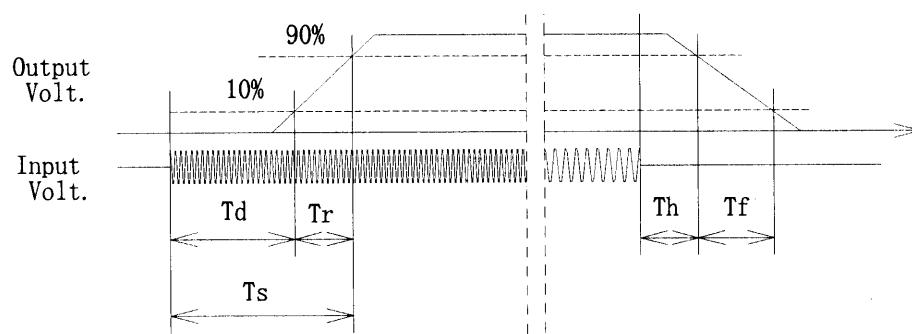
Temperature 25°C
Testing Circuitry Figure A

1. Graph



2. Values

Load	Time	T d	T r	T s	T h	T f	[mS]
50 %		353.0	1.5	354.5	71.0	48.8	
100 %		352.5	2.0	354.5	29.2	29.2	



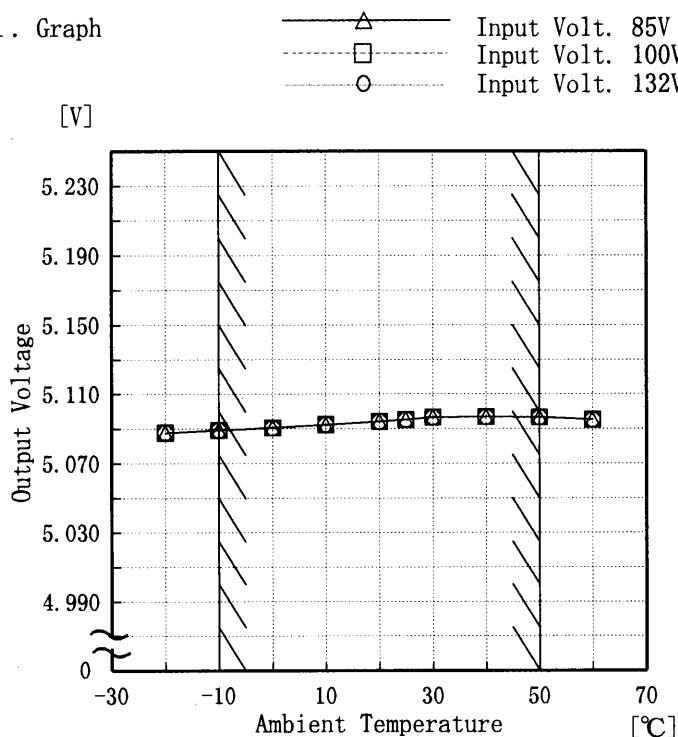
COSEL

Model LEA100F-5

Item Ambient Temperature Drift
周囲温度変動

Object +5V20A

1. Graph



(注) 斜線は定格周囲温度範囲を示す。

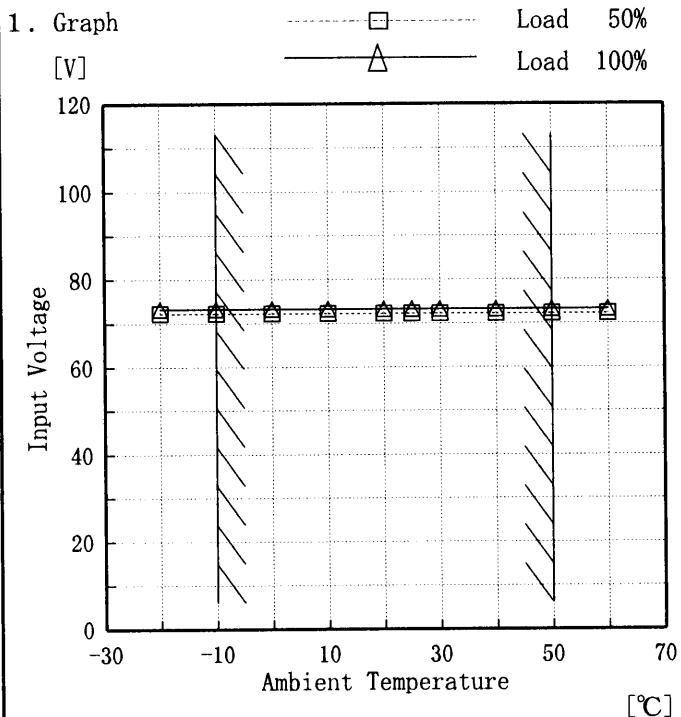
Testing Circuitry Figure A

2. Values

Temperature [°C]	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
	Output Volt. [V]	Output Volt. [V]	Output Volt. [V]
-20	5.088	5.088	5.088
-10	5.089	5.089	5.089
0	5.091	5.091	5.091
10	5.092	5.092	5.092
20	5.094	5.094	5.094
25	5.095	5.095	5.095
30	5.097	5.097	5.097
40	5.097	5.097	5.097
50	5.097	5.097	5.097
60	5.095	5.095	5.095
—	—	—	—



Model	LEA100F-5
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+5V 20A



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

Testing Circuitry Figure A

2. Values

Ambient Temp. [°C]	Load 50%	Load 100%
	Input Volt. [V]	Input Volt. [V]
-20	72	73
-10	72	73
0	72	73
10	72	73
20	72	73
25	72	73
30	72	73
40	72	73
50	72	73
60	72	73
—	—	—

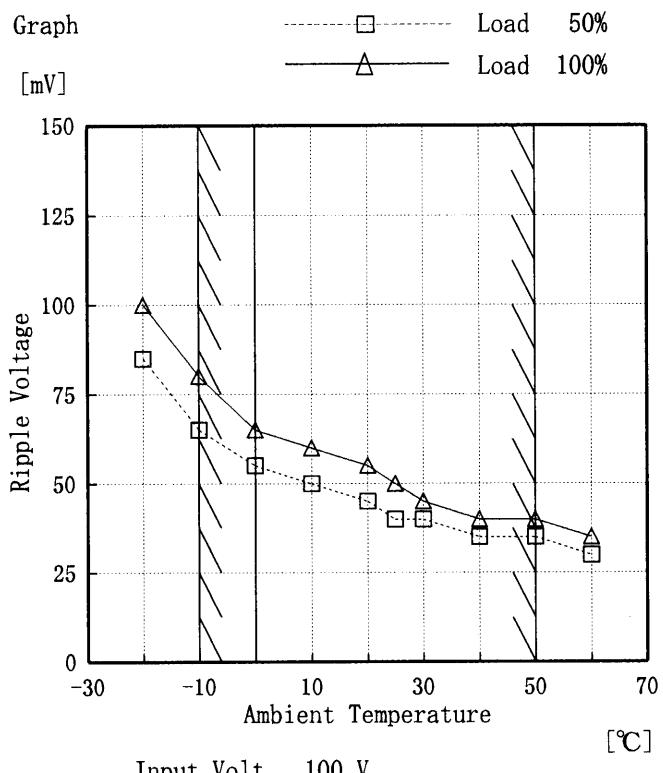
COSEL

Model LEA100F-5

Item Ripple Voltage (by Ambient Temp.)
リップル電圧 (周囲温度特性)

Object +5V 20A

1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

Testing Circuitry Figure A

2. Values

Ambient Temp. [°C]	Load 50%	Load 100%
	Ripple Output Volt. [mV]	Ripple Output Volt. [mV]
-20	85	100
-10	65	80
0	55	65
10	50	60
20	45	55
25	40	50
30	40	45
40	35	40
50	35	40
60	30	35
—	—	—

COSEL

Model	LEA100F-5	Temperature	25 °C																					
Item	Time Lapse Drift 経時ドリフト	Testing Circuitry	Figure A																					
Object	+5V 20A																							
1. Graph			2. Values																					
<p>[V]</p> <table border="1"> <caption>Data points from Figure A graph</caption> <thead> <tr> <th>Time [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>5.099</td></tr> <tr><td>0.5</td><td>5.095</td></tr> <tr><td>1.0</td><td>5.095</td></tr> <tr><td>2.0</td><td>5.095</td></tr> <tr><td>3.0</td><td>5.095</td></tr> <tr><td>4.0</td><td>5.095</td></tr> <tr><td>5.0</td><td>5.095</td></tr> <tr><td>6.0</td><td>5.095</td></tr> <tr><td>7.0</td><td>5.095</td></tr> <tr><td>8.0</td><td>5.095</td></tr> </tbody> </table>			Time [H]	Output Voltage [V]	0.0	5.099	0.5	5.095	1.0	5.095	2.0	5.095	3.0	5.095	4.0	5.095	5.0	5.095	6.0	5.095	7.0	5.095	8.0	5.095
Time [H]	Output Voltage [V]																							
0.0	5.099																							
0.5	5.095																							
1.0	5.095																							
2.0	5.095																							
3.0	5.095																							
4.0	5.095																							
5.0	5.095																							
6.0	5.095																							
7.0	5.095																							
8.0	5.095																							
<p>Output Voltage [V]</p> <p>Time [H]</p> <p>Input Volt. 100V Load 100%</p>																								



Model	LEA100F-5	Testing Circuitry Figure A
Item	Output Voltage Accuracy 定電圧精度	
Object	+5V 20A	

Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10~50 °C

Input Voltage : 85~132 V

Load Current : 0.00~20.00 A

* Output Voltage Accuracy = ±(Maximum of Output Voltage — Minimum of Output Voltage) / 2

$$* \text{ Output Voltage Accuracy (Ration)} = \frac{\text{Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

定電圧精度

周囲温度、入力電圧、負荷を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 -10~50 °C

入力電圧 85~132 V

負荷電流 0.00~20.00 A

* 定電圧精度(変動値) = ±(出力電圧の最高値—出力電圧の最低値) / 2

$$* \text{ 定電圧精度(変動率)} = \frac{\text{変動値}}{\text{定格出力電圧}} \times 100$$

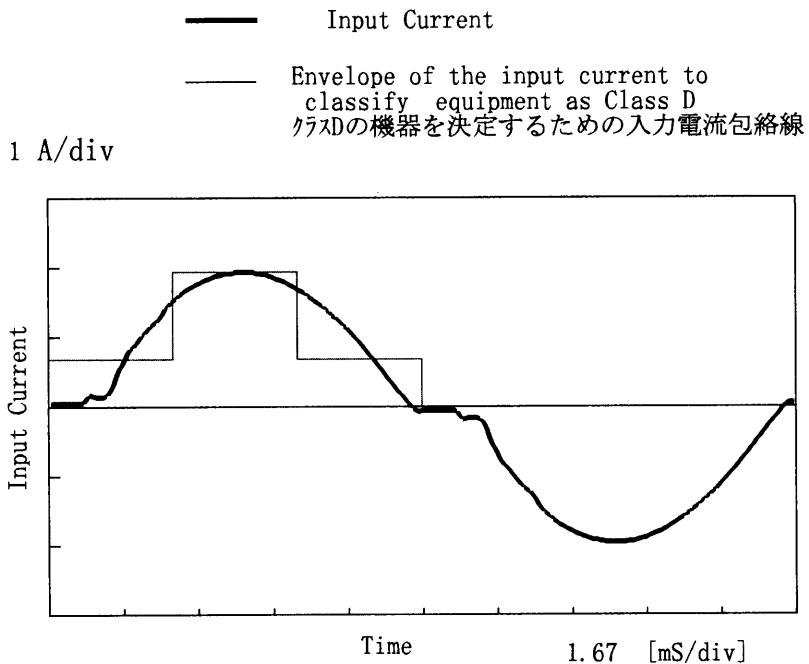
Item	Temperature [°C]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy(Ration) [%]
Maximum Voltage	50	132	0.00	5.117	±14	±0.3
Minimum Voltage	-10	100	20.00	5.089		

COSEL

Model	LEA100F-5
Item	Harmonic Current 高調波電流
Object	—

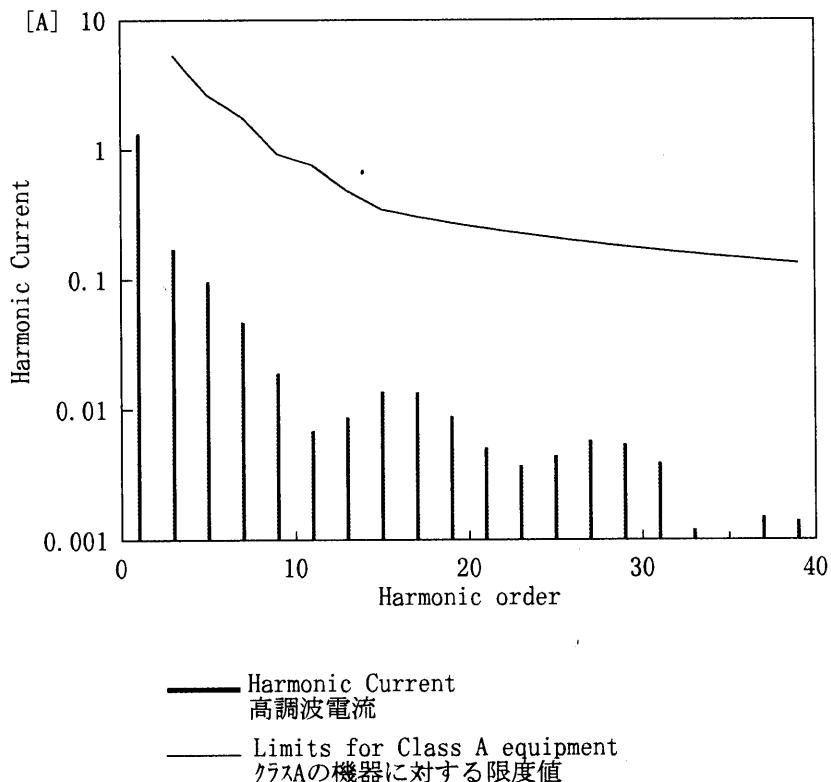
Temperature
Testing Circuitry25°C
Figure E

1. Input Current Waveform



Conditions	Values
Input Voltage [V]	99.8
Input Current [A]	1.356
Active Power [W]	133.9
Apparent Power [VA]	135.4
Frequency [Hz]	60
Power Factor	0.989
Output Power [W]	100

2. Harmonic Current

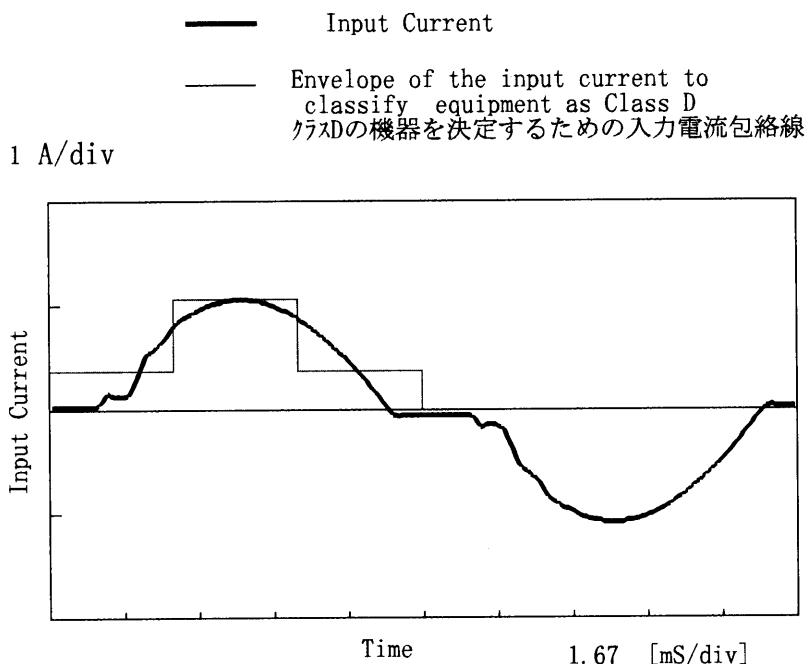


Harmonics order	Limits 限度値 [A]	Values 測定値 [A]
1	—	1.34040
2	—	0.00050
3	5.30060	0.17270
4	—	0.00030
5	2.62725	0.09700
6	—	0.00010
7	1.77455	0.04720
8	—	0.00010
9	0.92184	0.01910
10	—	0.00010
11	0.76052	0.00690
12	—	0.00010
13	0.48397	0.00870
14	—	0.00010
15	0.34569	0.01370
16	—	0.00000
17	0.30502	0.01350
18	—	0.00000
19	0.27291	0.00890
20	—	0.00000
21	0.24692	0.00510
22	—	0.00010
23	0.22545	0.00370
24	—	0.00000
25	0.20741	0.00440
26	—	0.00000
27	0.19205	0.00580
28	—	0.00000
29	0.17881	0.00540
30	—	0.00010
31	0.16727	0.00390
32	—	0.00000
33	0.15713	0.00120
34	—	0.00000
35	0.14815	0.00080
36	—	0.00010
37	0.14015	0.00150
38	—	0.00000
39	0.13296	0.00140
40	—	0.00010

COSEL

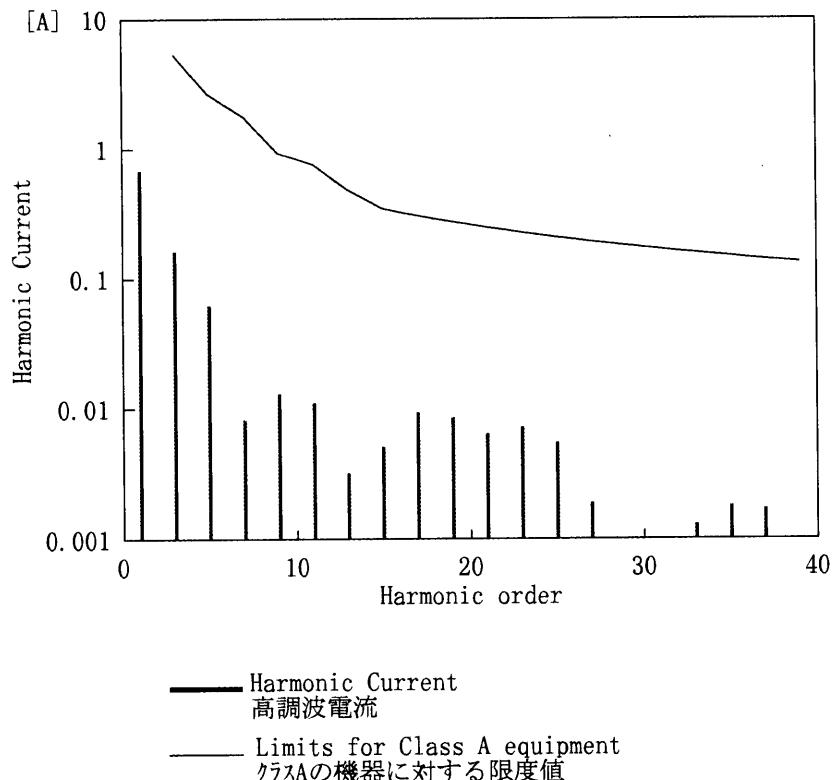
Model	LEA100F-5	Temperature Testing Circuitry	25°C Figure E
Item	Harmonic Current 高調波電流		
Object	_____		

1. Input Current Waveform



Conditions	Values
Input Voltage [V]	100.1
Input Current [A]	0.702
Active Power [W]	68
Apparent Power [VA]	70.4
Frequency [Hz]	60
Power Factor	0.966
Output Power [W]	50

2. Harmonic Current



Harmonics order 高調波次数	Limits 限度値 [A]	Values 測定値 [A]
1	—	0.68000
2	—	0.00060
3	5.28472	0.16320
4	—	0.00010
5	2.61938	0.06270
6	—	0.00010
7	1.76923	0.00820
8	—	0.00010
9	0.91908	0.01300
10	—	0.00000
11	0.75824	0.01110
12	—	0.00000
13	0.48252	0.00320
14	—	0.00010
15	0.34466	0.00510
16	—	0.00010
17	0.30411	0.00930
18	—	0.00010
19	0.27210	0.00850
20	—	0.00000
21	0.24618	0.00640
22	—	0.00000
23	0.22478	0.00720
24	—	0.00000
25	0.20679	0.00550
26	—	0.00010
27	0.19148	0.00190
28	—	0.00010
29	0.17827	0.00060
30	—	0.00000
31	0.16677	0.00030
32	—	0.00000
33	0.15666	0.00130
34	—	0.00000
35	0.14771	0.00180
36	—	0.00000
37	0.13973	0.00170
38	—	0.00000
39	0.13256	0.00100
40	—	0.00000



Model	LEA100F-5	
Item	Condensation 結露特性	Testing Circuitry Figure A
Object	+5V20A	

1. Condensation test

Testing procedure is as follows.

- ① Keeping and cooling the unit in a tank at -10°C for an hour with the input off.
- ② Taking it out of the tank and dewing itself in a room where the temperature is 25°C and the humidity is 40%RH.
- ③ Testing electrical characteristics of the unit to confirm there be no fault.

1. 結露特性試験

入力を切った状態で、恒温槽で-10°Cに冷却しておき、約1時間後に恒温槽から取り出し、室温25°C、湿度40%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

2. Values

Item	Data	Testing Conditions
Output Voltage [V]	5.119	Input Volt.: 100V, Load Current:20A
Line Regulation [mV]	1	Input Volt.: 85~132V, Load Current:20A
Load Regulation [mV]	22	Input Volt.: 100V, Load Current:0~20A



Model	LEA100F-5	Temperature Testing Circuitry Figure B
Item	Leakage Current 漏洩電流	
Object	_____	

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DENTORI	0.15	0.18	0.24
(B) IEC60950	0.15	0.18	0.24

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力の両相について測定し、その大きい方を漏洩電流測定値とする。

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B) IEC60950	—	—	—



Model	LEA100F-5	Temperature Testing Circuitry	25°C Figure C
Item	Line Noise Tolerance 入力雑音耐量		
Object	+5V 20 A		

1. Results

Pulse Width [nS]	MODE	No protection failure should occur 保護回路の誤動作がない	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation
1000	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation

Conditions

Input Voltage : 100 V
 Pulse Voltage : 2000 V
 Pulse Cycle : 10 mS
 Pulse Input Duration: 1 min. or more
 Load : 100 %

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Model	LEA100F-5	Temperature Testing Circuitry Figure D
Item	Conducted Emission 雜音端子電壓	
Object	_____	

1. Graph

Remarks

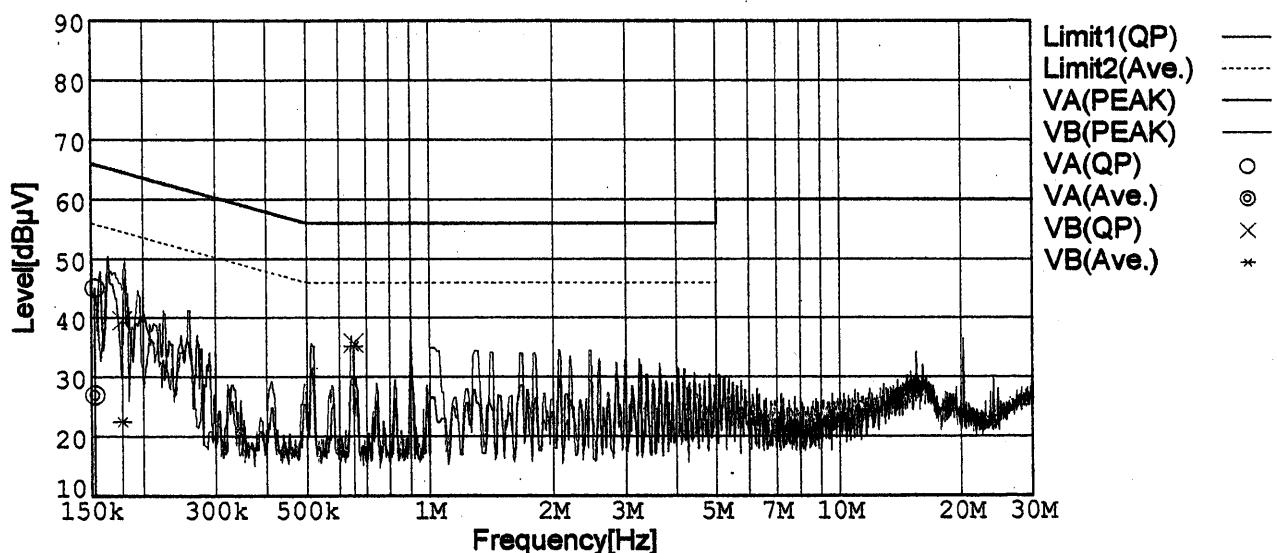
Input Volt. 100V (VCCI Class B)

120V (FCC Class B)

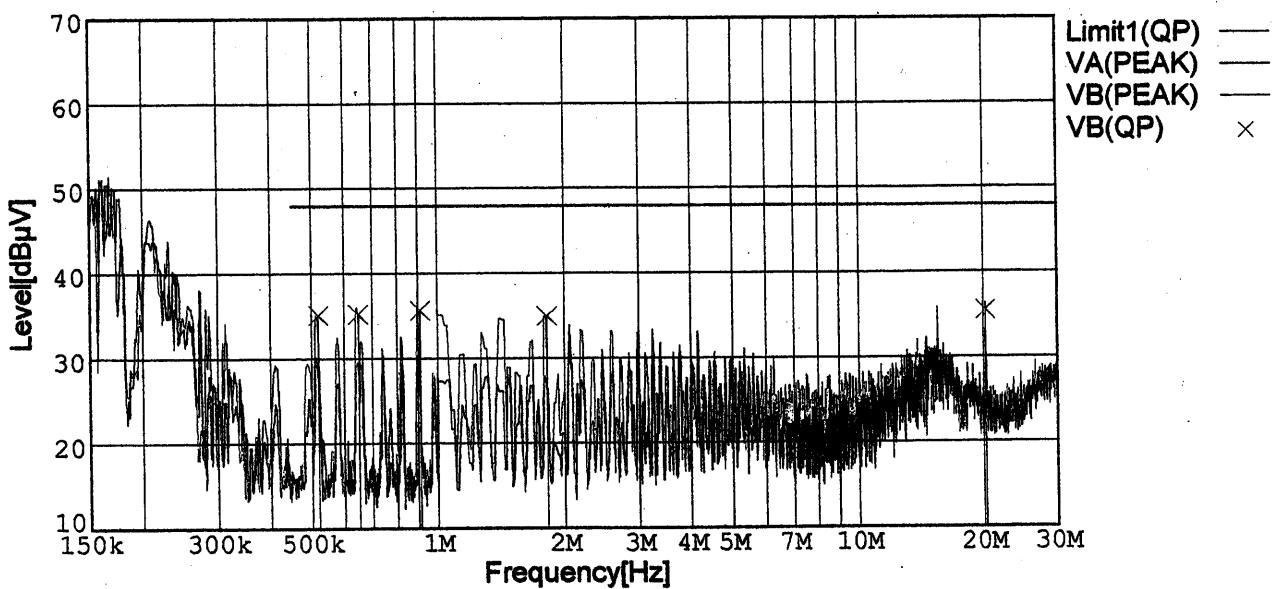
Load 100 %

Limit1: [VCCI] Class B(QP)

Limit2: [VCCI] Class B(Ave.)



Limit1: [FCC Part15] Class B



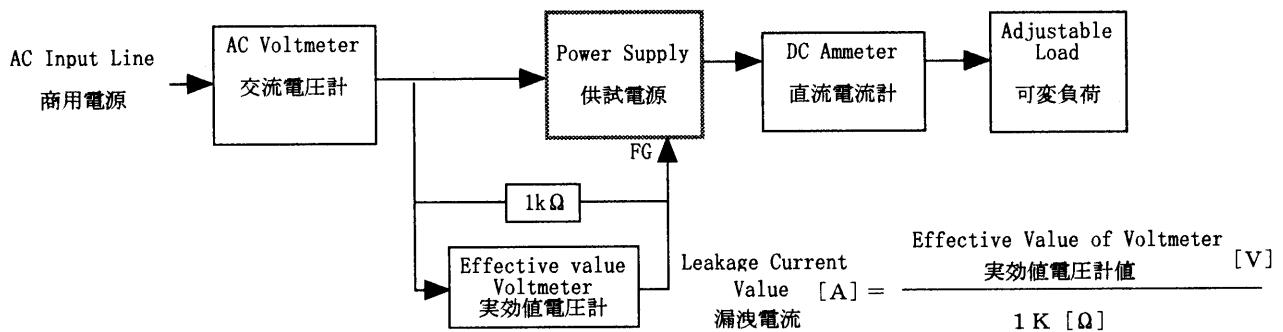
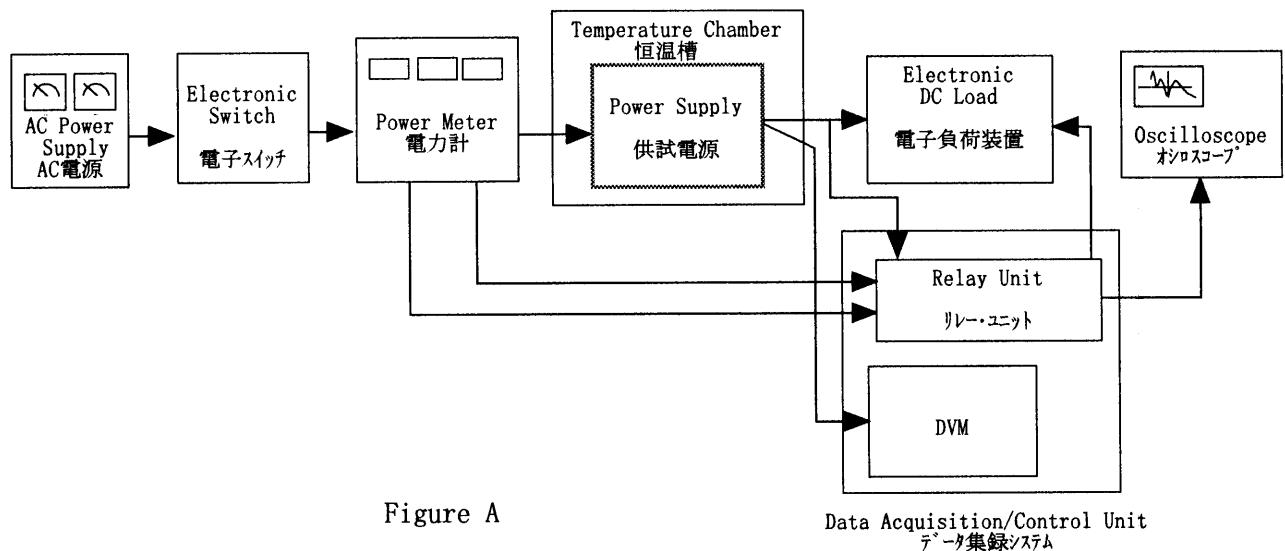


Figure B (DENTORI)

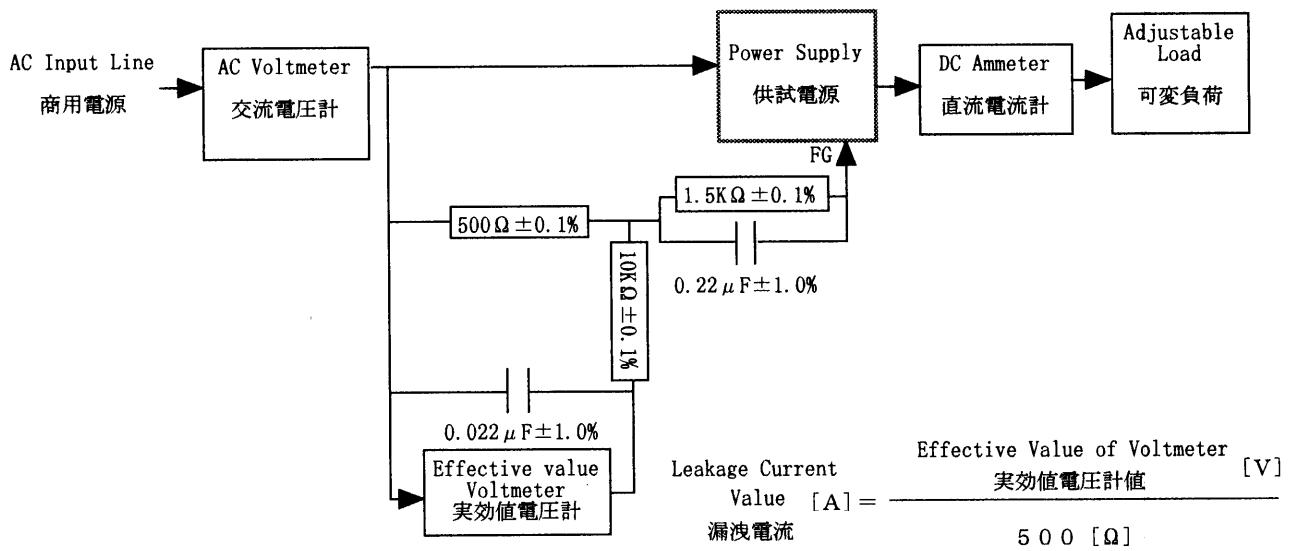


Figure B (IEC60950)

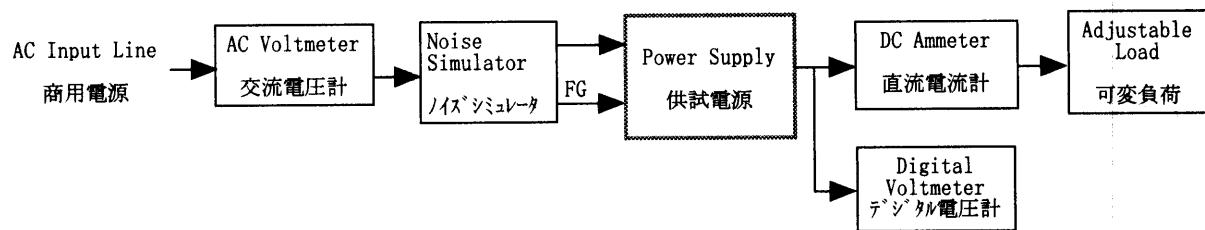


Figure C

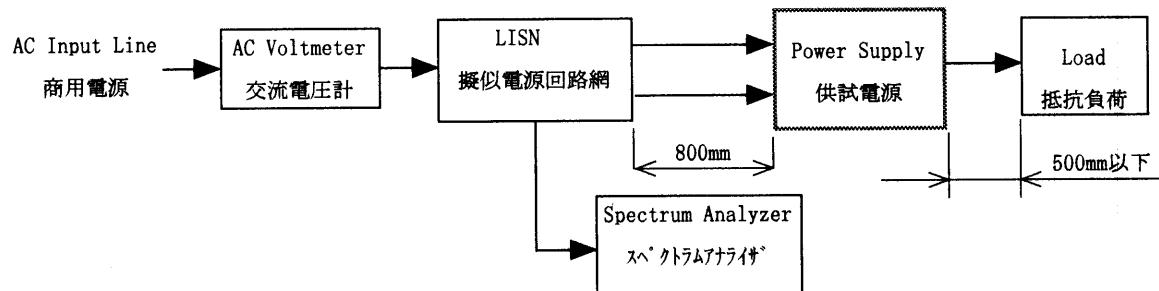


Figure D

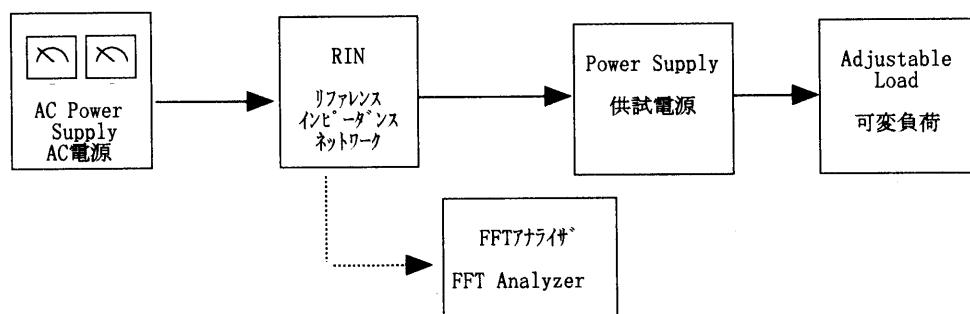


Figure E