



TEST DATA OF LDA75F-15 (100V INPUT)

Regulated DC Power Supply

Date : Aug. 20. 1999

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コーセル株式会社
COSEL CO., LTD.



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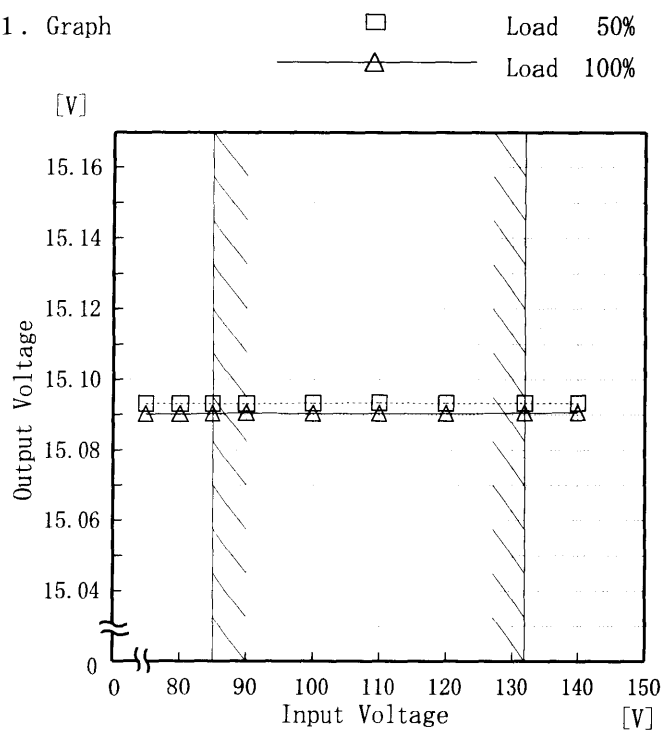
Model LDA75F-15

Item Line Regulation 静的入力変動

Object +15.0V5A

Temperature 25°C
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
75	15.093	15.090
80	15.093	15.090
85	15.093	15.091
90	15.093	15.091
100	15.093	15.091
110	15.093	15.090
120	15.093	15.091
132	15.093	15.091
140	15.093	15.091

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Model		LDA75F-15	Temperature		25℃
Item		Input Current (by Load Current) 入力電流（負荷特性）	Testing Circuitry		Figure A
Output					

1. Graph

△

Input Volt. 85V

□

Input Volt. 100V

○

Input Volt. 132V

Input Current [A]

Load Current [A]

Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Input Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	0.065	0.069	0.071
0.8	0.348	0.319	0.272
1.6	0.615	0.548	0.453
2.4	0.893	0.790	0.642
3.2	1.169	1.030	0.833
4.0	1.449	1.275	1.029
4.8	1.737	1.525	1.230
5.0	1.807	1.587	1.281
5.5	1.978	1.738	1.402
—	—	—	—
—	—	—	—
—	—	—	—

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Model		LDA75F-15		Temperature		25℃																																																								
Item		Input Power (by Load Current) 入力電力 (負荷特性)		Testing Circuitry		Figure A																																																								
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1. Graph				2. Values																																																										
<div><div>△</div> Input Volt. 85V</div> <div><div>□</div> Input Volt. 100V</div> <div><div>○</div> Input Volt. 132V</div> <div><p>Note: Slanted line shows the range of the rated load current</p><p>(注) 斜線は定格負荷電流範囲を示す。</p></div>				<table><tr><th rowspan="2">Load Current</th><th colspan="3">Input Power [W]</th></tr><tr><th>Input Volt. 85[V]</th><th>Input Volt. 100[V]</th><th>Input Volt. 132[V]</th></tr><tr><td>0.0</td><td>2.01</td><td>2.41</td><td>3.17</td></tr><tr><td>0.8</td><td>16.17</td><td>16.57</td><td>17.47</td></tr><tr><td>1.6</td><td>30.39</td><td>30.65</td><td>31.37</td></tr><tr><td>2.4</td><td>44.50</td><td>44.59</td><td>45.10</td></tr><tr><td>3.2</td><td>58.57</td><td>58.48</td><td>58.70</td></tr><tr><td>4.0</td><td>73.14</td><td>72.87</td><td>72.90</td></tr><tr><td>4.8</td><td>88.50</td><td>88.00</td><td>87.70</td></tr><tr><td>5.0</td><td>92.10</td><td>91.40</td><td>91.10</td></tr><tr><td>5.5</td><td>101.70</td><td>100.90</td><td>100.20</td></tr><tr><td>—</td><td>—</td><td>—</td><td>—</td></tr><tr><td>—</td><td>—</td><td>—</td><td>—</td></tr><tr><td>—</td><td>—</td><td>—</td><td>—</td></tr></table>				Load Current	Input Power [W]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0.0	2.01	2.41	3.17	0.8	16.17	16.57	17.47	1.6	30.39	30.65	31.37	2.4	44.50	44.59	45.10	3.2	58.57	58.48	58.70	4.0	73.14	72.87	72.90	4.8	88.50	88.00	87.70	5.0	92.10	91.40	91.10	5.5	101.70	100.90	100.20	—	—	—	—	—	—	—	—	—	—	—	—
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Model		LDA75F-15		Temperature		25℃	
Item		Efficiency (by Load Current) 効率 (負荷電流特性)		Testing Circuitry		Figure A	
Output		—————					

1. Graph

△

Input Volt. 85V

□

Input Volt. 100V

○

Input Volt. 132V

Efficiency [%]

90

80

70

60

50

40

0

2

4

6

Load Current [A]

Note: Slanted line shows the range of the rated load current

(注) 斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Efficiency [%]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
0.8	75.1	74.0	70.5
1.6	80.4	80.1	78.4
2.4	82.0	82.1	81.3
3.2	82.6	82.9	82.6
4.0	82.7	83.1	83.1
4.8	82.4	83.0	83.3
5.0	82.4	83.1	83.4
5.5	82.1	82.8	83.4
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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Model	LDA75F-15
Item	Hold-Up Time 出力保持時間
Object	+15.0V5A

1. Graph

□ Load 50%

—△— Load 100%

[mS]

1000

100

10

1

0

80

90

100

110

120

130

140

150

0

80

90

100

110

120

130

140

150

[V]

Input Voltage [V]	Hold-Up Time [mS] (Load 50%)	Hold-Up Time [mS] (Load 100%)
75	22	10
80	27	12
85	33	15
90	40	18
100	53	25
110	68	33
120	85	41
132	107	53
140	123	61

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated input voltage.

出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。

(注)斜線は定格入力電圧範囲を示す。

Temperature

25°C

Testing Circuitry

Figure A

2. Values

Input Voltage [V]	Hold-Up Time [mS]	
	Load 50%	Load 100%
75	22	10
80	27	12
85	33	15
90	40	18
100	53	25
110	68	33
120	85	41
132	107	53
140	123	61

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Model	LDA75F-15
Item	Instantaneous Interruption Compensation 瞬時停電保障
Object	+15.0V5A

1. Graph

△

□

○

Input Volt. 85 V

Input Volt. 100 V

Input Volt. 132 V

[mS]

1000

100

10

1

Instantaneous Compensation Time

0

2

4

6

[A]

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note:Slanted line shows the range of the rated load current.

瞬時停電保障時間とは、出力電圧が定電圧精度の規格範囲を保持している瞬時停電時間をいう。

(注)斜線は定格負荷電流範囲を示す。

Temperature

25℃

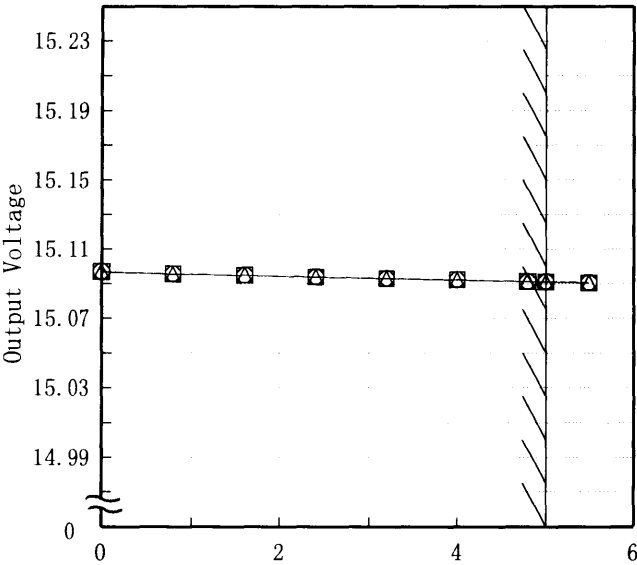
Testing Circuitry

Figure A

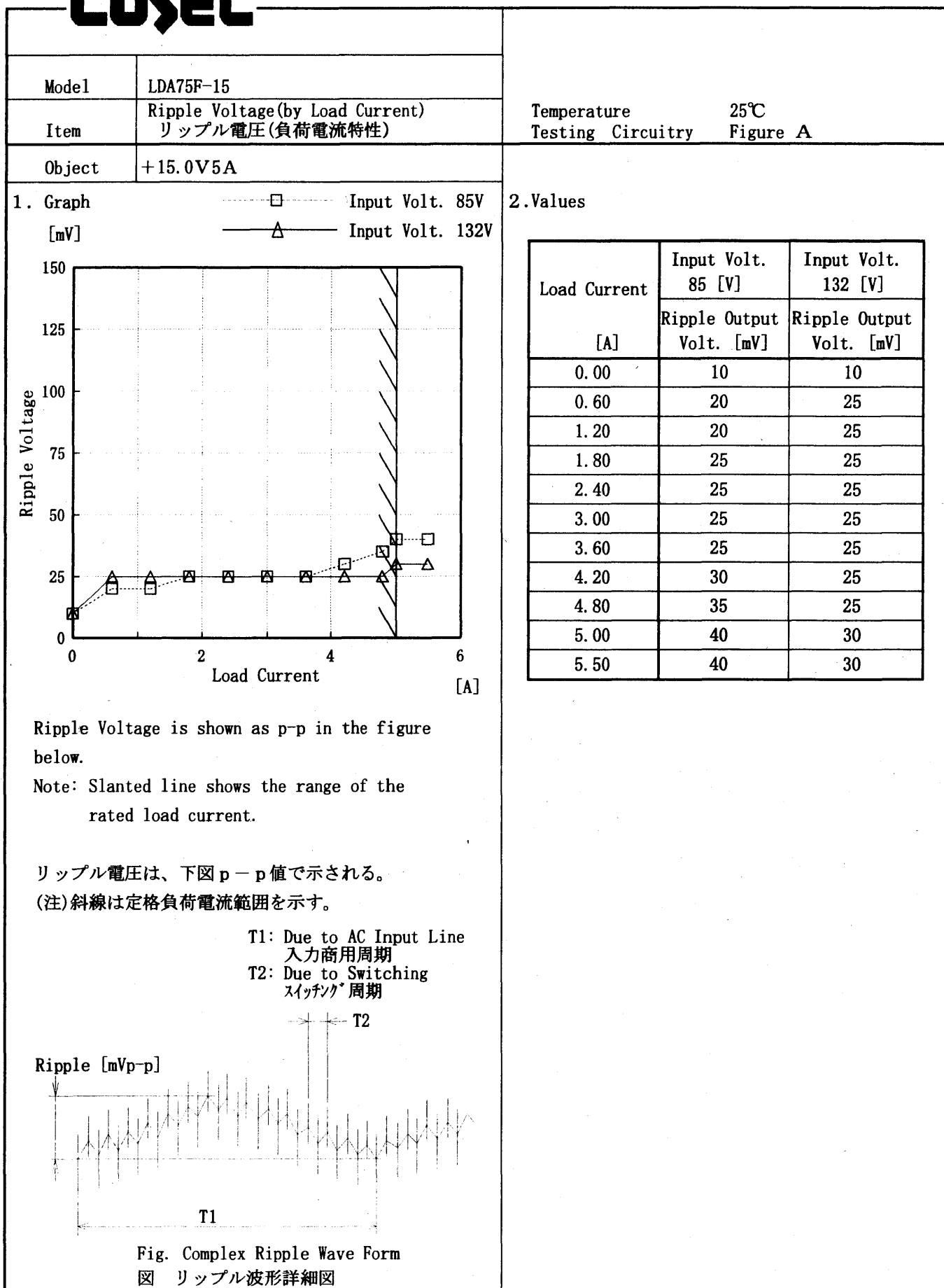
2. Values

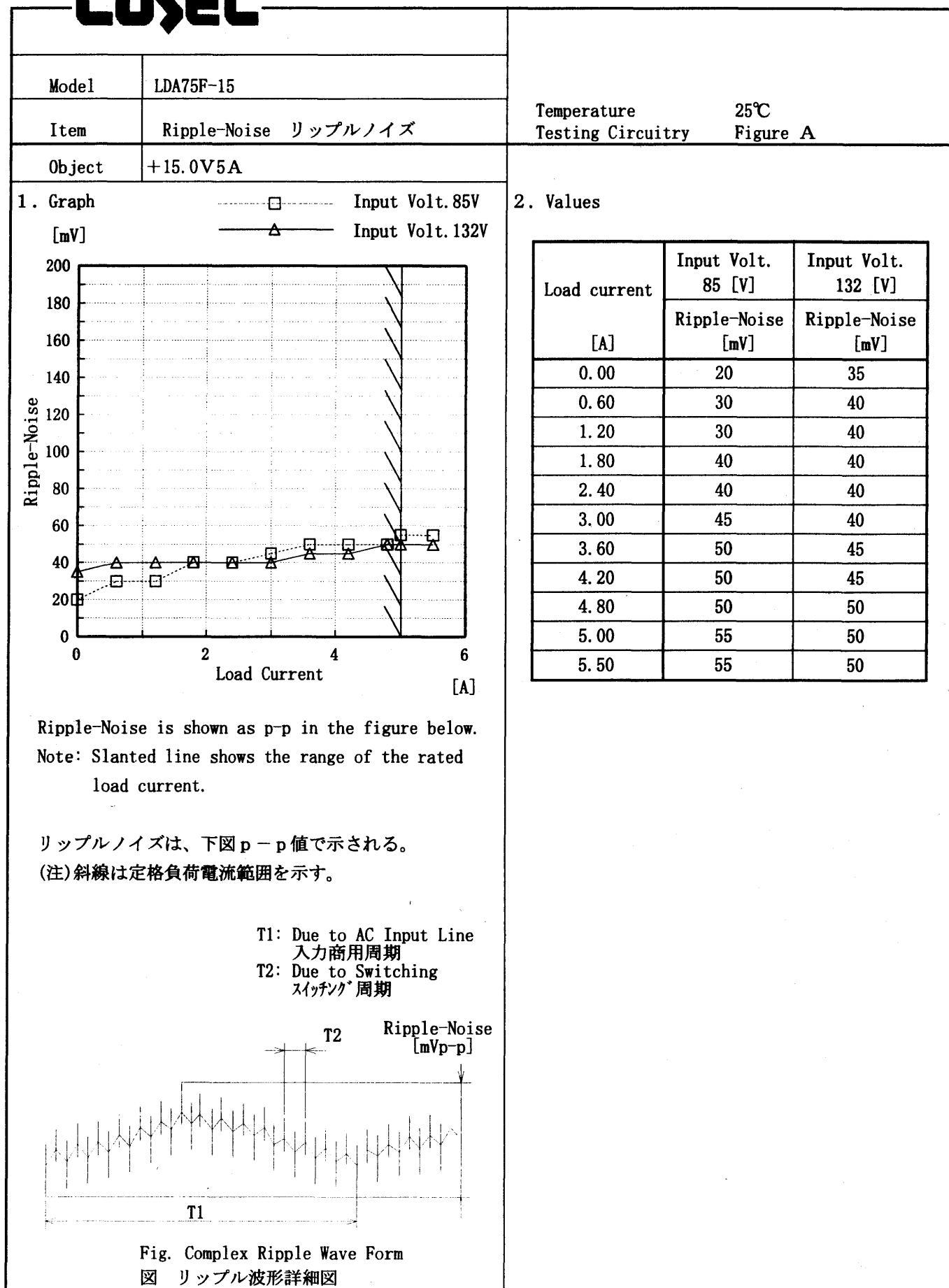
Load Current [A]	Time [mS]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	—	—	—
0.8	96	155	307
1.6	48	81	165
2.4	31	54	112
3.2	22	39	82
4.0	14	31	65
4.8	14	23	54
5.0	13	23	52
5.5	12	21	46
—	—	—	—
—	—	—	—

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Model		LDA75F-15		Temperature		25℃																																																
Item		Load Regulation 静的負荷変動		Testing Circuitry		Figure A																																																
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Load Current [A]	Output Voltage [V]																																																					
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Model		LDA75F-15	Temperature		25℃																																																							
Item		Overcurrent Protection 過電流保護	Testing Circuitry		Figure A																																																							
Object		+15.0V5A																																																										
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13.50	6.89	6.84	6.85																																																									
12.00	6.93	6.90	6.92																																																									
10.50	6.98	6.96	6.98																																																									
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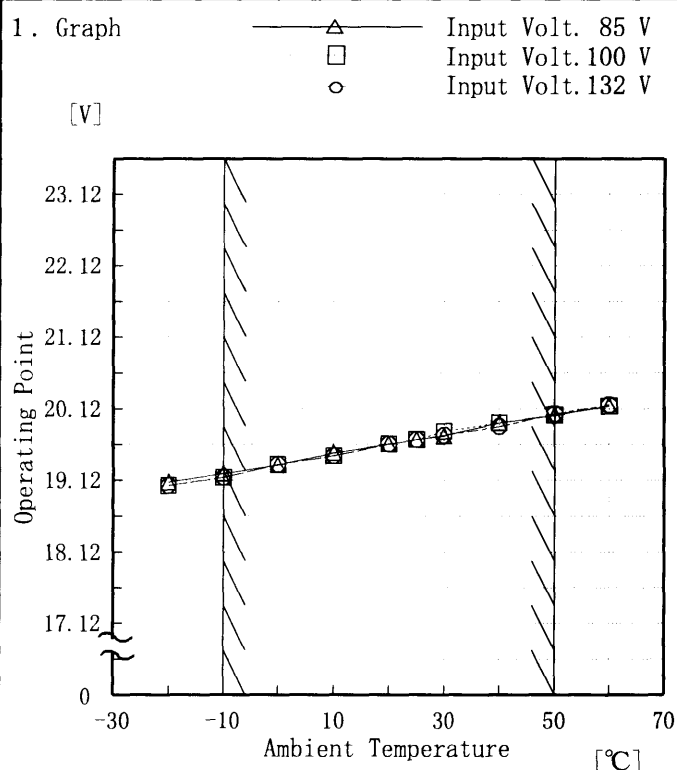
Model LDA75F-15

Item Overvoltage Protection
過電圧保護

Object +15.0V5A

Testing Circuitry Figure A

1. Graph

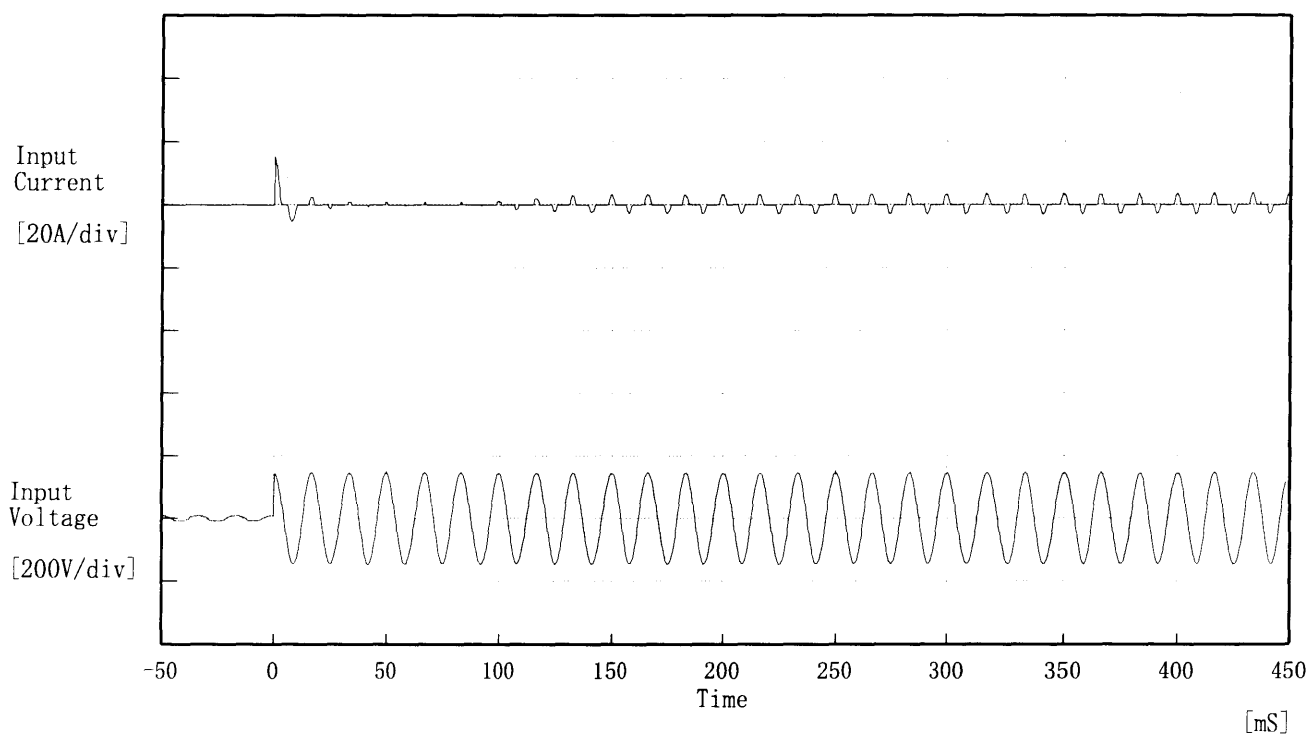


2. Values

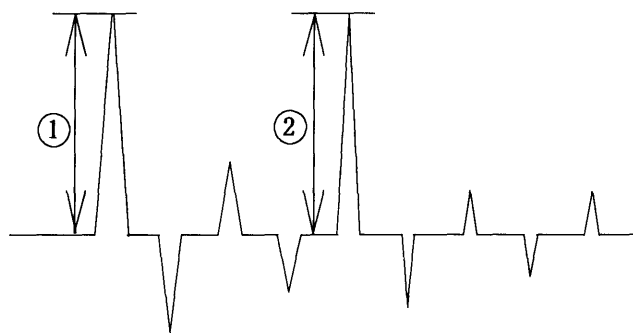
Ambient Temperature [°C]	Operating Point [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
-20	19.09	19.04	19.04
-10	19.21	19.16	19.16
0	19.33	19.34	19.34
10	19.50	19.46	19.46
20	19.63	19.63	19.63
25	19.69	19.69	19.69
30	19.74	19.80	19.75
40	19.92	19.92	19.87
50	20.03	20.04	20.05
60	20.16	20.16	20.17
—	—	—	—

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Model	LDA75F-15	Temperature 25°C Testing Circuitry Figure A
Item	Inrush Current 突入電流	
Object		



Input Voltage 100 V
Frequency 60 Hz
Load 100 %
Inrush Current
① 15.16 [A]
② 3.56 [A]



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Model	LDA75F-15	Temperature 25°C Testing Circuitry Figure A
Item	Dynamic Load Responce 動的負荷変動	
Object	+15.0V5A	

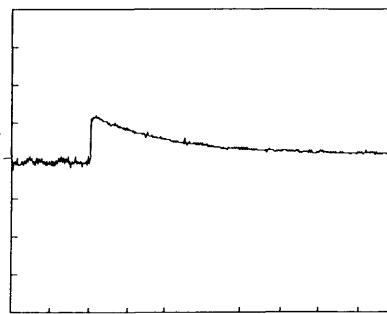
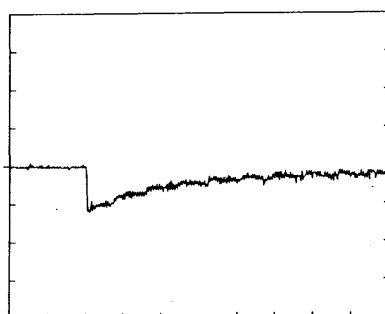
Input Volt. 100 V

Cycle 1000 mS

Load Current

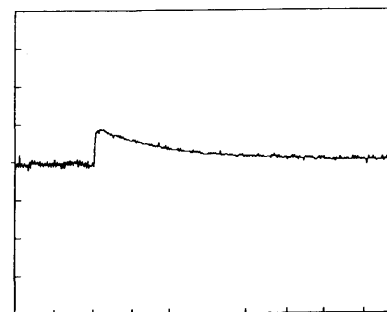
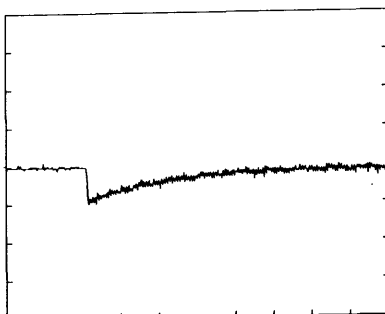
Load 0% ↔

Load 100 %



Load 0% ↔

Load 50 %



100 mV/div

10 mS/div

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Model LDA75F-15

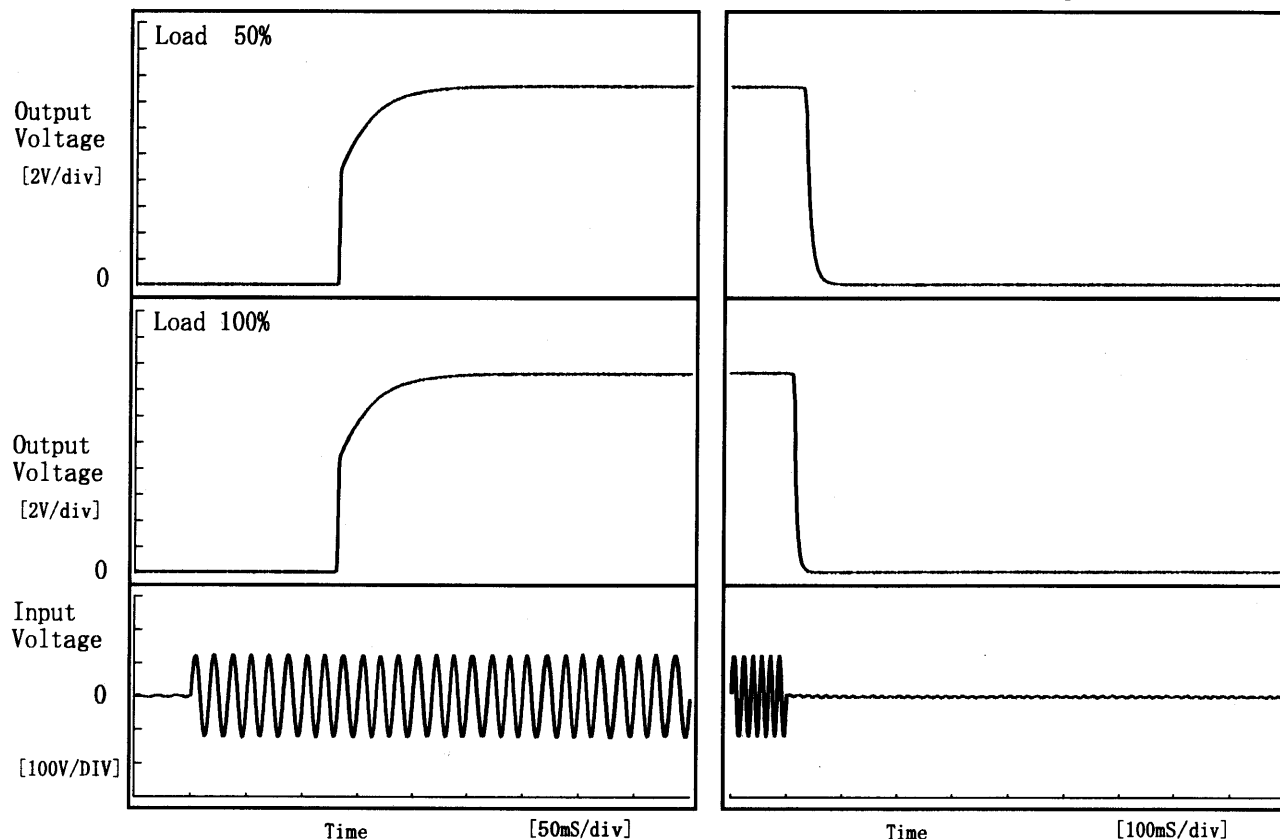
Item Rise and Fall Time 立上り、立下り時間

Temperature 25°C
Testing Circuitry Figure A

Object +15.0V5A

1. Graph

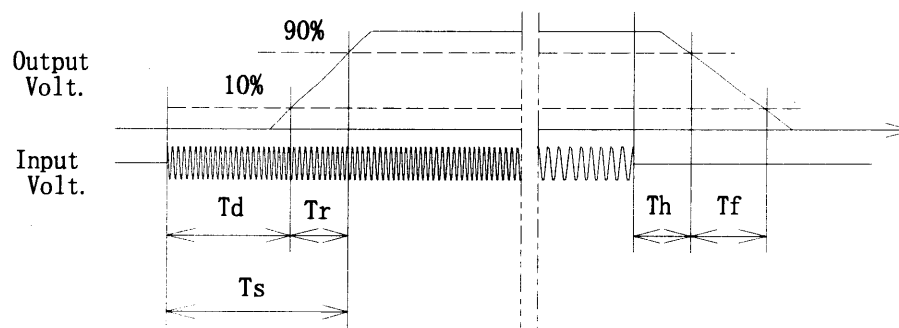
Input Volt. 85 V



2. Values

[mS]

Load \ Time	T d	T r	T s	T h	T f
50 %	131.5	39.5	171.0	33.5	19.5
100 %	131.5	39.5	171.0	15.0	11.0



COSEL

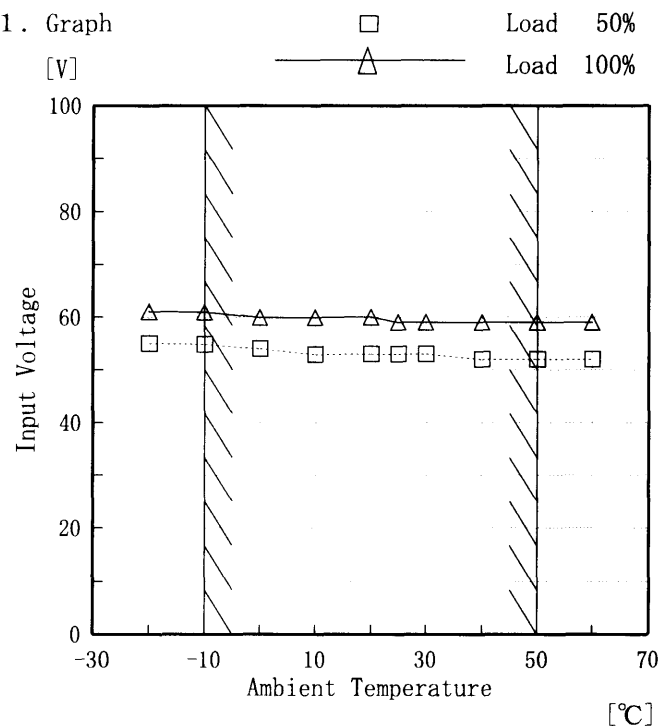
Model LDA75F-15

Item Minimum Input Voltage for Regulated Output Voltage
最低レギュレーション電圧

Object +15.0V5A

Testing Circuitry Figure A

1. Graph



2. Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	55	61
-10	55	61
0	54	60
10	53	60
20	53	60
25	53	59
30	53	59
40	52	59
50	52	59
60	52	59
—	—	—

Note: Slanted line shows the range of the rated ambient temperature.

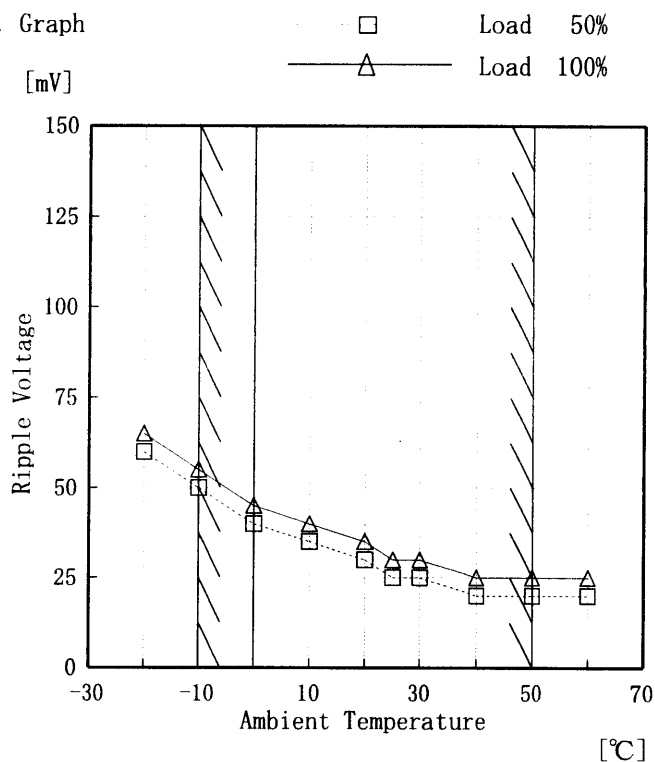
(注)斜線は定格周囲温度範囲を示す。

COSEL

Model	LDA75F-15
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)
Object	+15.0V5A

Testing Circuitry Figure A

1. Graph



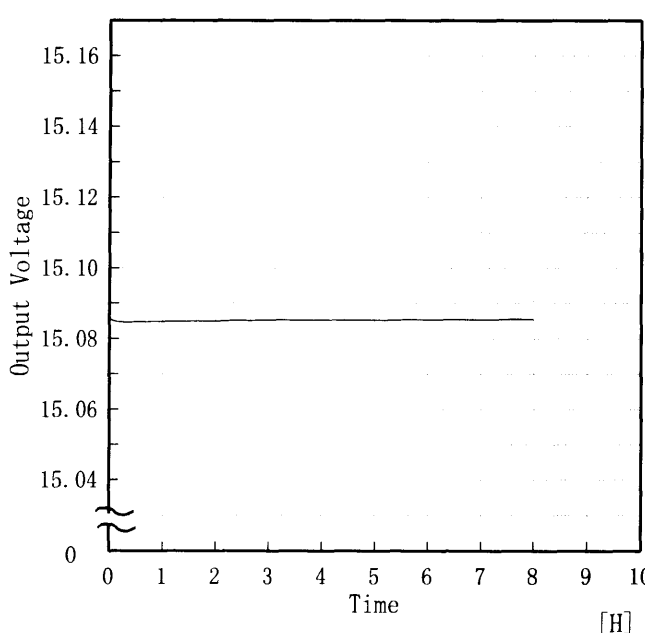
Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。

2. Values

Ambient Temp. [°C]	Load 50% Ripple Output Volt. [mV]	Load 100% Ripple Output Volt. [mV]
-20	60	65
-10	50	55
0	40	45
10	35	40
20	30	35
25	25	30
30	25	30
40	20	25
50	20	25
60	20	25
—	—	—

COSEL

COSEL																									
Model	LDA75F-15																								
Item	Time Lapse Drift 経時ドリフト	Temperature	25℃																						
		Testing Circuitry	Figure A																						
Object	+15.0V5A																								
1. Graph		2.Values																							
<div>[V]</div> <div></div> <div>Output Voltage</div> <div>Time</div> <div>[H]</div> <div>Input Volt. 100V</div> <div>Load 100%</div>		<table><tr><th>Time since start [H]</th><th>Output Voltage [V]</th></tr><tr><td>0.0</td><td>15.086</td></tr><tr><td>0.5</td><td>15.085</td></tr><tr><td>1.0</td><td>15.085</td></tr><tr><td>2.0</td><td>15.085</td></tr><tr><td>3.0</td><td>15.085</td></tr><tr><td>4.0</td><td>15.085</td></tr><tr><td>5.0</td><td>15.085</td></tr><tr><td>6.0</td><td>15.085</td></tr><tr><td>7.0</td><td>15.085</td></tr><tr><td>8.0</td><td>15.086</td></tr></table>		Time since start [H]	Output Voltage [V]	0.0	15.086	0.5	15.085	1.0	15.085	2.0	15.085	3.0	15.085	4.0	15.085	5.0	15.085	6.0	15.085	7.0	15.085	8.0	15.086
Time since start [H]	Output Voltage [V]																								
0.0	15.086																								
0.5	15.085																								
1.0	15.085																								
2.0	15.085																								
3.0	15.085																								
4.0	15.085																								
5.0	15.085																								
6.0	15.085																								
7.0	15.085																								
8.0	15.086																								

COSEL

Model		LDA75F-15	Testing Circuitry Figure A
Item		Output Voltage Accuracy 定電圧精度	
Object		+15.0V5A	

Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10~50 °C

Input Voltage : 85~132 V

Load Current : 0~5 A

* Output Voltage Accuracy = $\pm (\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

* Output Voltage Accuracy (Ration) = $\frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$

定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 -10~50 °C

入力電圧 85~132 V

負荷電流 0~5 A

* 定電圧精度(変動値) = $\pm (\text{出力電圧の最高値} - \text{出力電圧の最低値}) / 2$

* 定電圧精度(変動率) = $\frac{\text{変動値}}{\text{定格出力電圧}} \times 100$

Item	Temperature [°C]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy (Ration) [%]
Maximum Voltage	50	85	0	15.099	±16	±0.2
Minimum Voltage	-10	85	5	15.069		

COSEL

Model		LDA75F-15	Testing Circuitry Figure A
Item		Condensation 結露特性	
Object		+15.0V5A	

1. Condensation test

Testing procedure is as follows.

- ① Keeping and cooling the unit in a tank at -10°C for an hour with the input off.
- ② Taking it out of the tank and dewing itself in a room where the temperature is 25°C and the humidity is 40%RH.
- ③ Testing electrical characteristics of the unit to confirm there be no fault. .

1. 結露特性試験

入力を切った状態で、恒温槽で -10°C に冷却しておき、約1時間後に恒温槽から取り出し、室温 25°C 、湿度40%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

2. Values

Item	Data	Testing Conditions
Output Voltage [V]	15.091	Input Volt.: 100V, Load Current:5A
Line Regulation [mV]	5	Input Volt.: 85~132V, Load Current:5A
Load Regulation [mV]	9	Input Volt.: 100V, Load Current:0~5A

COSEL

Model	LDA75F-15	Temperature	25℃
Item	Leakage Current 漏洩電流	Testing Circuitry	Figure B
Object	_____		

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DENTORI	0.15	0.16	0.22
(B) IEC60950	0.14	0.16	0.20

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B) IEC60950	—	—	—

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力 of 両相について測定し、その大きい方を漏洩電流測定値とする。

COSEL

Model		LDA75F-15	Temperature Testing Circuitry	25°C Figure C
Item		Line Noise Tolerance 入力雑音耐量		
Object		+15.0V5A		

1. Results

Pulse Width [nS]	MODE	No protection failure should occur 保護回路の誤動作がない	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation
1000	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation

2. Conditions

Input Voltage : 100 V
 Pulse Voltage : 2000 V
 Pulse Cycle : 10 mS
 Pulse Input Duration : 1 min. or more
 Load : 100 %

COSEL

Model	LDA75F-15		
Item	Conducted Emission 雑音端子電圧	Temperature	25°C
Object		Testing Circuitry	Figure D

1. Graph

Remarks

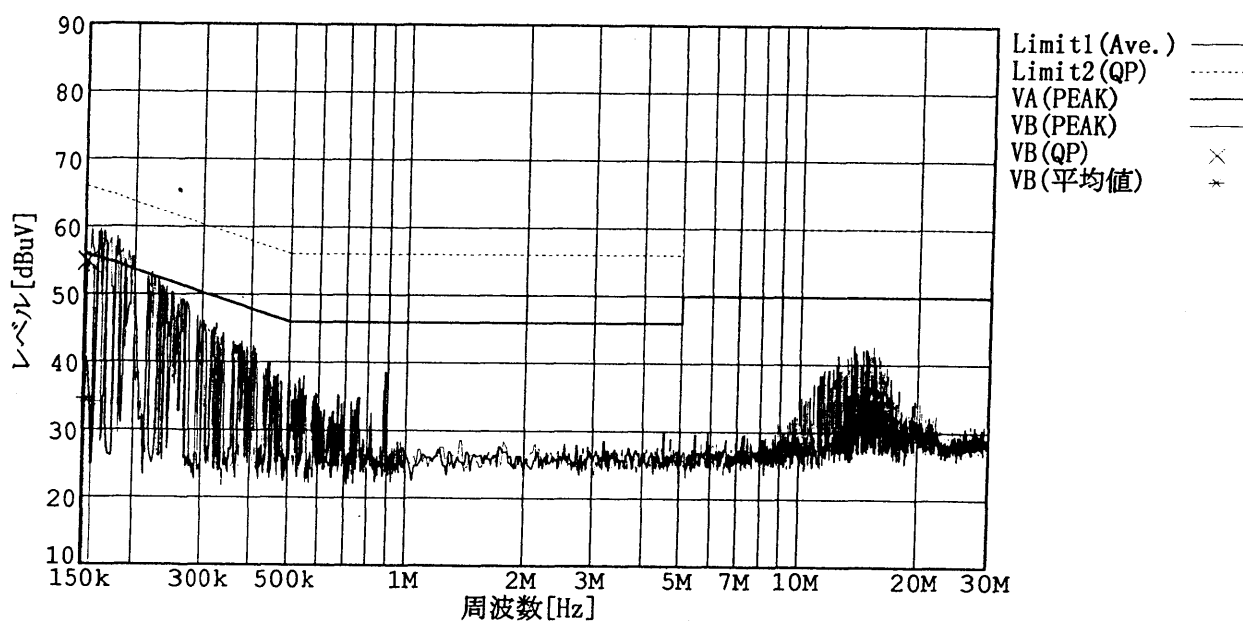
Input Volt. 100 V (VCCI Class B)

120 V (FCC Class B)

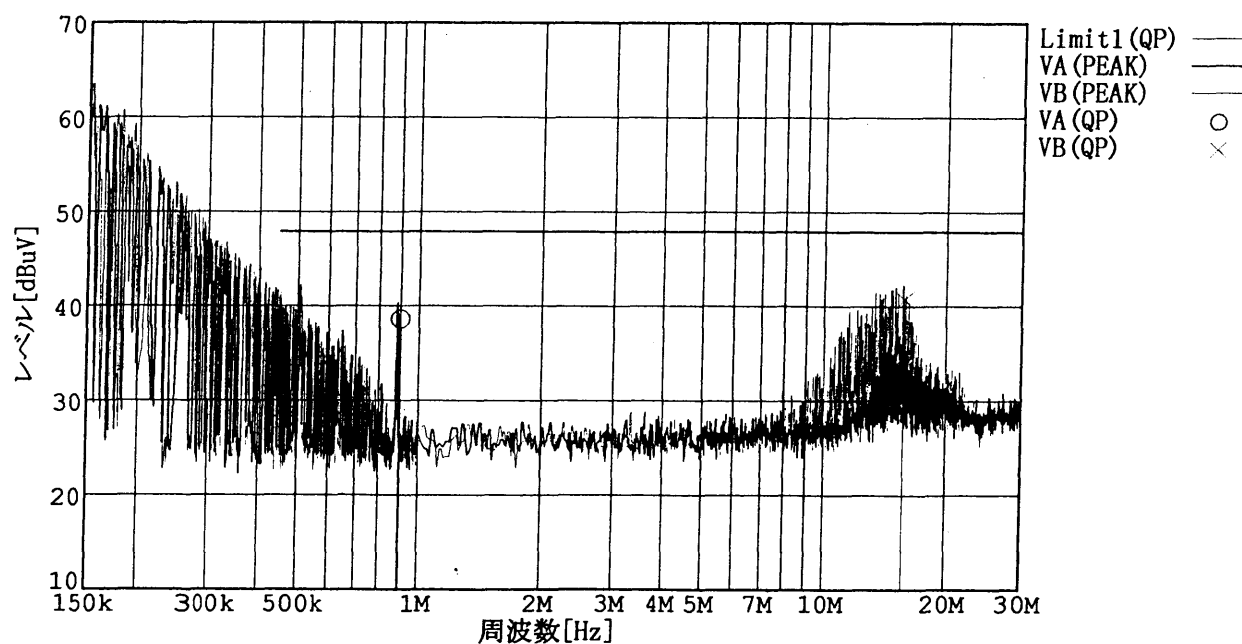
Load 100 %

規格 1 : [VCCI] Class B(平均値)

規格 2 : [VCCI] Class B(QP)



規格 1 : [FCC Part15] Class B



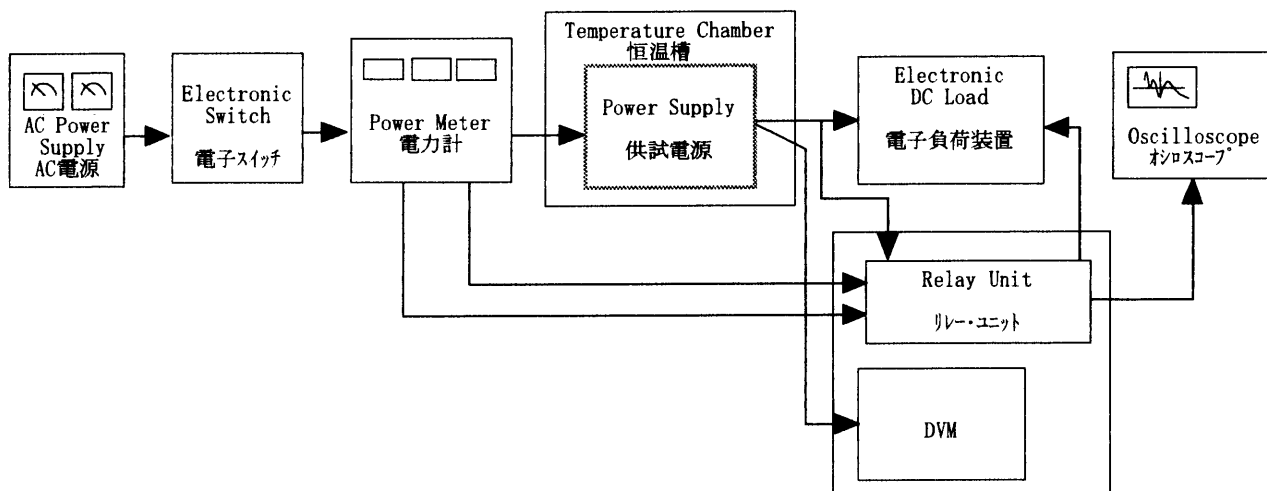


Figure A

Data Acquisition/Control Unit
データ集録システム

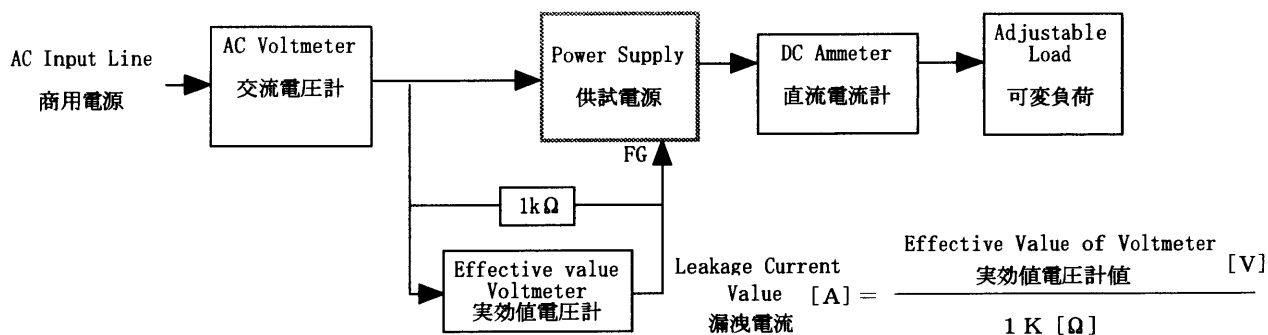


Figure B (DENTORI)

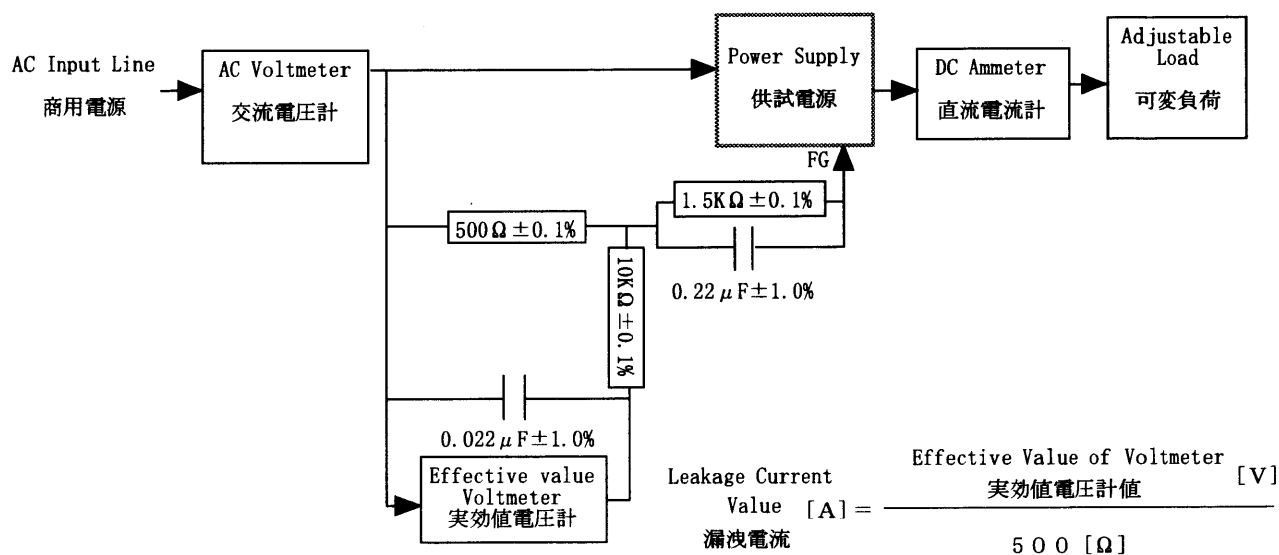


Figure B (IEC 60950)

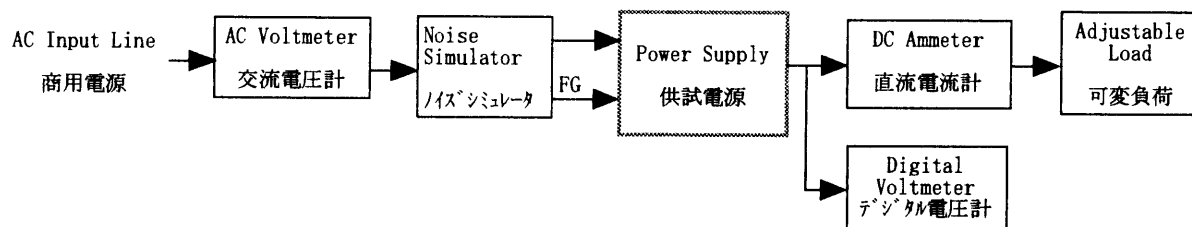


Figure C

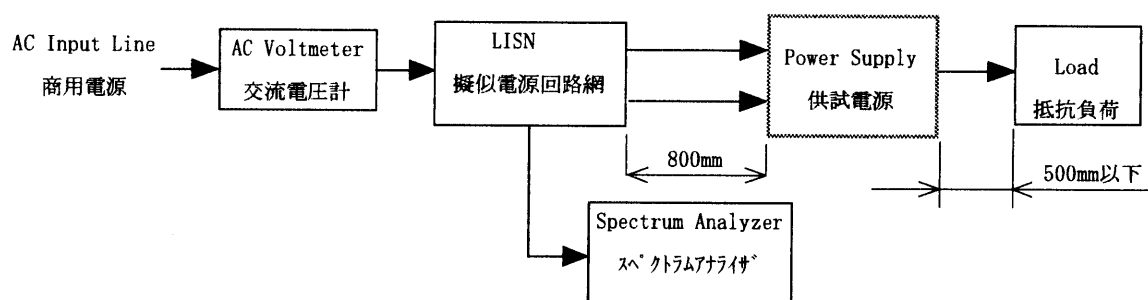


Figure D

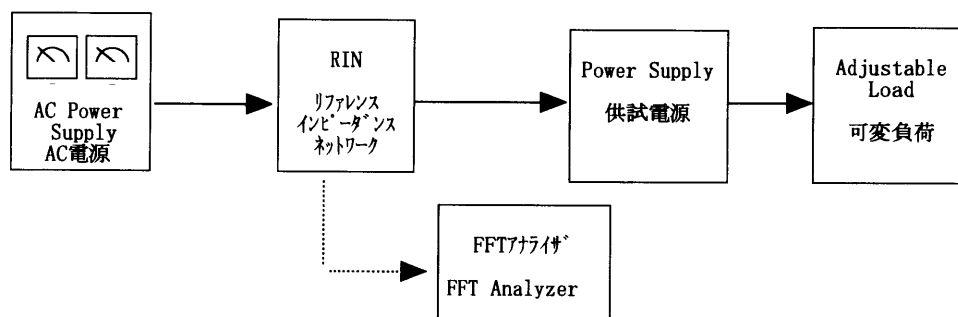


Figure E