参考資料

COSEL

TEST DATA OF LDA50F-5

(100V INPUT)

Regulated DC Power Supply

Aug. 23, 1999

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Design Engineer

コーセル株式会社

COSEL CO., LTD.

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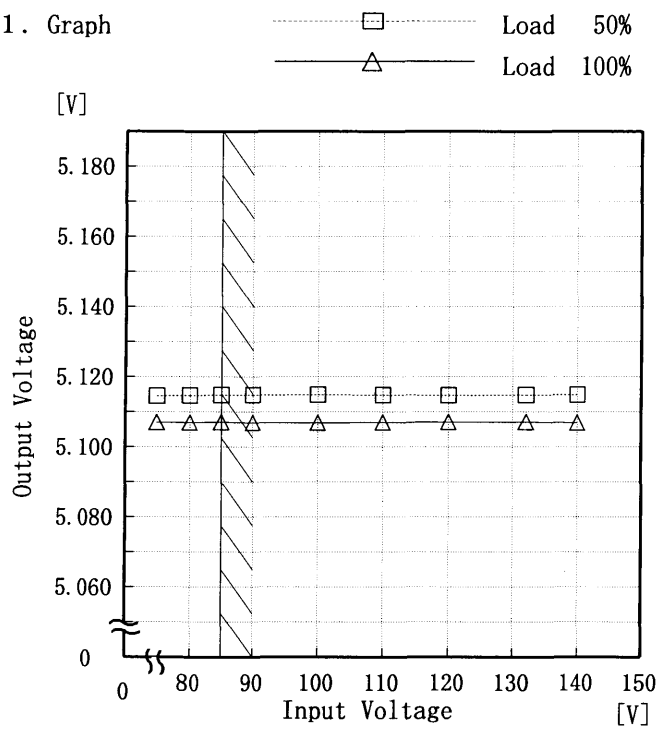
1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
75	5.115	5.107
80	5.115	5.107
85	5.115	5.107
90	5.115	5.107
100	5.115	5.107
110	5.115	5.107
120	5.115	5.107
132	5.115	5.107
140	5.115	5.107

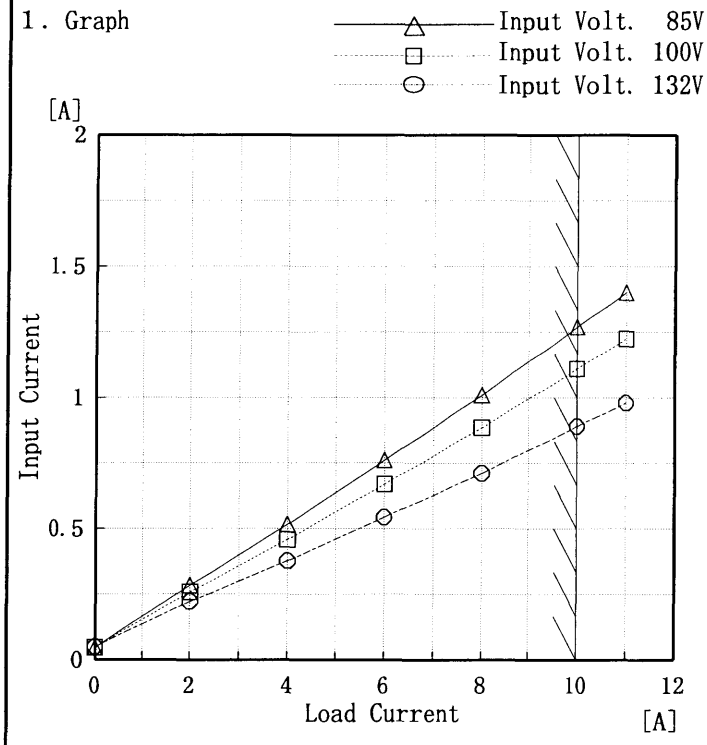
BC−4074

COSEL

Model	LDA50F-5
Item	Input Current (by Load Current) 入力電流（負荷特性）
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

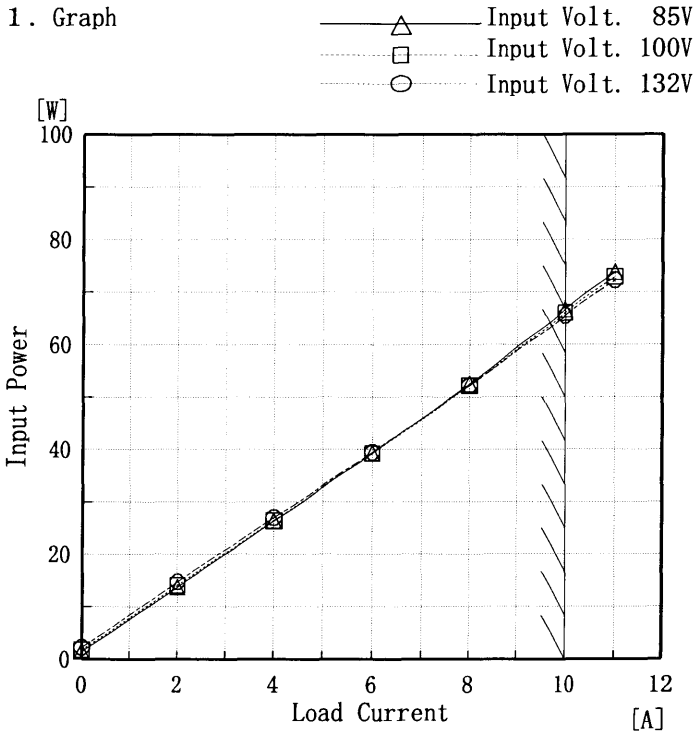
Load Current [A]	Input Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	0.046	0.047	0.050
2	0.285	0.259	0.222
4	0.516	0.459	0.378
6	0.762	0.670	0.544
8	1.010	0.886	0.712
10	1.269	1.110	0.890
11	1.401	1.224	0.980
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

COSEL

Model	LDA50F-5
Item	Input Power (by Load Current) 入力電力（負荷特性）
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

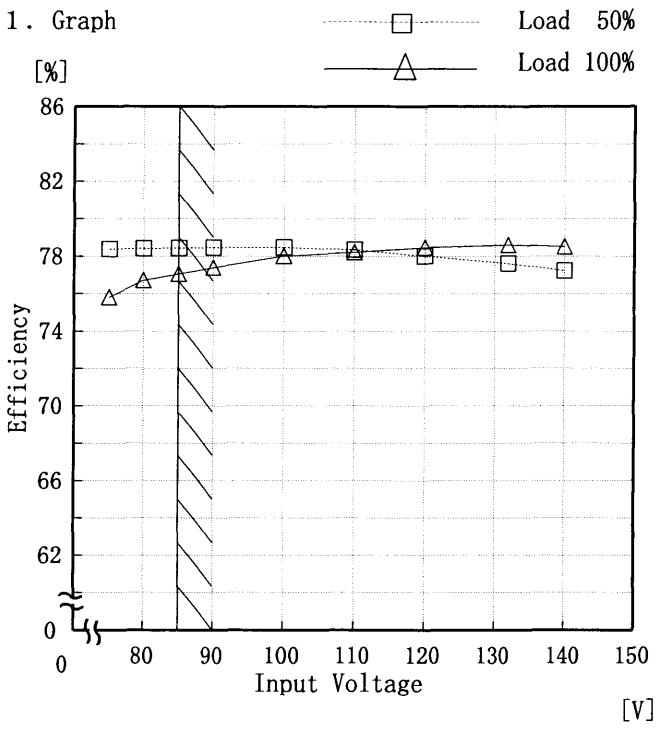
2. Values

Load Current [A]	Input Power [W]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	1.47	1.71	2.25
2	13.75	14.05	14.76
4	26.35	26.49	26.98
6	39.29	39.21	39.45
8	52.48	52.18	52.20
10	66.63	66.00	65.50
11	73.82	72.96	72.30
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

COSEL

Model	LDA50F-5	Temperature	25℃
Item	Efficiency 効率	Testing Circuitry	Figure A
Object	———————————		

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Efficiency [%]	
	Load 50%	Load 100%
75	78.4	75.8
80	78.4	76.7
85	78.4	77.1
90	78.5	77.4
100	78.5	78.0
110	78.3	78.2
120	78.0	78.5
132	77.6	78.6
140	77.2	78.5

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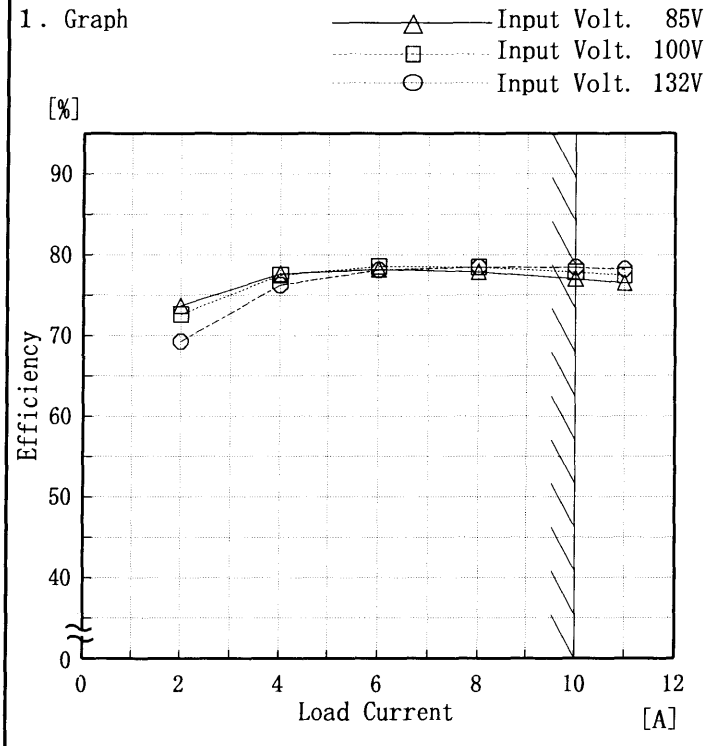
Model	LDA50F-5
Item	Efficiency (by Load Current) 効率 (負荷特性)
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Efficiency [%]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
2	73.7	72.6	69.3
4	77.7	77.5	76.3
6	78.2	78.5	78.1
8	77.9	78.5	78.5
10	77.0	77.8	78.4
11	76.5	77.5	78.2
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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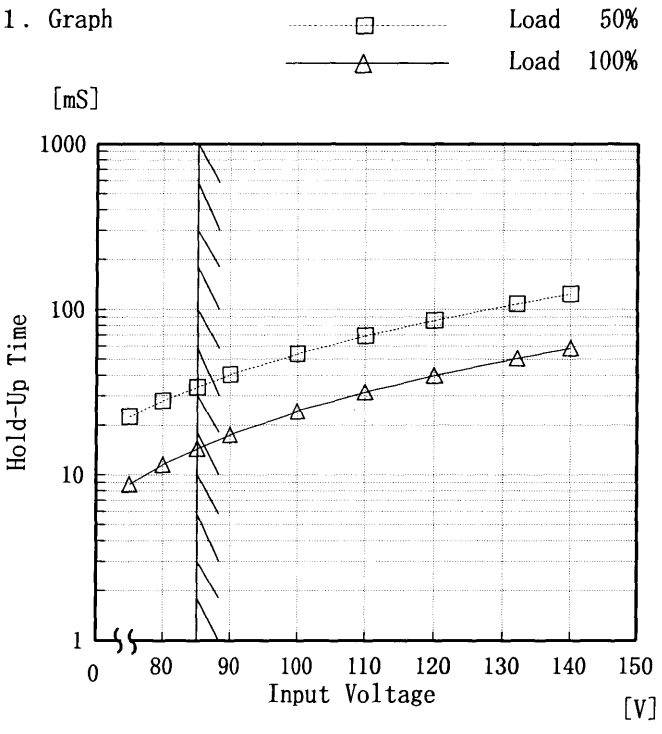
Model	LDA50F-5
Item	Hold-Up Time　出力保持時間
Object	+5.0V10A

Temperature　25℃
Testing Circuitry　Figure A

1. Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated input voltage.

出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Hold-Up Time [mS]	
	Load 50%	Load 100%
75	23	9
80	28	12
85	34	14
90	40	17
100	54	24
110	69	32
120	86	40
132	108	51
140	124	59

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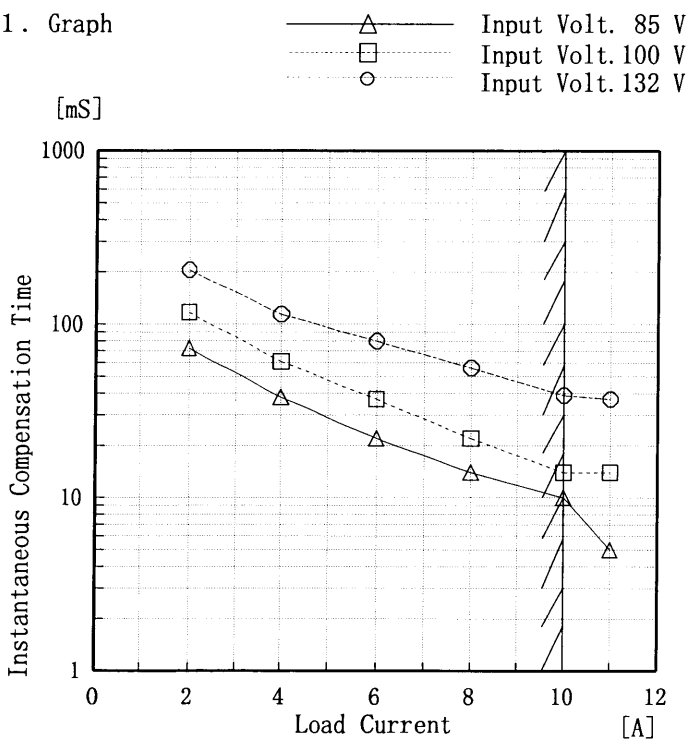
Model	LDA50F-5
Item	Instantaneous Interruption Compensation 瞬時停電保障
Object	+5.0V10A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.
Note:Slanted line shows the range of the rated load current.

瞬時停電保障時間とは、出力電圧が定電圧精度の規格範囲を保持している瞬時停電時間をいう。
(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Time [mS]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	—	—	—
2	73	117	206
4	38	61	114
6	22	37	80
8	14	22	56
10	10	14	39
11	5	14	37
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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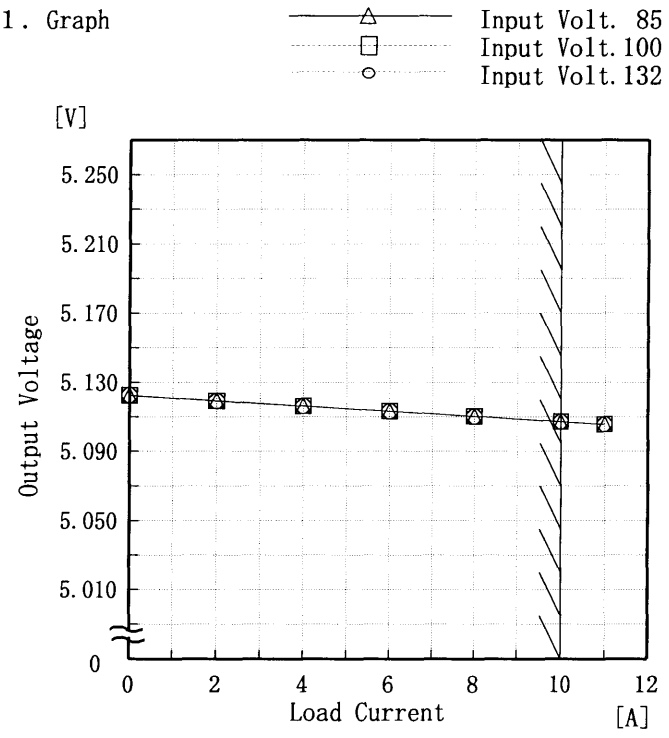
Model	LDA50F-5
Item	Load Regulation 静的負荷変動
Object	+5.0V10A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	5.122	5.123	5.122
2	5.119	5.119	5.119
4	5.116	5.116	5.116
6	5.113	5.113	5.113
8	5.110	5.110	5.110
10	5.107	5.107	5.107
11	5.106	5.106	5.106
—	—	—	—
—	—	—	—
—	—	—	—

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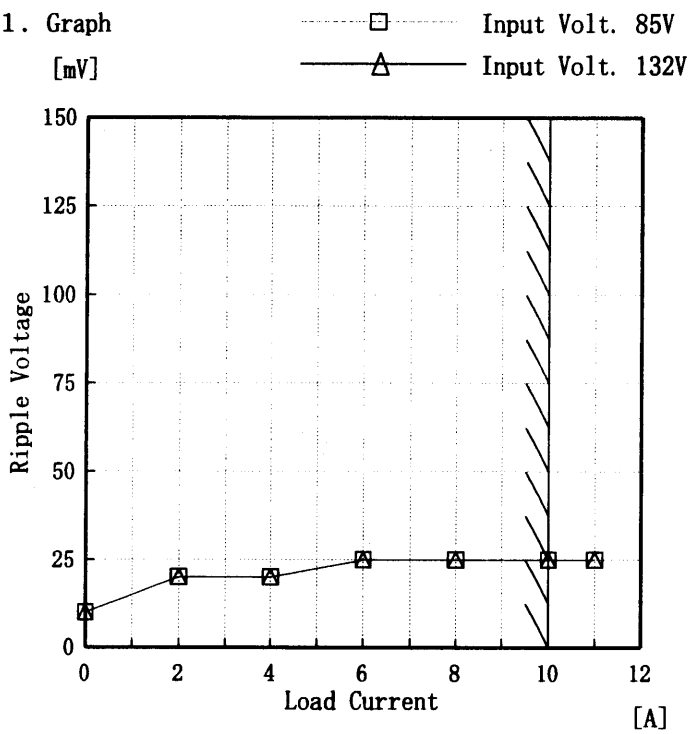
Model	LDA50F-5
Item	Ripple Voltage(by Load Current) リップル電圧(負荷電流特性)
Object	+5.0V10A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Ripple Voltage is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

リップル電圧は、下図ｐ－ｐ値で示される。

(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
ｽｲｯﾁﾝｸﾞ周期

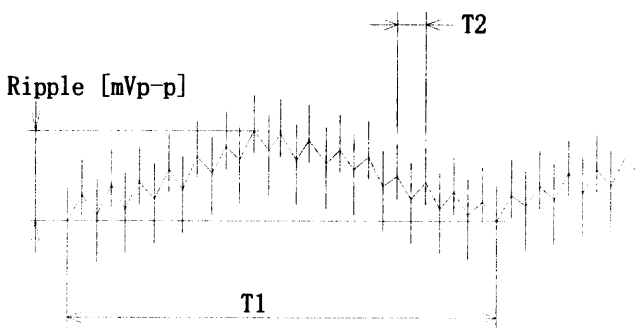


Fig. Complex Ripple Wave Form
図 リップル波形詳細図

2.Values

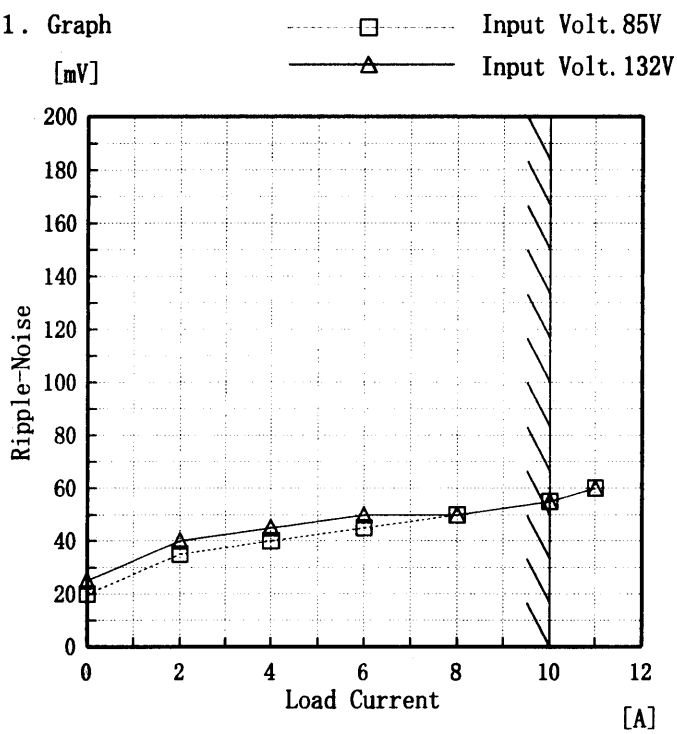
Load Current [A]	Input Volt. 85 [V] Ripple Output Volt. [mV]	Input Volt. 132 [V] Ripple Output Volt. [mV]
0.00	10	10
2.00	20	20
4.00	20	20
6.00	25	25
8.00	25	25
10.00	25	25
11.00	25	25
—	—	—
—	—	—
—	—	—
—	—	—

COSEL

Model	LDA50F-5
Item	Ripple-Noise リップルノイズ
Object	+5.0V10A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Ripple-Noise is shown as p-p in the figure below.
Note: Slanted line shows the range of the rated load current.

リップルノイズは、下図ｐ－ｐ値で示される。
(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
ｽｲｯﾁﾝｸﾞ周期

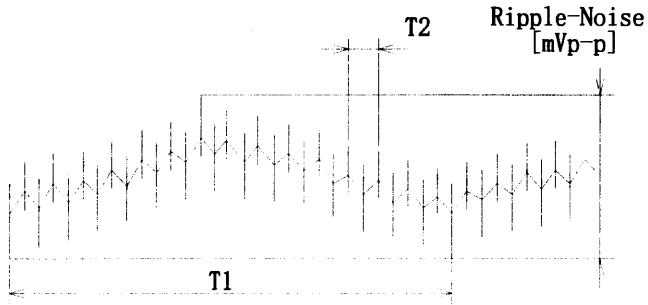


Fig. Complex Ripple Wave Form
図 リップル波形詳細図

2. Values

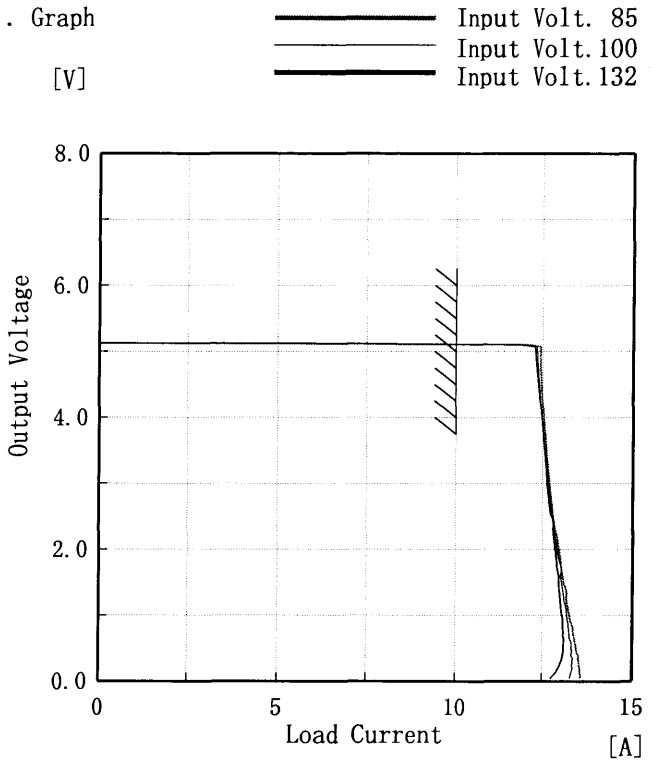
Load current [A]	Input Volt. 85 [V] Ripple-Noise [mV]	Input Volt. 132 [V] Ripple-Noise [mV]
0.00	20	25
2.00	35	40
4.00	40	45
6.00	45	50
8.00	50	50
10.00	55	55
11.00	60	60
—	—	—
—	—	—
—	—	—
—	—	—

COSEL

Model	LDA50F-5
Item	Overcurrent Protection 過電流保護
Object	+5.0V10A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Output Voltage [V]	Load Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
5.00	12.43	12.33	12.27
4.75	12.44	12.36	12.31
4.50	12.46	12.38	12.35
4.00	12.53	12.48	12.46
3.50	12.61	12.57	12.55
3.00	12.70	12.67	12.63
2.50	12.81	12.78	12.75
2.00	12.97	12.91	12.86
1.50	13.09	13.03	12.96
1.00	13.25	13.19	13.04
0.50	13.45	13.32	13.10
0.00	13.55	13.28	12.73

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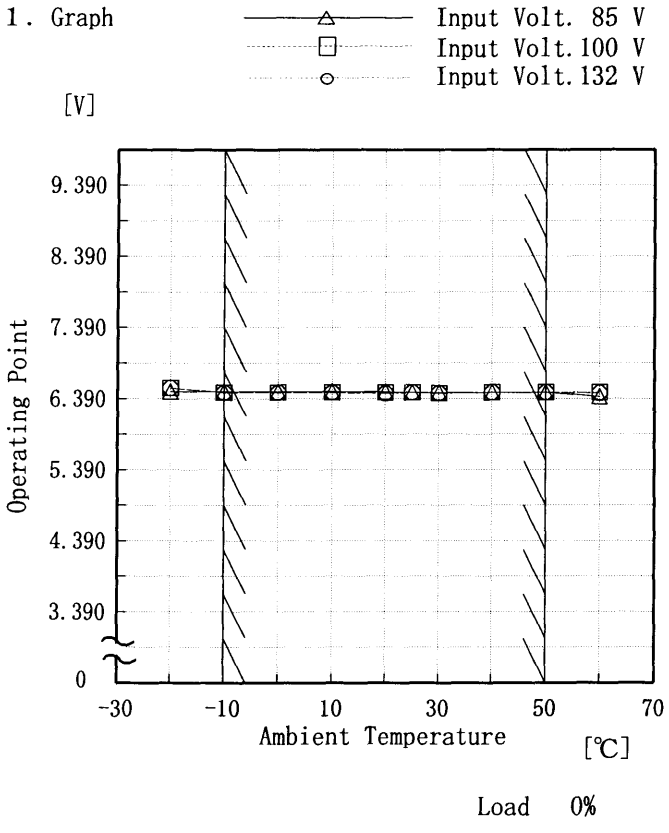
Model	LDA50F-5
Item	Overvoltage Protection 過電圧保護
Object	+5.0V10A

Testing Circuitry Figure A

1. Graph



Load 0%

Note: Slanted line shows the range of the rated ambient temperature.

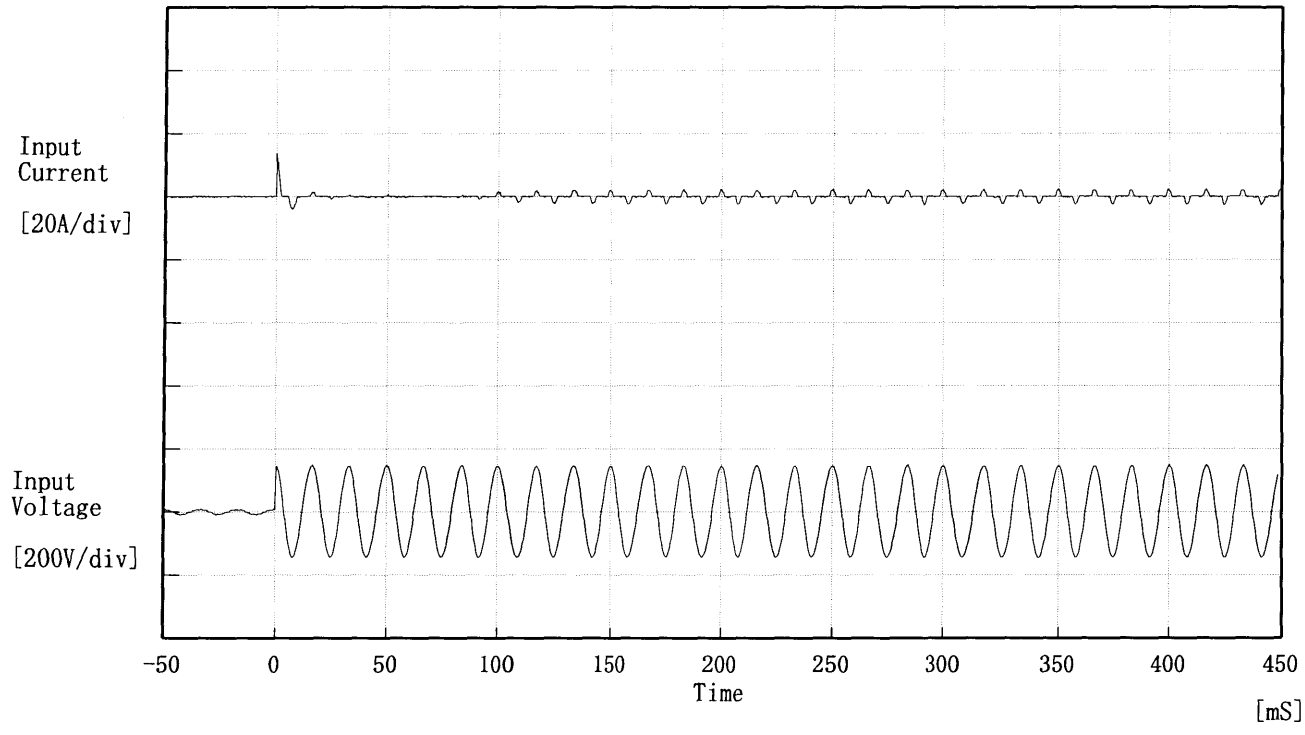
(注)斜線は定格周囲温度範囲を示す。

2. Values

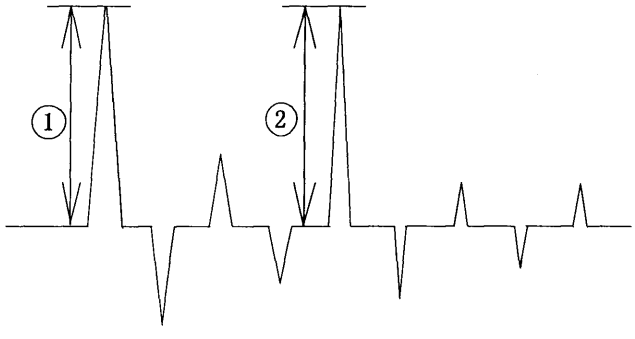
Ambient Temperature [℃]	Operating Point [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
-20	6.49	6.54	6.54
-10	6.49	6.48	6.48
0	6.49	6.48	6.48
10	6.49	6.48	6.48
20	6.49	6.48	6.47
25	6.48	6.48	6.48
30	6.48	6.47	6.47
40	6.48	6.48	6.48
50	6.48	6.48	6.48
60	6.42	6.48	6.47
—	—	—	—

COSEL

Model	LDA50F-5		
Item	Inrush Current 突入電流	Temperature	25℃
		Testing Circuitry	Figure A
Object	――――――――		



Input Voltage 100 V
Frequency 60 Hz
Load 100 %
Inrush Current
① 13.70 [A]
② 2.70 [A]



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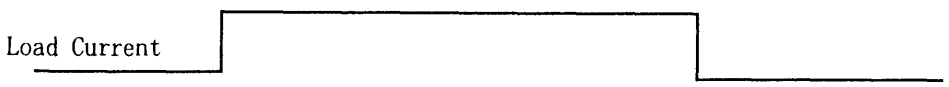
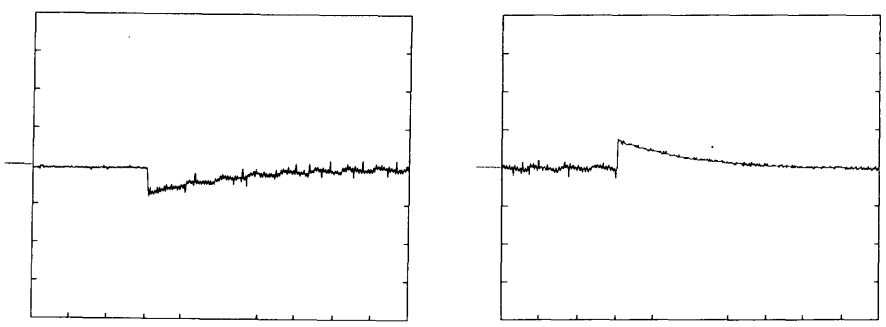
Model	LDA50F-5	
Item	Dynamic Load Responce 動的負荷変動	Temperature 25℃ Testing Circuitry Figure A
Object	+5.0V10A	

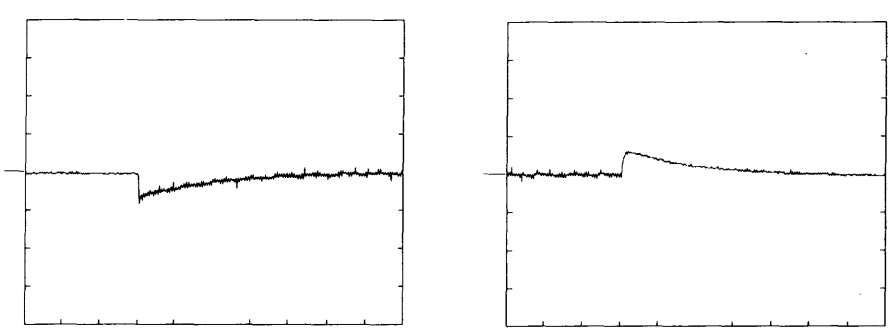
Input Volt. 100 V
Cycle 1000 mS



Load 0% ⟷
Load 100 %



Load 0% ⟷
Load 50 %



100 mV/div

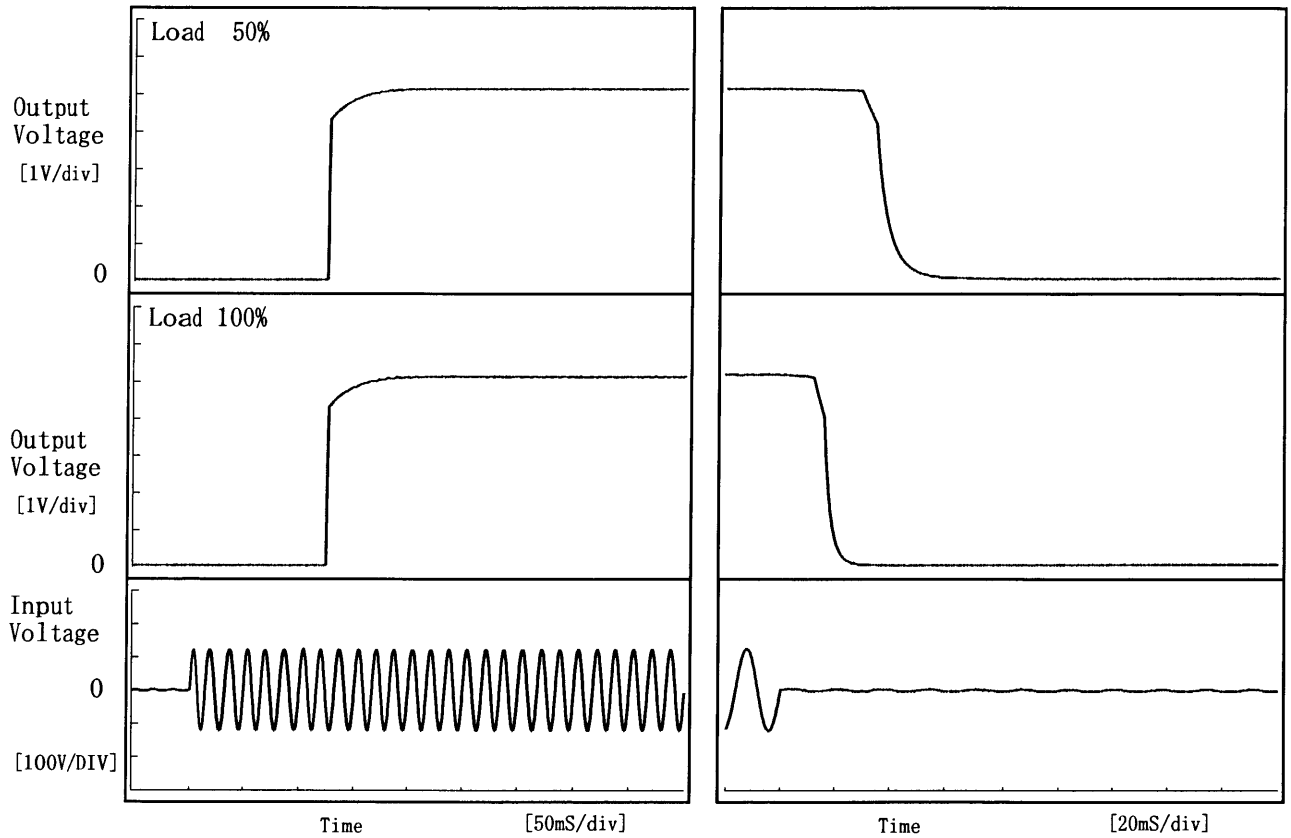
10 mS/div

COSEL

Model	LDA50F-5		
Item	Rise and Fall Time 立上り、立下り時間	Temperature Testing Circuitry	25℃ Figure A
Object	+5.0V10A		

1. Graph

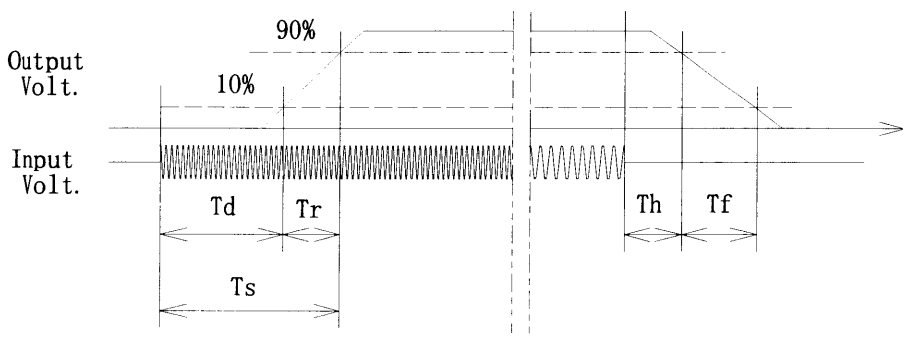
Input Volt. 85 V



2. Values

[mS]

Load \ Time	T d	T r	T s	T h	T f
50 %	125.5	7.5	133.0	32.7	12.4
100 %	125.3	8.3	133.5	14.4	6.9

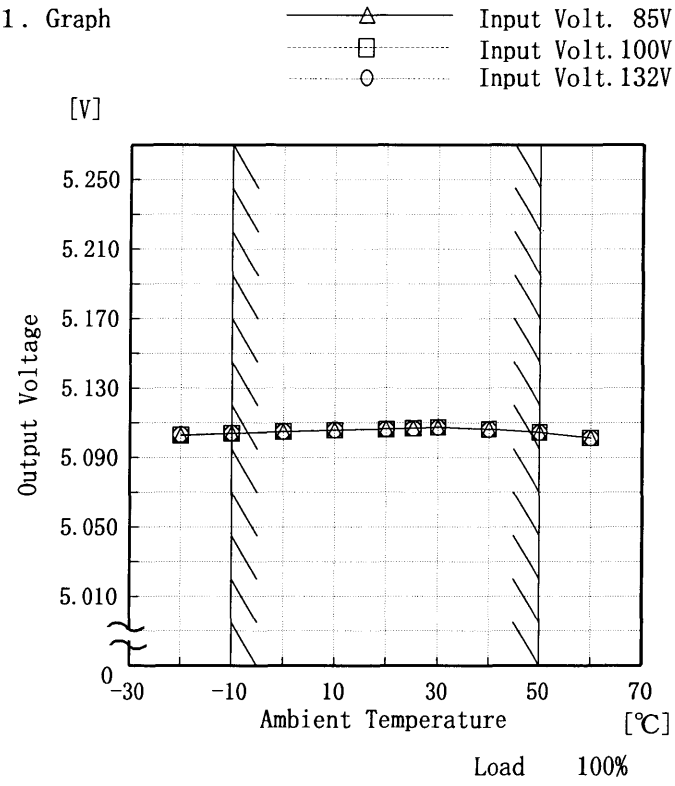


COSEL

Model	LDA50F-5
Item	Ambient Temperature Drift 周囲温度変動
Object	+5.0V10A

Testing Circuitry Figure A

1. Graph



Load 100%

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

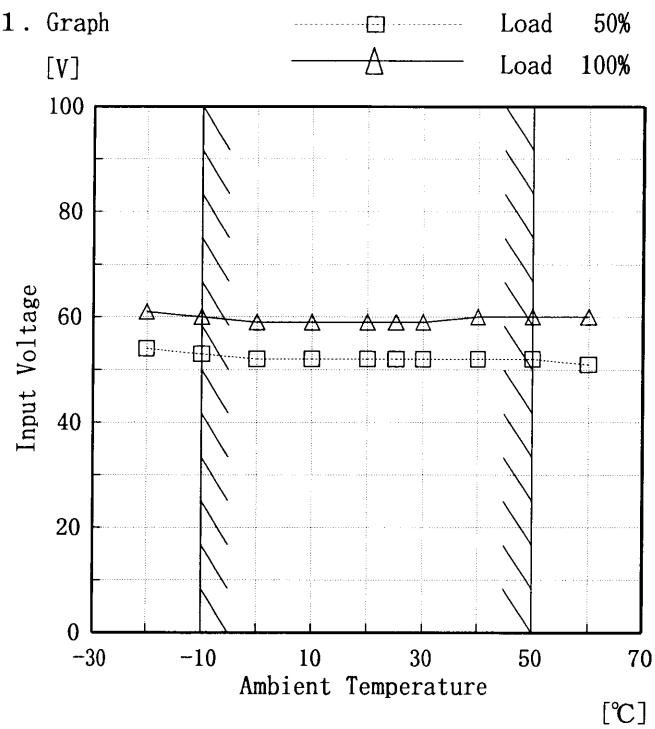
Ambient Temperature [℃]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
-20	5.103	5.103	5.103
-10	5.104	5.104	5.104
0	5.105	5.105	5.105
10	5.106	5.106	5.106
20	5.106	5.106	5.107
25	5.107	5.107	5.107
30	5.107	5.107	5.107
40	5.106	5.106	5.106
50	5.104	5.104	5.104
60	5.101	5.101	5.101
—	—	—	—

COSEL

Model	LDA50F-5
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+5.0V10A

Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

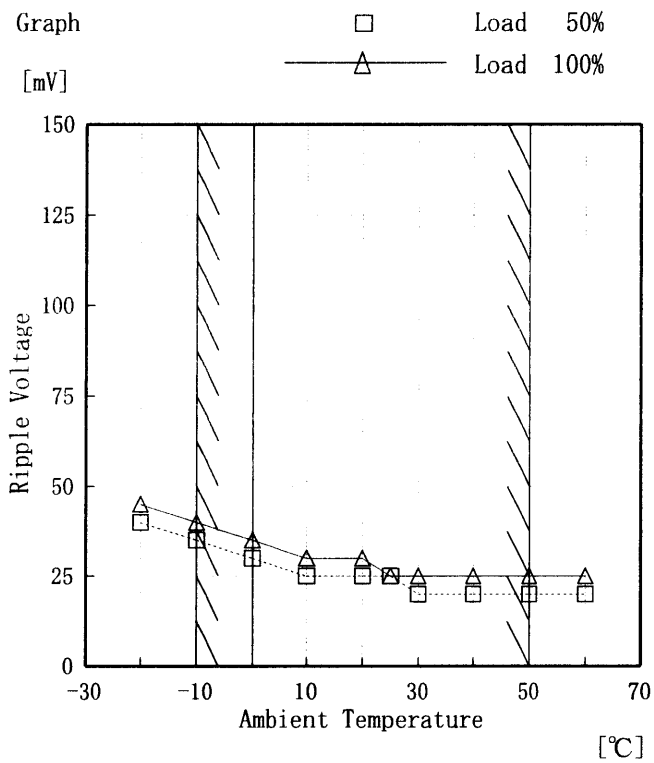
Ambient Temperature [℃]	Input Voltage [V]	
	Load 50%	Load 100%
-20	54	61
-10	53	60
0	52	59
10	52	59
20	52	59
25	52	59
30	52	59
40	52	60
50	52	60
60	51	60
—	—	—

COSEL

Model	LDA50F-5
Item	Ripple Voltage (by Ambient Temp.) リップル電圧（周囲温度特性）
Object	+5.0V10A

Testing Circuitry Figure A

1. Graph



Input Volt. 100 V

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

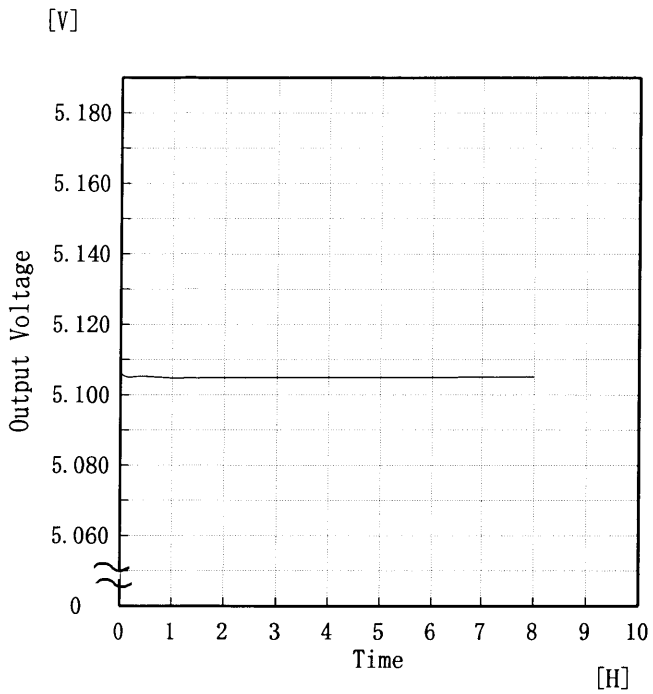
Ambient Temp. [℃]	Load 50% Ripple Output Volt. [mV]	Load 100% Ripple Output Volt. [mV]
-20	40	45
-10	35	40
0	30	35
10	25	30
20	25	30
25	25	25
30	20	25
40	20	25
50	20	25
60	20	25
—	—	—

COSEL

Model	LDA50F-5
Item	Time Lapse Drift 経時ドリフト
Object	+5.0V10A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Input Volt. 100V
Load 100%

2. Values

Time since start [H]	Output Voltage [V]
0.0	5.107
0.5	5.105
1.0	5.105
2.0	5.105
3.0	5.105
4.0	5.105
5.0	5.105
6.0	5.105
7.0	5.105
8.0	5.105

COSEL

Model	LDA50F-5	
Item	Output Voltage Accuracy 定電圧精度	Testing Circuitry Figure A
Object	+5.0V10A	

Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature -10～50 ℃
Input Voltage : 85～132 V
Load Current : 0～10 A

* Output Voltage Accuracy = ±(Maximum of Output Voltage − Minimum of Output Voltage)／2

$$* \text{Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 -10～50 ℃
入力電圧 85～132 V
負荷電流 0～10 A

* 定電圧精度(変動値) = ±(出力電圧の最高値−出力電圧の最低値)／2

$$* \text{定電圧精度(変動率)} = \frac{\text{変動値}}{\text{定格出力電圧}} \times 100$$

Item	Temperature [℃]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy(Ration)[%]
Maximum Voltage	25	132	0	5.122	±8	±0.2
Minimum Voltage	50	132	10	5.107		

COSEL

Model	LDA50F-5	
Item	Condensation 結露特性	Testing Circuitry Figure A
Object	+5.0V10A	

1. Condensation test

Testing procedure is as follows.

① Keeping and cooling the unit in a tank at -10℃ for an hour with the input off.

② Taking it out of the tank and dewing itself in a room where the temperature is 25℃ and the humidity is 40%RH.

③ Testing electrical characteristics of the unit to confirm there be no fault.

1．結露特性試験

入力を切った状態で、恒温槽で－１０℃に冷却しておき、約１時間後に恒温槽から取り出し、室温２５℃、湿度４０%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

2．Values

Item	Data	Testing Conditions
Output Voltage [V]	5.107	Input Volt.: 100V, Load Current:10A
Line Regulation [mV]	2	Input Volt.: 85～132V, Load Current:10A
Load Regulation [mV]	15	Input Volt.: 100V, Load Current:0～10A

COSEL

Model	LDA50F-5		
Item	Leakage Current 漏洩電流	Temperature	25℃
		Testing Circuitry	Figure B
Object	————————		

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DENTORI	0.17	0.21	0.27
(B) IEC60950	0.17	0.21	0.27

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B) IEC60950	—	—	—

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力の両相について測定し、その大きい方を漏洩電流測定値とする。

COSEL

Model	LDA50F-5		
Item	Line Noise Tolerance 入力雑音耐量	Temperature Testing Circuitry	25℃ Figure C
Object	+5.0V10A		

1. Results

Pulse Width [nS]	MODE	No protection failure should occur 保護回路の誤動作がない	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation
1000	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation

2. Conditions

Input Voltage : 100 V
Pulse Voltage : 2000 V
Pulse Cycle : 10 mS
Pulse Input Duration : 1 min. or more
Load : 100 %

COSEL

Model	LDA50F-5		
Item	Conducted Emission 雑音端子電圧	Temperature Testing Circuitry	25℃ Figure D
Object	――――――――		

1. Graph

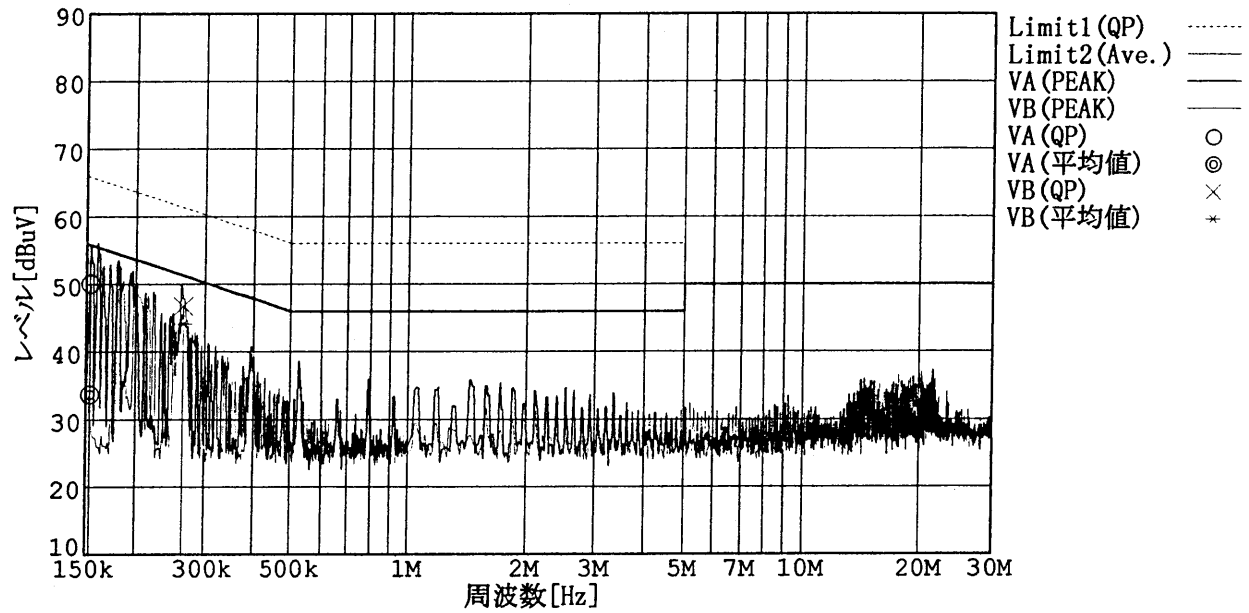
Remarks

Input Volt. 100 V (VCCI Class B)

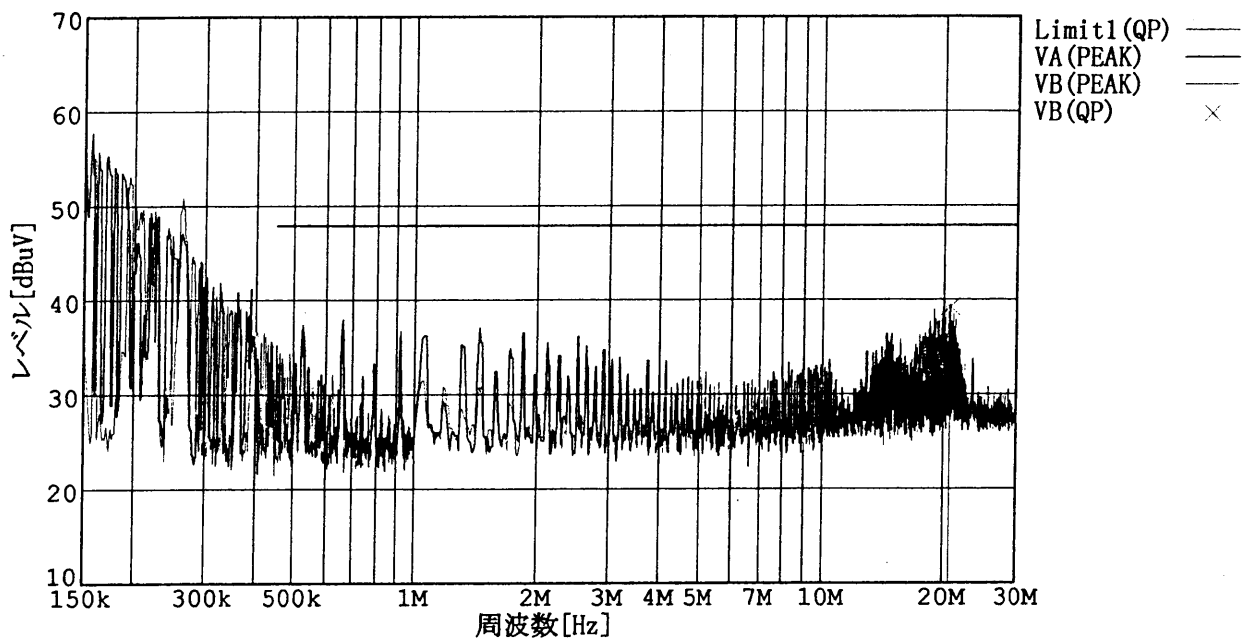
120 V (FCC Class B)

Load 100 %

規格1: [VCCI] Class B(QP)
規格2: [VCCI] Class B(平均値)



規格1: [FCC Part15] Class B



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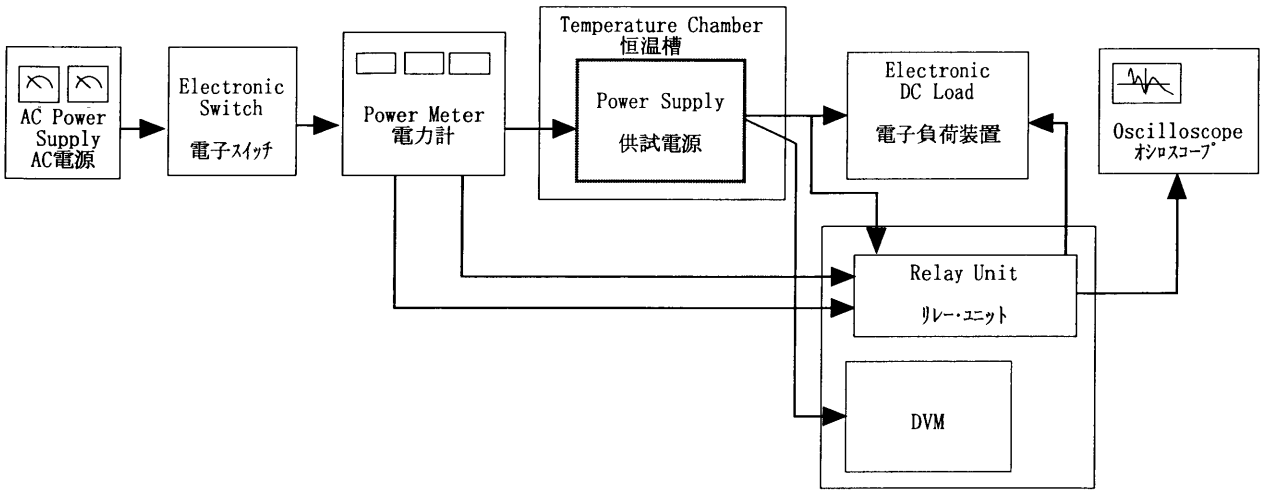

Figure A

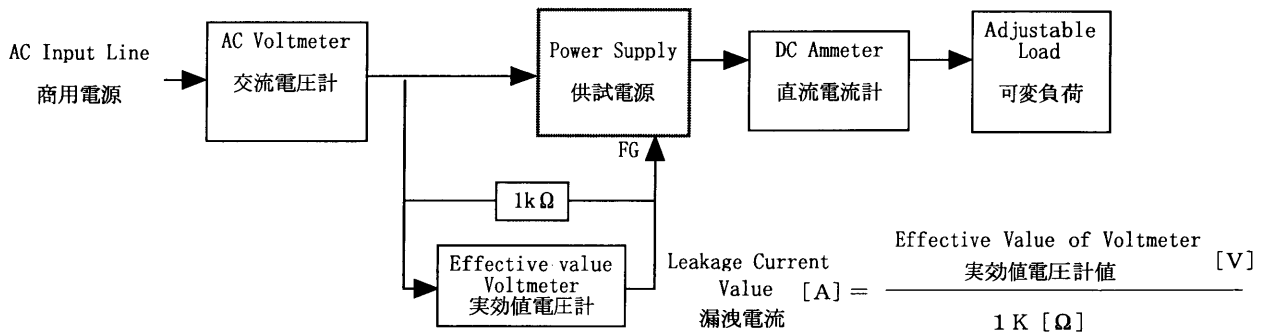

Figure B(DENTORI)

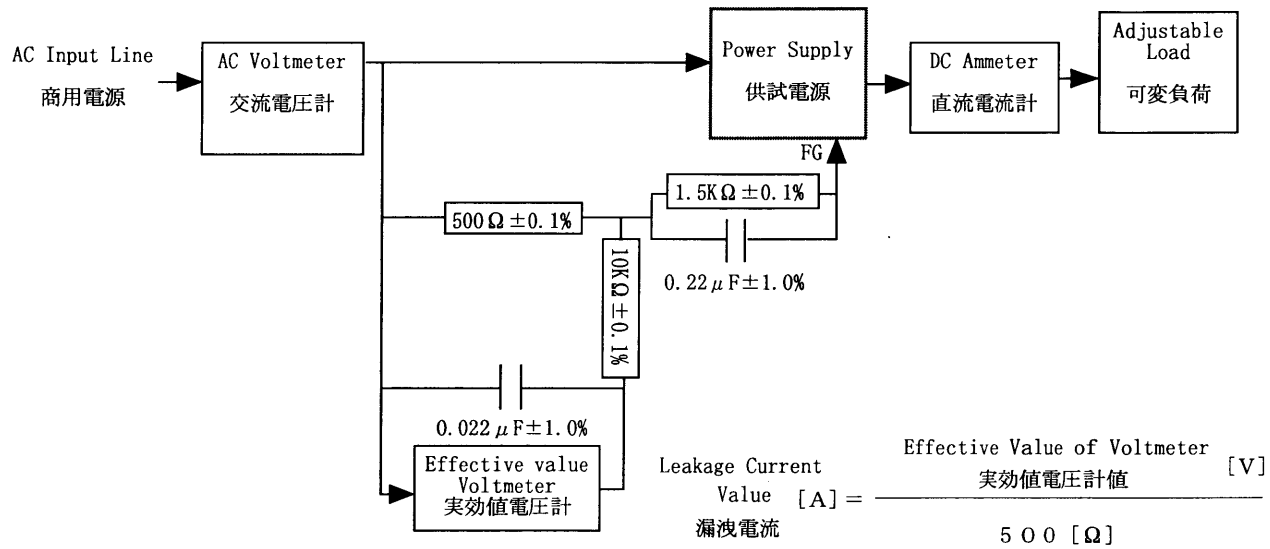

Figure B（ＩＥＣ６０９５０）

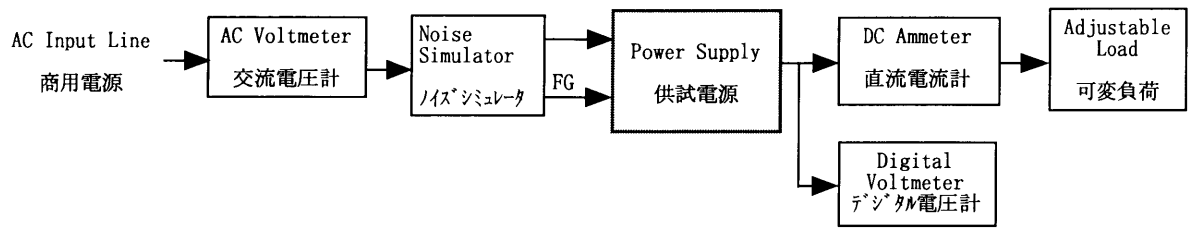

Figure C

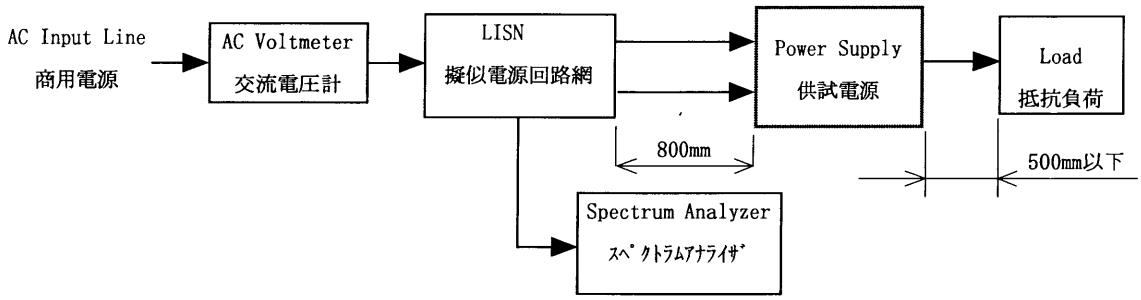

Figure D

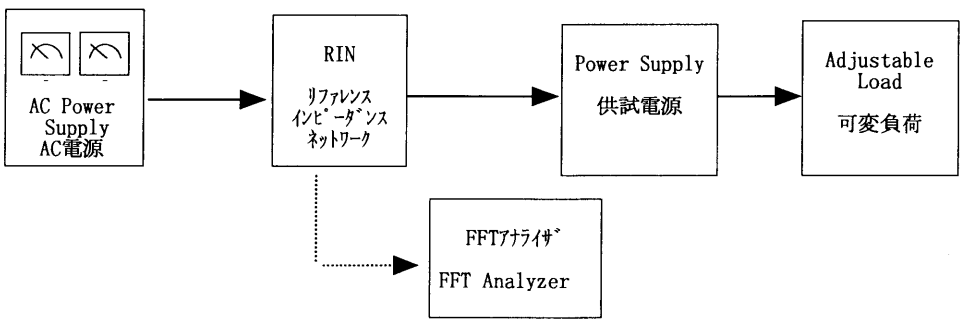

Figure E