



TEST DATA OF LDA50F-24

(100V INPUT)

Regulated DC Power Supply

Aug. 23, 1999

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Design Manager

Prepared by : T. Ashihara
Design Engineer

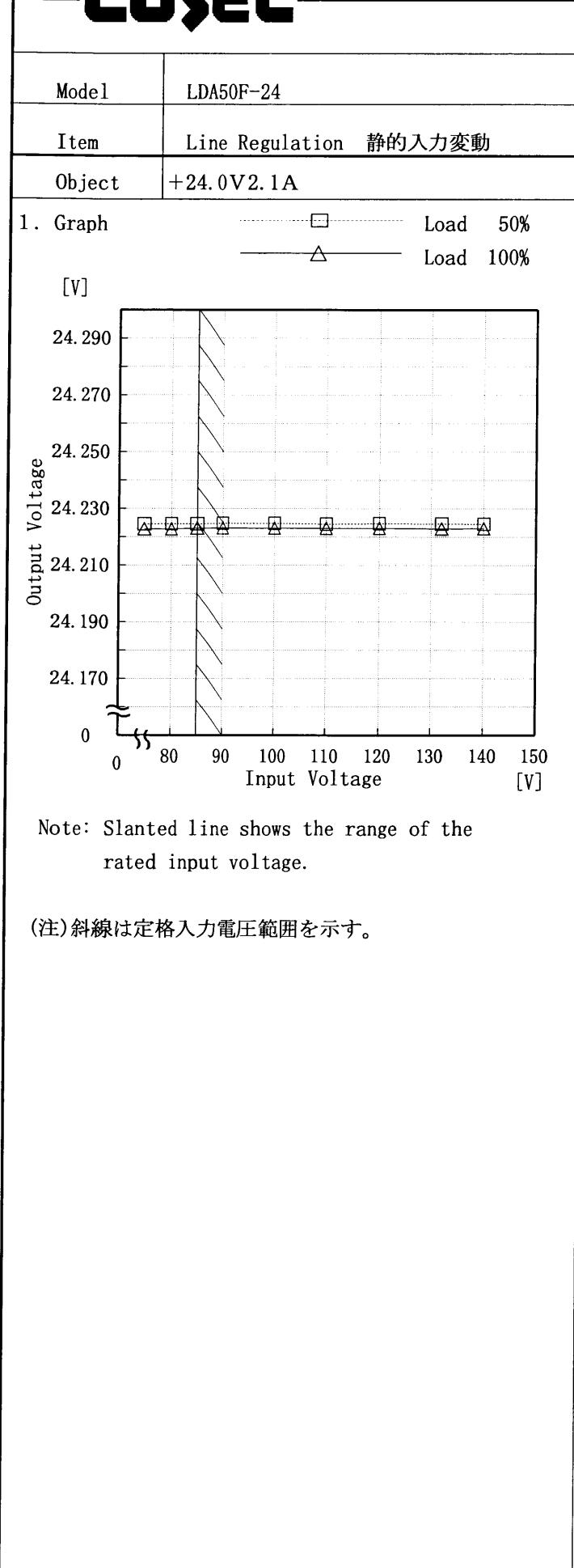
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COSEL CO., LTD.



C O N T E N T S

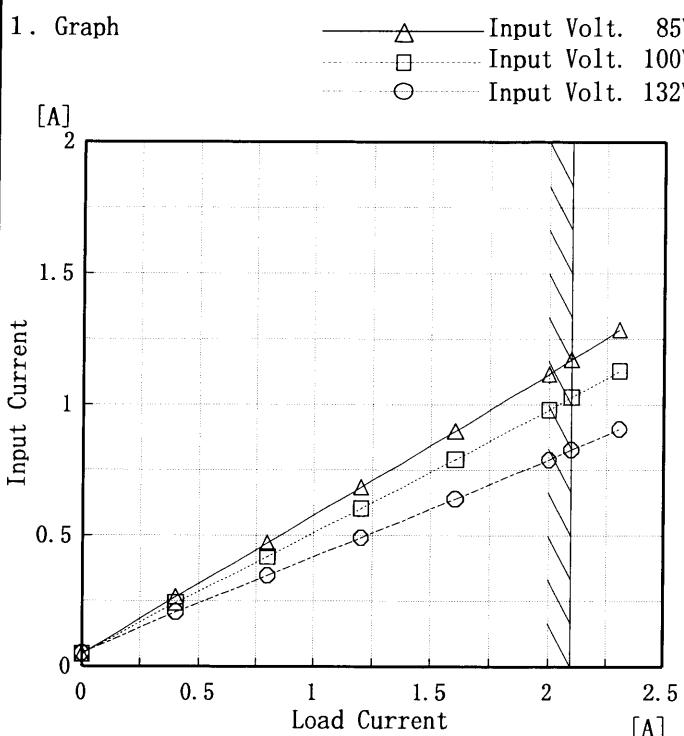
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Model	LDA50F-24
Item	Input Current (by Load Current) 入力電流 (負荷特性)
Output	—

Temperature 25°C
Testing Circuitry Figure A

2. Values

Load Current [A]	Input Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	0.046	0.048	0.050
0.40	0.266	0.243	0.208
0.80	0.471	0.419	0.347
1.20	0.684	0.604	0.492
1.60	0.900	0.791	0.639
2.00	1.116	0.980	0.789
2.10	1.172	1.028	0.828
2.31	1.287	1.129	0.907
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

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Model	LDA50F-24																																																									
Item	Input Power (by Load Current) 入力電力 (負荷特性)	Temperature 25°C	Testing Circuitry Figure A																																																							
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1. Graph	<p>—△— Input Volt. 85V —□— Input Volt. 100V —○— Input Volt. 132V</p> <table border="1"> <caption>Data points from Figure A</caption> <thead> <tr> <th>Load Current [A]</th> <th>Input Power [W] (85V)</th> <th>Input Power [W] (100V)</th> <th>Input Power [W] (132V)</th> </tr> </thead> <tbody> <tr><td>0.00</td><td>1.49</td><td>1.74</td><td>2.33</td></tr> <tr><td>0.40</td><td>12.73</td><td>12.97</td><td>13.66</td></tr> <tr><td>0.80</td><td>23.80</td><td>23.93</td><td>24.43</td></tr> <tr><td>1.20</td><td>34.96</td><td>34.98</td><td>35.29</td></tr> <tr><td>1.60</td><td>46.29</td><td>46.15</td><td>46.30</td></tr> <tr><td>2.00</td><td>57.91</td><td>57.54</td><td>57.40</td></tr> <tr><td>2.10</td><td>60.70</td><td>60.28</td><td>60.00</td></tr> <tr><td>2.31</td><td>67.05</td><td>66.49</td><td>66.10</td></tr> </tbody> </table>			Load Current [A]	Input Power [W] (85V)	Input Power [W] (100V)	Input Power [W] (132V)	0.00	1.49	1.74	2.33	0.40	12.73	12.97	13.66	0.80	23.80	23.93	24.43	1.20	34.96	34.98	35.29	1.60	46.29	46.15	46.30	2.00	57.91	57.54	57.40	2.10	60.70	60.28	60.00	2.31	67.05	66.49	66.10																			
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Note: Slanted line shows the range of the rated load current

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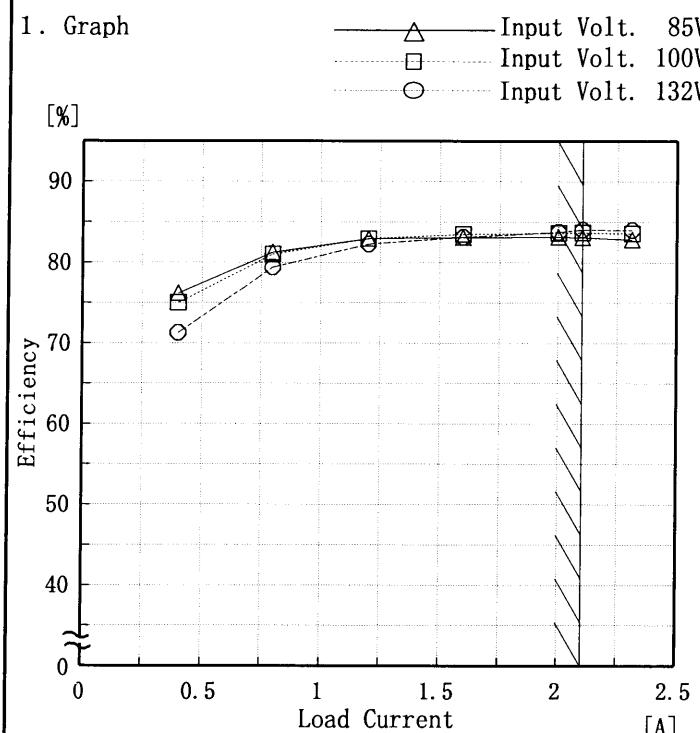
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Item	Efficiency 効率																																		
Object	_____																																		
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<p>Efficiency [%] vs Input Voltage [V]</p> <p>Legend: Load 50% (Squares), Load 100% (Triangles)</p> <table border="1"> <thead> <tr> <th>Input Voltage [V]</th> <th>Efficiency Load 50% [%]</th> <th>Efficiency Load 100% [%]</th> </tr> </thead> <tbody> <tr><td>80</td><td>82.5</td><td>82.5</td></tr> <tr><td>90</td><td>82.5</td><td>82.5</td></tr> <tr><td>100</td><td>82.0</td><td>83.0</td></tr> <tr><td>110</td><td>81.5</td><td>83.5</td></tr> <tr><td>120</td><td>81.0</td><td>83.5</td></tr> <tr><td>130</td><td>80.5</td><td>83.5</td></tr> <tr><td>140</td><td>80.0</td><td>83.5</td></tr> </tbody> </table>				Input Voltage [V]	Efficiency Load 50% [%]	Efficiency Load 100% [%]	80	82.5	82.5	90	82.5	82.5	100	82.0	83.0	110	81.5	83.5	120	81.0	83.5	130	80.5	83.5	140	80.0	83.5								
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Note: Slanted line shows the range of the rated input voltage.

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Model	LDA50F-24
Item	Efficiency (by Load Current) 効率(負荷特性)
Output	—



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

Temperature 25°C
Testing Circuitry Figure A

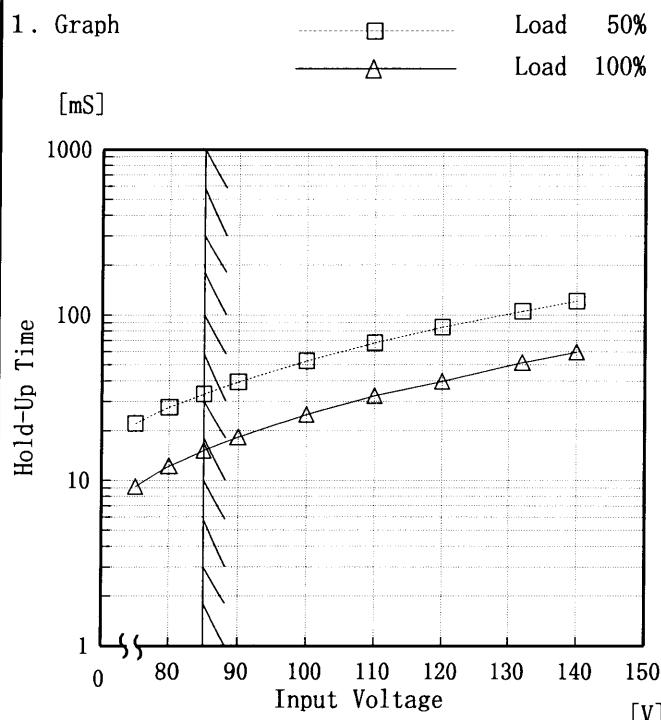
2. Values

Load Current [A]	Efficiency [%]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.40	76.2	75.0	71.3
0.80	81.3	81.0	79.4
1.20	82.9	83.0	82.3
1.60	83.1	83.5	83.2
2.00	83.2	83.7	83.8
2.10	83.1	83.7	84.0
2.31	82.8	83.6	84.0
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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Model	LDA50F-24
Item	Hold-Up Time 出力保持時間
Object	+24.0V 2.1A

Temperature 25°C
Testing Circuitry Figure A



2. Values

Input Voltage [V]	Hold-Up Time [mS]	
	Load 50%	Load 100%
75	22	9
80	28	12
85	33	15
90	39	18
100	53	25
110	68	33
120	85	40
132	106	52
140	122	60

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated input voltage.

出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。

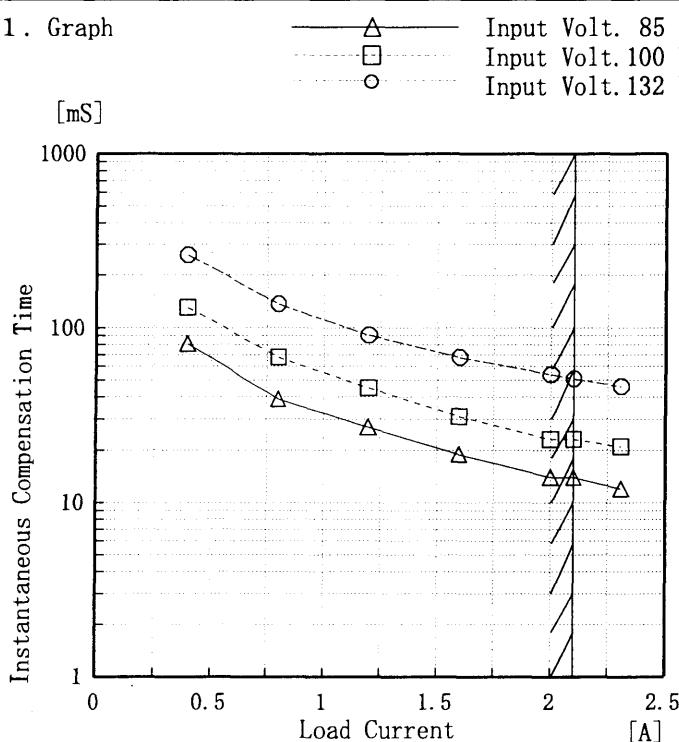
(注)斜線は定格入力電圧範囲を示す。

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Model	LDA50F-24
Item	Instantaneous Interruption Compensation 瞬時停電保障
Object	+24.0V 2.1A

Temperature 25°C
Testing Circuitry Figure A

1. Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated load current.

瞬時停電保障時間とは、出力電圧が定電圧精度の規格範囲を保持している瞬時停電時間をいう。

(注) 斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Time [mS]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	—	—	—
0.40	81	131	262
0.80	39	68	137
1.20	27	45	91
1.60	19	31	68
2.00	14	23	54
2.10	14	23	51
2.31	12	21	46
—	—	—	—
—	—	—	—
—	—	—	—

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Model	LDA50F-24	Temperature	25°C																																															
Item	Load Regulation 靜的負荷変動	Testing Circuitry	Figure A																																															
Object	+24.0V 2.1A																																																	
1. Graph		2. Values																																																
<p>Graph showing Output Voltage [V] vs Load Current [A]. The graph displays three curves for Input Voltages of 85V, 100V, and 132V. The output voltage remains constant at approximately 24.23V until the rated load current of 2.1A is reached, after which it drops sharply. A slanted line indicates the rated load current range.</p>		<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Output Voltage [V]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr> <td>0.00</td><td>24.228</td><td>24.227</td><td>24.227</td></tr> <tr> <td>0.40</td><td>24.226</td><td>24.226</td><td>24.226</td></tr> <tr> <td>0.80</td><td>24.226</td><td>24.225</td><td>24.225</td></tr> <tr> <td>1.20</td><td>24.225</td><td>24.225</td><td>24.225</td></tr> <tr> <td>1.60</td><td>24.224</td><td>24.225</td><td>24.224</td></tr> <tr> <td>2.00</td><td>24.224</td><td>24.224</td><td>24.224</td></tr> <tr> <td>2.10</td><td>24.224</td><td>24.224</td><td>24.223</td></tr> <tr> <td>2.31</td><td>24.223</td><td>24.223</td><td>24.223</td></tr> <tr> <td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr> <td>—</td><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>		Load Current [A]	Output Voltage [V]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0.00	24.228	24.227	24.227	0.40	24.226	24.226	24.226	0.80	24.226	24.225	24.225	1.20	24.225	24.225	24.225	1.60	24.224	24.225	24.224	2.00	24.224	24.224	24.224	2.10	24.224	24.224	24.223	2.31	24.223	24.223	24.223	—	—	—	—	—	—	—	—
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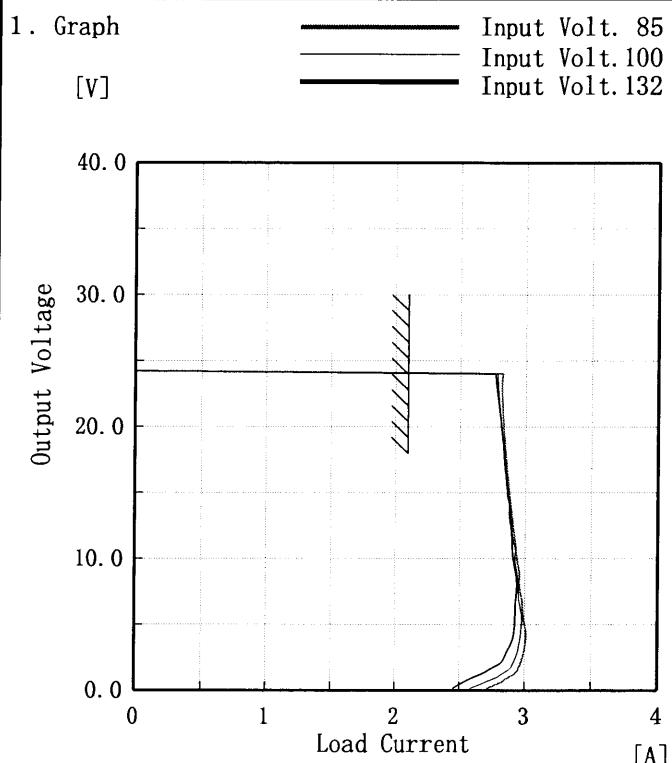
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1. Graph	<p style="text-align: center;">□ Input Volt. 85V [mV] △ Input Volt. 132V</p>	2. Values																																						
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<p>Ripple Voltage is shown as p-p in the figure below.</p> <p>Note: Slanted line shows the range of the rated load current.</p> <p>リップル電圧は、下図 p – p 値で示される。 (注)斜線は定格負荷電流範囲を示す。</p>		<p>T1: Due to AC Input Line 入力商用周期</p> <p>T2: Due to Switching スイッチング周期</p>																																						
<p>Fig. Complex Ripple Wave Form 図 リップル波形詳細図</p>																																								

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Model	LDA50F-24	Temperature Testing Circuitry	25°C Figure A																																						
Item	Ripple-Noise リップルノイズ																																								
Object	+24.0V 2.1A																																								
1. Graph		2. Values																																							
<p>Graph showing Ripple-Noise [mV] vs Load Current [A]. The Y-axis ranges from 0 to 200 mV, and the X-axis ranges from 0 to 2.5 A. Two sets of data points are shown: Input Volt. 85V (squares) and Input Volt. 132V (triangles). Dashed lines connect the points. A solid diagonal line at approximately 1.5 A indicates the rated load current range.</p>		<table border="1"> <thead> <tr> <th rowspan="2">Load current [A]</th> <th>Input Volt. 85 [V]</th> <th>Input Volt. 132 [V]</th> </tr> <tr> <th>Ripple-Noise [mV]</th> <th>Ripple-Noise [mV]</th> </tr> </thead> <tbody> <tr><td>0.00</td><td>20</td><td>25</td></tr> <tr><td>0.50</td><td>40</td><td>50</td></tr> <tr><td>1.00</td><td>45</td><td>50</td></tr> <tr><td>1.50</td><td>45</td><td>50</td></tr> <tr><td>2.00</td><td>50</td><td>55</td></tr> <tr><td>2.10</td><td>50</td><td>55</td></tr> <tr><td>2.30</td><td>50</td><td>55</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>		Load current [A]	Input Volt. 85 [V]	Input Volt. 132 [V]	Ripple-Noise [mV]	Ripple-Noise [mV]	0.00	20	25	0.50	40	50	1.00	45	50	1.50	45	50	2.00	50	55	2.10	50	55	2.30	50	55	—	—	—	—	—	—	—	—	—	—	—	—
Load current [A]	Input Volt. 85 [V]	Input Volt. 132 [V]																																							
	Ripple-Noise [mV]	Ripple-Noise [mV]																																							
0.00	20	25																																							
0.50	40	50																																							
1.00	45	50																																							
1.50	45	50																																							
2.00	50	55																																							
2.10	50	55																																							
2.30	50	55																																							
—	—	—																																							
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—	—	—																																							
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<p>Ripple-Noise is shown as p-p in the figure below. Note: Slanted line shows the range of the rated load current.</p> <p>リップルノイズは、下図 p - p 値で示される。 (注)斜線は定格負荷電流範囲を示す。</p> <p>T1: Due to AC Input Line T2: Due to Switching</p> <p>Fig. Complex Ripple Wave Form 図 リップル波形詳細図</p>			<p>Detailed view of the complex ripple wave form. The Y-axis is labeled "Ripple-Noise [mVp-p]" and the X-axis is labeled "T1" and "T2". The waveform shows high-frequency noise superimposed on a lower-frequency periodic component.</p>																																						

COSSEL

Model	LDA50F-24
Item	Overcurrent Protection 過電流保護
Object	+24.0V 2.1A

Temperature 25°C
Testing Circuitry Figure A

2. Values

Output Voltage [V]	Load Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
24.00	2.82	2.77	2.76
22.80	2.81	2.78	2.77
21.60	2.81	2.79	2.78
19.20	2.83	2.81	2.82
16.80	2.85	2.84	2.84
14.40	2.88	2.87	2.87
12.00	2.91	2.90	2.89
9.60	2.94	2.92	2.91
7.20	2.97	2.95	2.93
4.80	3.00	2.98	2.92
2.40	2.98	2.93	2.83
0.00	2.70	2.58	2.45

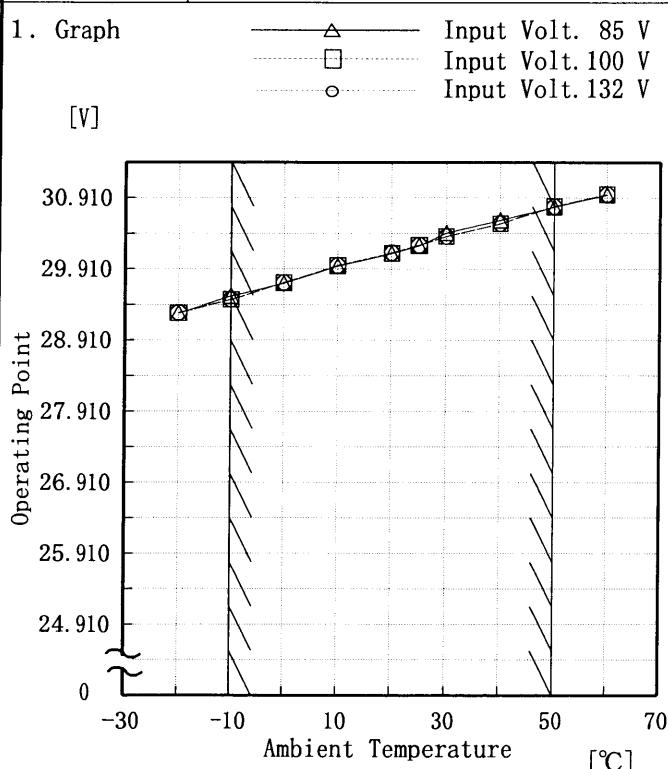
Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

COSSEL

Model	LDA50F-24
Item	Overvoltage Protection 過電圧保護
Object	+24.0V 2.1A

Testing Circuitry Figure A



2. Values

Ambient Temperature [°C]	Operating Point [V]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
-20	29.29	29.29	29.30
-10	29.53	29.48	29.48
0	29.71	29.72	29.72
10	29.95	29.96	29.96
20	30.13	30.13	30.14
25	30.24	30.25	30.25
30	30.42	30.37	30.37
40	30.60	30.55	30.55
50	30.78	30.79	30.79
60	30.95	30.96	30.96
—	—	—	—

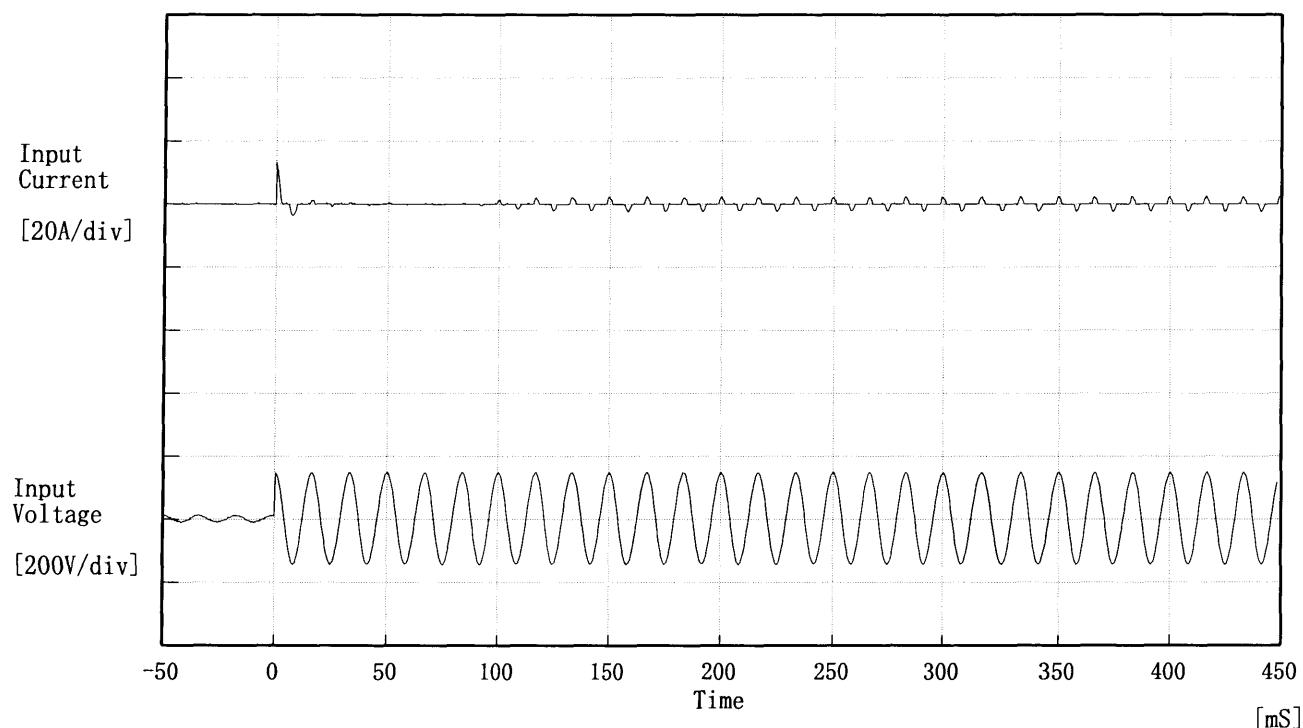
Load 0%

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

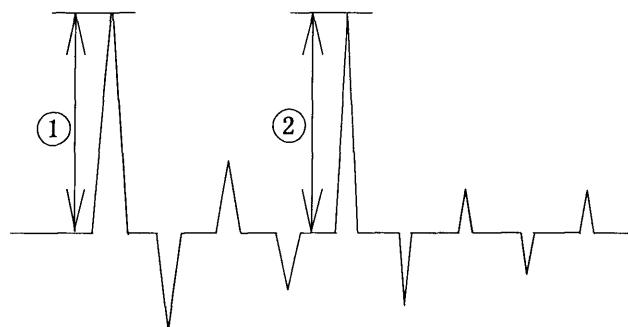
COSEL

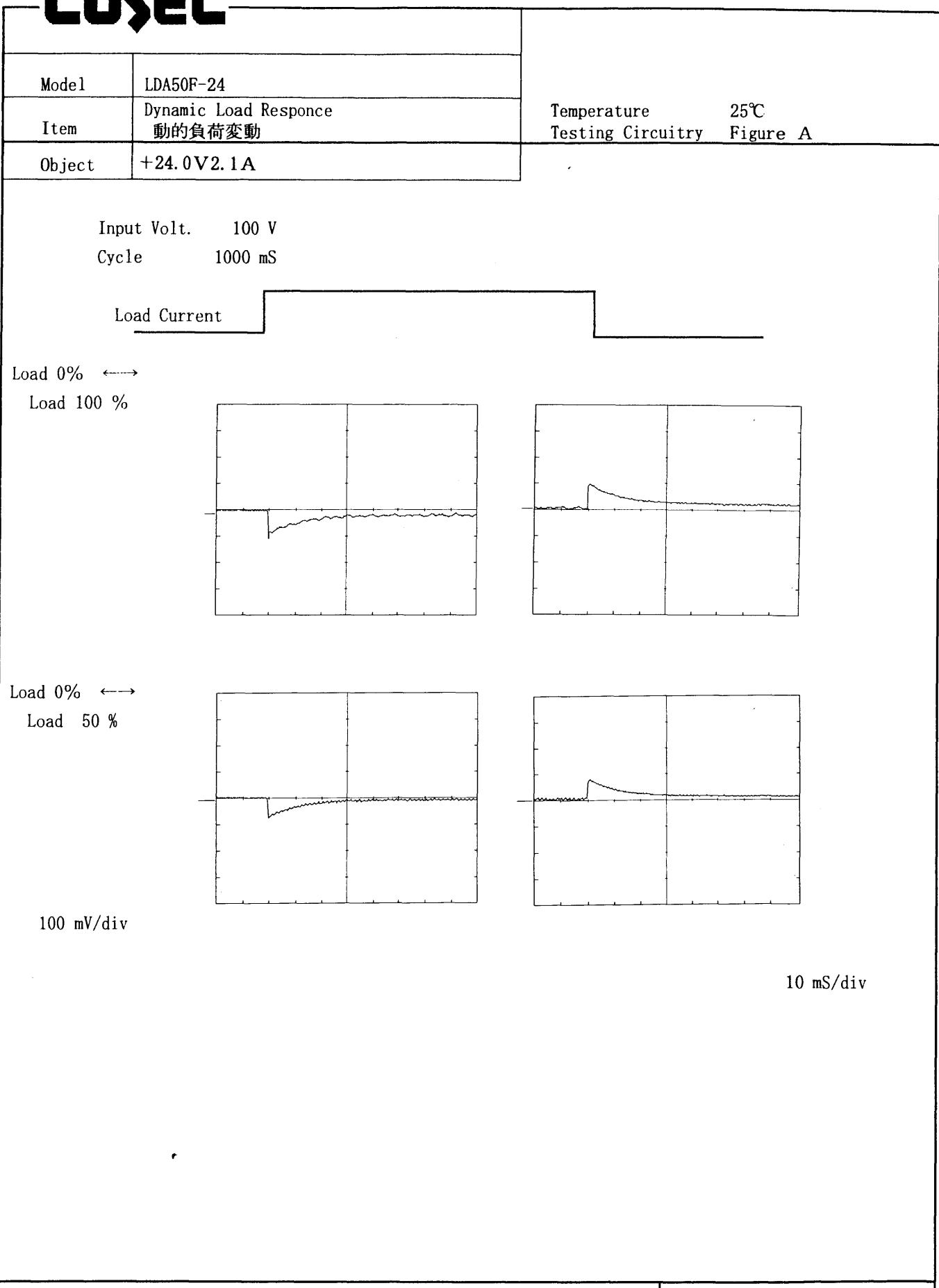
Model	LDA50F-24	Temperature	25°C
Item	Inrush Current 突入電流	Testing Circuitry	Figure A
Object	—		



Input Voltage 100 V
 Frequency 60 Hz
 Load 100 %
 Inrush Current

- ① 13.22 [A]
- ② 2.42 [A]



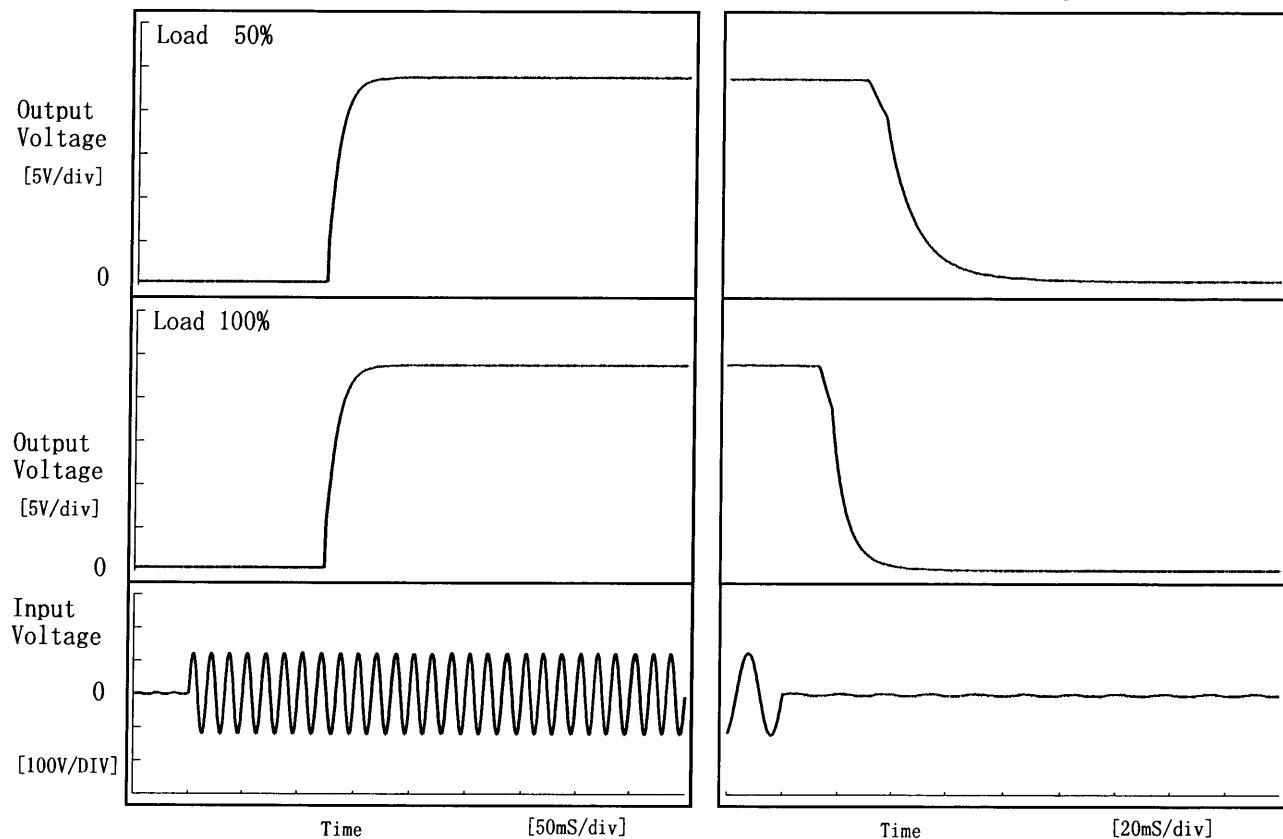
COSEL

COSEL

Model	LDA50F-24
Item	Rise and Fall Time 立ち上り、立下り時間
Object	+24.0 V 2.1 A

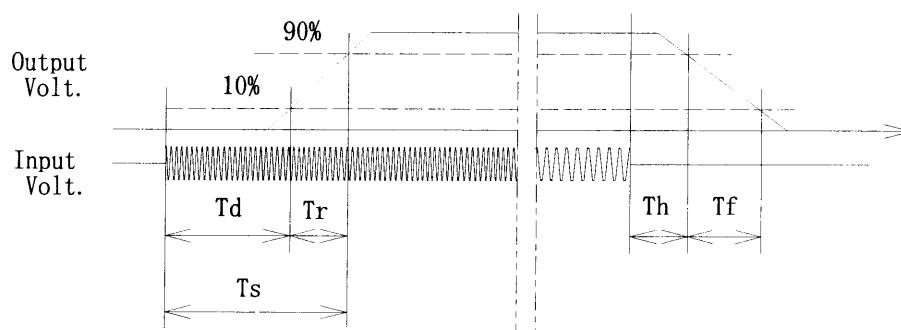
Temperature 25°C
Testing Circuitry Figure A

1. Graph

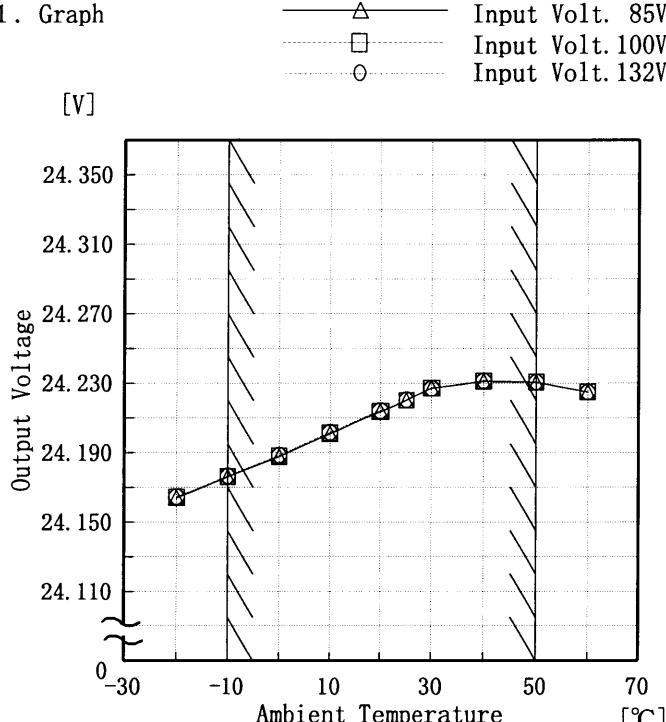


2. Values

Load	Time	T d	T r	T s	T h	T f
50 %		122.8	23.8	146.5	33.1	29.8
100 %		122.5	23.8	146.3	14.8	14.7

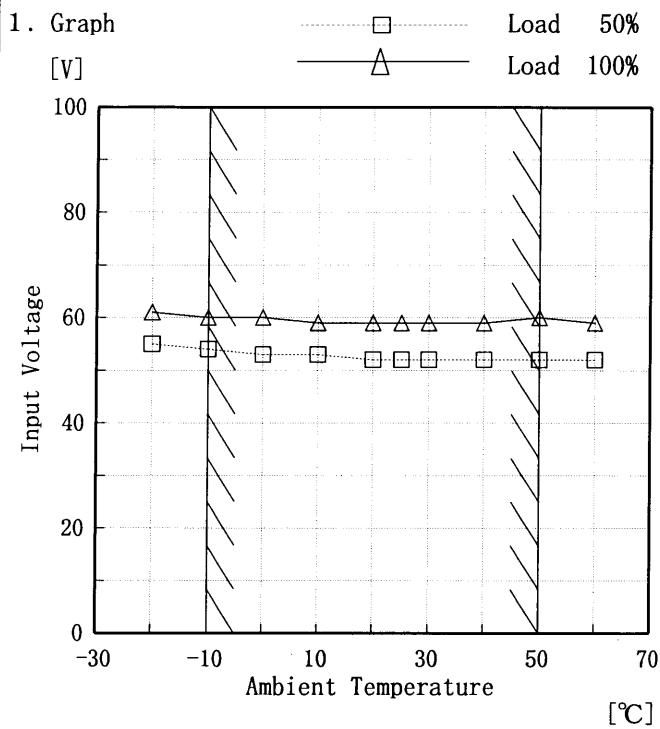


COSEL

Model LDA50F-24 Item Ambient Temperature Drift 周囲温度変動 Object +24.0V 2.1A	Testing Circuitry Figure A																																																			
	1. Graph	2. Values																																																		
	 <p>Output Voltage [V]</p> <p>Ambient Temperature [°C]</p> <p>Load 100%</p> <p>Note: Slanted line shows the range of the rated ambient temperature.</p> <p>(注) 斜線は定格周囲温度範囲を示す。</p>	<table border="1"> <thead> <tr> <th rowspan="2">Ambient Temperature [°C]</th> <th colspan="3">Output Voltage [V]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>-20</td><td>24.164</td><td>24.164</td><td>24.165</td></tr> <tr><td>-10</td><td>24.176</td><td>24.176</td><td>24.177</td></tr> <tr><td>0</td><td>24.188</td><td>24.188</td><td>24.188</td></tr> <tr><td>10</td><td>24.201</td><td>24.201</td><td>24.202</td></tr> <tr><td>20</td><td>24.213</td><td>24.214</td><td>24.214</td></tr> <tr><td>25</td><td>24.220</td><td>24.220</td><td>24.220</td></tr> <tr><td>30</td><td>24.227</td><td>24.227</td><td>24.227</td></tr> <tr><td>40</td><td>24.231</td><td>24.231</td><td>24.231</td></tr> <tr><td>50</td><td>24.230</td><td>24.231</td><td>24.231</td></tr> <tr><td>60</td><td>24.225</td><td>24.225</td><td>24.225</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>	Ambient Temperature [°C]	Output Voltage [V]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	-20	24.164	24.164	24.165	-10	24.176	24.176	24.177	0	24.188	24.188	24.188	10	24.201	24.201	24.202	20	24.213	24.214	24.214	25	24.220	24.220	24.220	30	24.227	24.227	24.227	40	24.231	24.231	24.231	50	24.230	24.231	24.231	60	24.225	24.225	24.225	—	—	—
Ambient Temperature [°C]	Output Voltage [V]																																																			
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]																																																	
-20	24.164	24.164	24.165																																																	
-10	24.176	24.176	24.177																																																	
0	24.188	24.188	24.188																																																	
10	24.201	24.201	24.202																																																	
20	24.213	24.214	24.214																																																	
25	24.220	24.220	24.220																																																	
30	24.227	24.227	24.227																																																	
40	24.231	24.231	24.231																																																	
50	24.230	24.231	24.231																																																	
60	24.225	24.225	24.225																																																	
—	—	—	—																																																	

COSEL

Model	LDA50F-24
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+24.0V 2.1A



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

Testing Circuitry Figure A

2. Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	55	61
-10	54	60
0	53	60
10	53	59
20	52	59
25	52	59
30	52	59
40	52	59
50	52	60
60	52	59
—	—	—

COSEL

Model	LDA50F-24																																								
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)	Testing Circuitry Figure A																																							
Object	+24.0V 2.1A																																								
1. Graph																																									
		□ Load 50%	—△— Load 100%																																						
		[mV]																																							
		Ripple Voltage [mV]	Ambient Temperature [°C]																																						
		Input Volt. 100 V																																							
Note: Slanted line shows the range of the rated ambient temperature.																																									
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<table border="1"> <thead> <tr> <th rowspan="2">Ambient Temp. [°C]</th> <th>Load 50%</th> <th>Load 100%</th> </tr> <tr> <th>Ripple Output Volt. [mV]</th> <th>Ripple Output Volt. [mV]</th> </tr> </thead> <tbody> <tr><td>-20</td><td>65</td><td>70</td></tr> <tr><td>-10</td><td>50</td><td>50</td></tr> <tr><td>0</td><td>35</td><td>40</td></tr> <tr><td>10</td><td>35</td><td>35</td></tr> <tr><td>20</td><td>30</td><td>30</td></tr> <tr><td>25</td><td>30</td><td>30</td></tr> <tr><td>30</td><td>30</td><td>30</td></tr> <tr><td>40</td><td>25</td><td>25</td></tr> <tr><td>50</td><td>25</td><td>25</td></tr> <tr><td>60</td><td>20</td><td>20</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>				Ambient Temp. [°C]	Load 50%	Load 100%	Ripple Output Volt. [mV]	Ripple Output Volt. [mV]	-20	65	70	-10	50	50	0	35	40	10	35	35	20	30	30	25	30	30	30	30	30	40	25	25	50	25	25	60	20	20	—	—	—
Ambient Temp. [°C]	Load 50%	Load 100%																																							
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50	25	25																																							
60	20	20																																							
—	—	—																																							

COSEL

Model	LDA50F-24	Temperature Testing Circuitry	25°C Figure A																						
Item	Time Lapse Drift 経時ドリフト																								
Object	+24.0V 2.1A																								
1. Graph			2. Values																						
<p>[V]</p> <p>Output Voltage [V]</p> <p>Time [H]</p> <p>Input Volt. 100V Load 100%</p>																									
			<table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>24.207</td></tr> <tr><td>0.5</td><td>24.210</td></tr> <tr><td>1.0</td><td>24.210</td></tr> <tr><td>2.0</td><td>24.211</td></tr> <tr><td>3.0</td><td>24.211</td></tr> <tr><td>4.0</td><td>24.212</td></tr> <tr><td>5.0</td><td>24.212</td></tr> <tr><td>6.0</td><td>24.212</td></tr> <tr><td>7.0</td><td>24.212</td></tr> <tr><td>8.0</td><td>24.212</td></tr> </tbody> </table>	Time since start [H]	Output Voltage [V]	0.0	24.207	0.5	24.210	1.0	24.210	2.0	24.211	3.0	24.211	4.0	24.212	5.0	24.212	6.0	24.212	7.0	24.212	8.0	24.212
Time since start [H]	Output Voltage [V]																								
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6.0	24.212																								
7.0	24.212																								
8.0	24.212																								



Model	LDA50F-24	Testing Circuitry Figure A
Item	Output Voltage Accuracy 定電圧精度	
Object	+24.0V 2.1A	

Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature -10~50 °C

Input Voltage : 85~132 V

Load Current : 0~2.1 A

* Output Voltage Accuracy = ±(Maximum of Output Voltage - Minimum of Output Voltage) / 2

$$* \text{ Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 -10~50 °C

入力電圧 85~132 V

負荷電流 0~2.1 A

* 定電圧精度(変動値) = ±(出力電圧の最高値 - 出力電圧の最低値) / 2

$$* \text{ 定電圧精度(変動率)} = \frac{\text{変動値}}{\text{定格出力電圧}} \times 100$$

Item	Temperature [°C]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy(Ration) [%]
Maximum Voltage	50	132	0.0	24.231	±34	±0.2
Minimum Voltage	-10	85	2.1	24.164		



Model	LDA50F-24	Testing Circuitry Figure A
Item	Condensation 結露特性	
Object	+24.0V 2.1A	

1. Condensation test

Testing procedure is as follows.

- ① Keeping and cooling the unit in a tank at -10°C for an hour with the input off.
- ② Taking it out of the tank and dewing itself in a room where the temperature is 25°C and the humidity is 40%RH.
- ③ Testing electrical characteristics of the unit to confirm there be no fault.

1. 結露特性試験

入力を切った状態で、恒温槽で-10°Cに冷却しておき、約1時間後に恒温槽から取り出し、室温25°C、湿度40%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

2. Values

Item	Data	Testing Conditions
Output Voltage [V]	24.222	Input Volt.: 100V, Load Current: 2.1A
Line Regulation [mV]	3	Input Volt.: 85~132V, Load Current: 2.1A
Load Regulation [mV]	7	Input Volt.: 100V, Load Current: 0~2.1A



Model	LDA50F-24	Temperature Testing Circuitry	25°C Figure B
Item	Leakage Current 漏洩電流		
Object	_____		

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DENTORI	0.17	0.20	0.27
(B) IEC60950	0.18	0.20	0.28

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力の両相について測定し、その大きい方を漏洩電流測定値とする。

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B) IEC60950	—	—	—



Model	LDA50F-24	Temperature Testing Circuitry	25°C Figure C
Item	Line Noise Tolerance 入力雑音耐量		
Object	+24.0V 2.1A		

1. Results

Pulse Width [nS]	MODE	No protection failure should occur 保護回路の誤動作がない	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation
1000	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation

2. Conditions

Input Voltage : 100 V
 Pulse Voltage : 2000 V
 Pulse Cycle : 10 mS
 Pulse Input Duration : 1 min. or more
 Load : 100 %

COSEL

Model	LDA50F-24	Temperature Testing Circuitry 25°C Figure D
Item	Conducted Emission 雜音端子電圧	
Object	_____	

1. Graph

Remarks

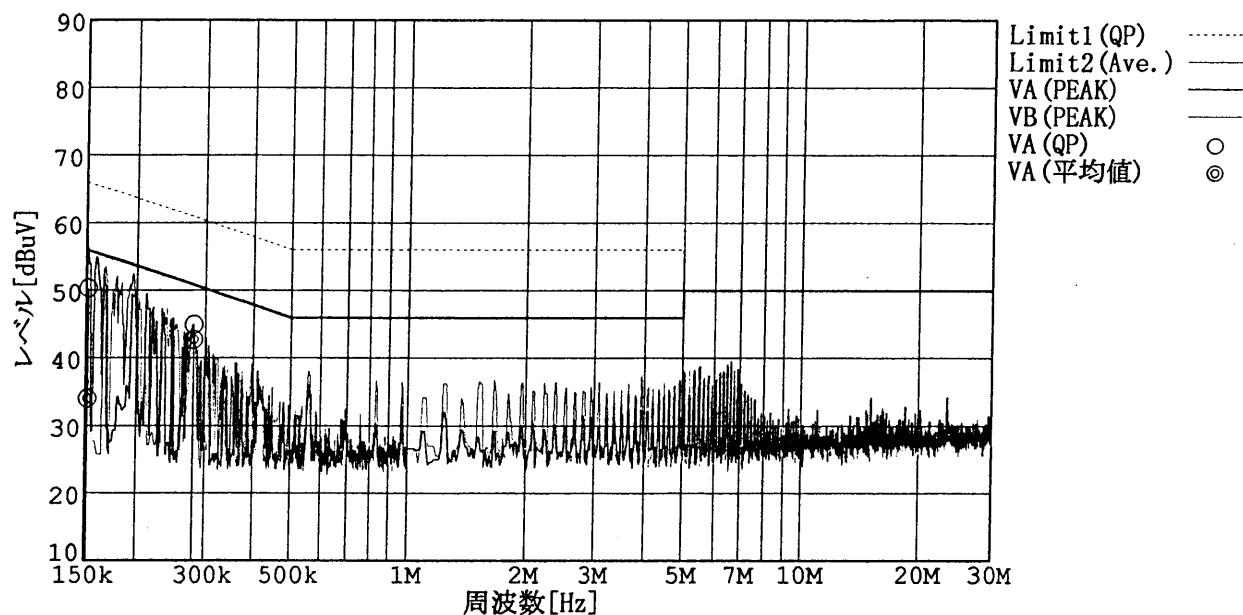
Input Volt. 100 V (VCCI Class B)

120 V (FCC Class B)

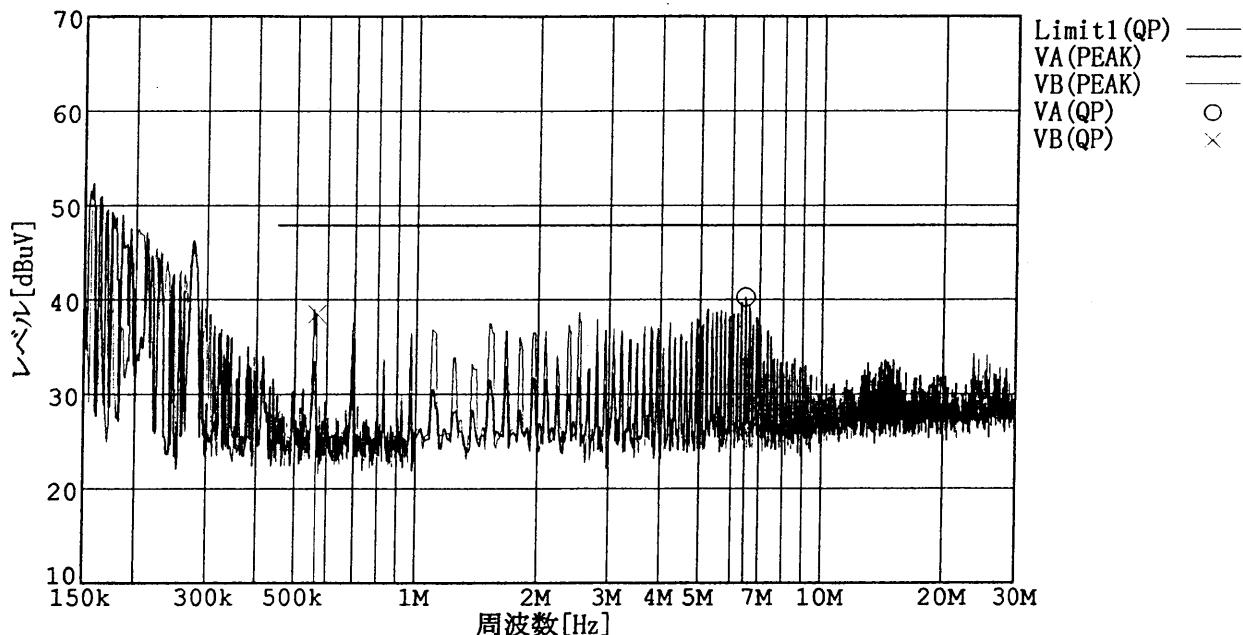
Load 100 %

規格 1 : [VCCI] Class B(QP)

規格 2 : [VCCI] Class B(平均値)



規格 1 : [FCC Part15] Class B



COSEL

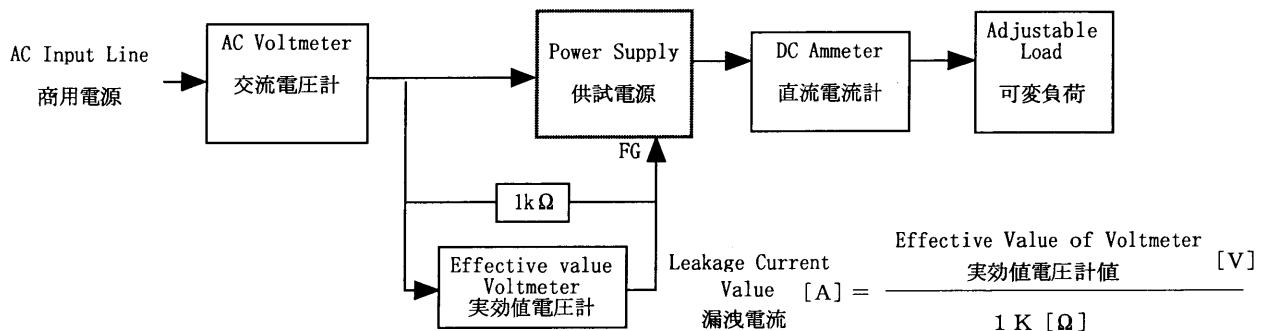
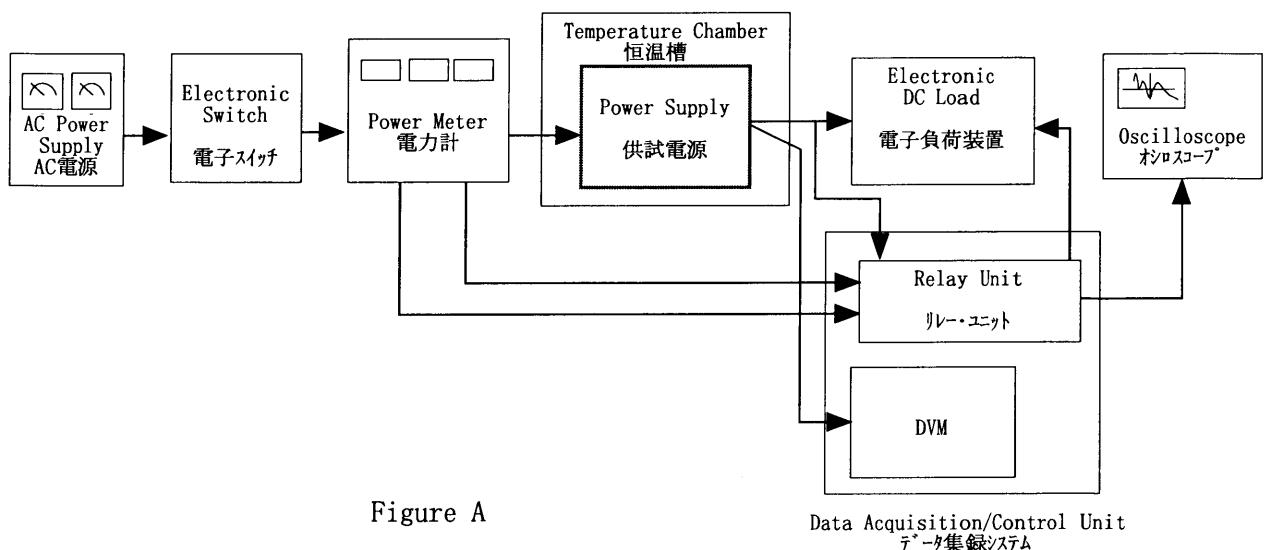


Figure B (DENTORI)

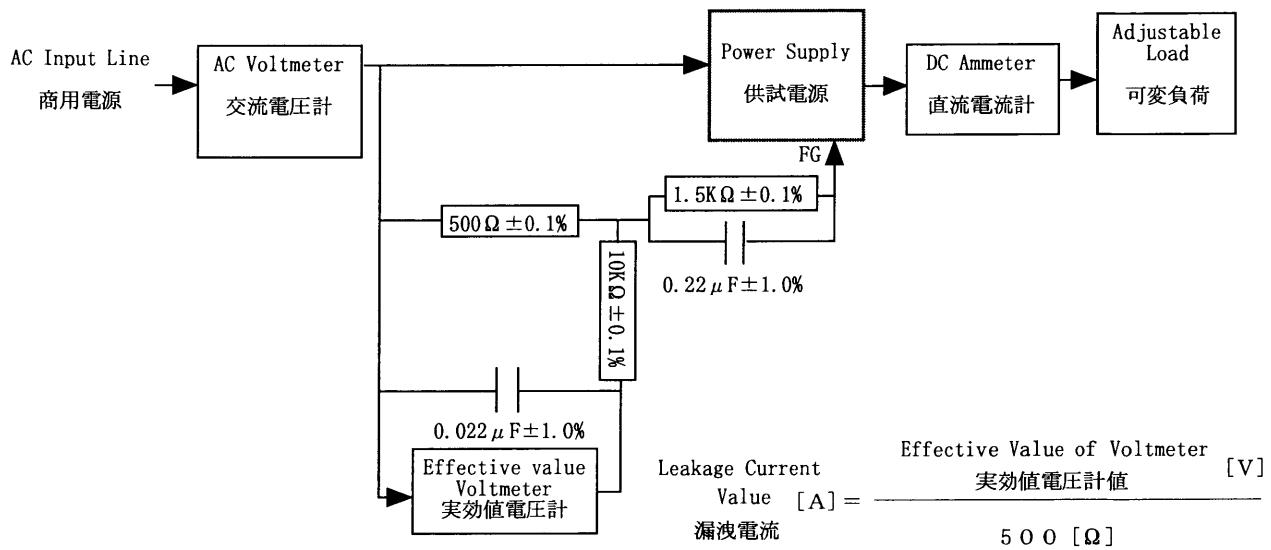


Figure B (IEC 60950)

COSEL

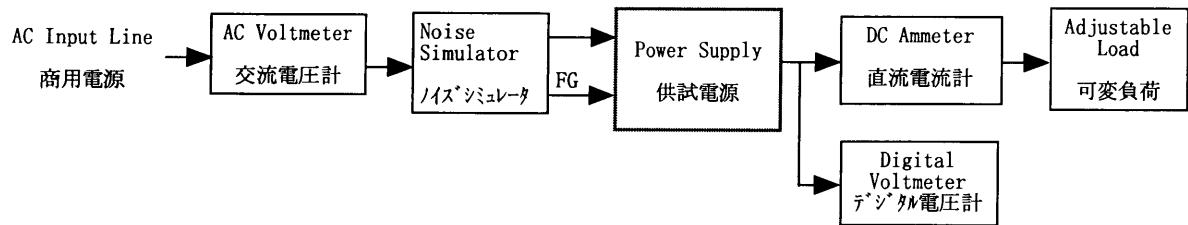


Figure C

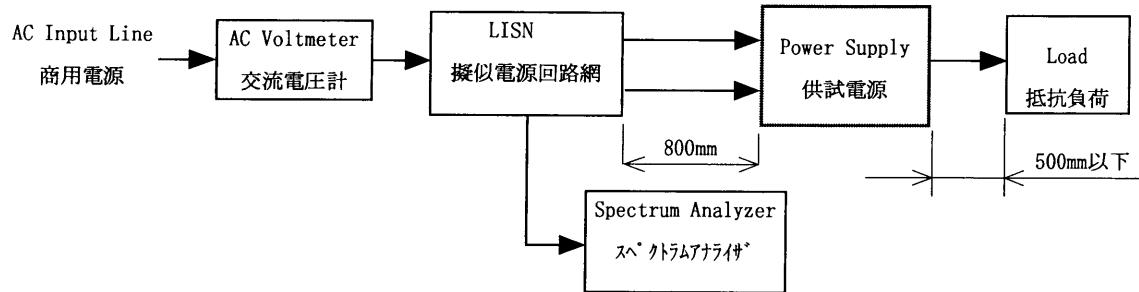


Figure D

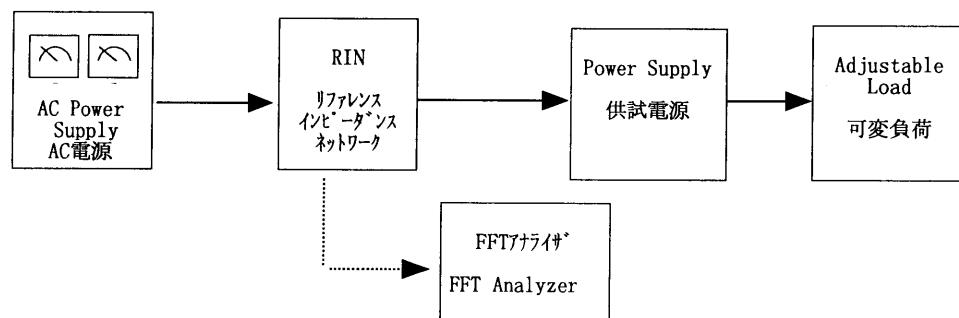


Figure E