



TEST DATA OF LDA30F-15

(100V INPUT)

Regulated DC Power Supply

Date : Aug. 17. 1999

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Design Manager

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コーセル株式会社
COSEL CO., LTD.



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Model LDA30F-15

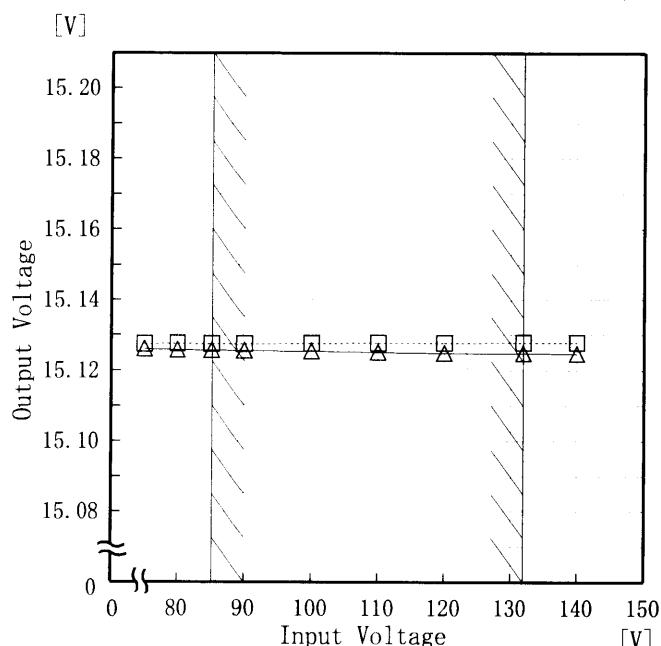
Item Line Regulation 静的入力変動

Object +15.0V2A

1. Graph

Load 50%

Load 100%



Note: Slanted line shows the range of the rated input voltage.

(注) 斜線は定格入力電圧範囲を示す。

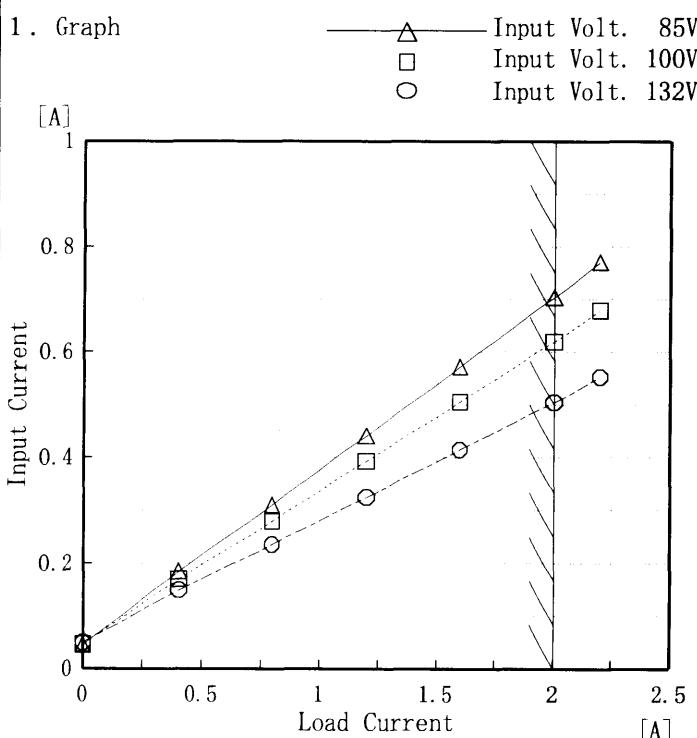
Temperature
Testing Circuitry25°C
Figure A

2. Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
75	15.127	15.126
80	15.128	15.126
85	15.127	15.126
90	15.127	15.126
100	15.128	15.125
110	15.128	15.125
120	15.128	15.125
132	15.128	15.125
140	15.128	15.125

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Model	LDA30F-15
Item	Input Current (by Load Current) 入力電流 (負荷特性)
Output	—

Temperature 25°C
Testing Circuitry Figure A

2. Values

Load Current [A]	Input Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	0.044	0.046	0.048
0.4	0.185	0.170	0.149
0.8	0.310	0.279	0.235
1.2	0.441	0.393	0.325
1.6	0.572	0.506	0.414
2.0	0.703	0.620	0.505
2.2	0.771	0.679	0.553
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

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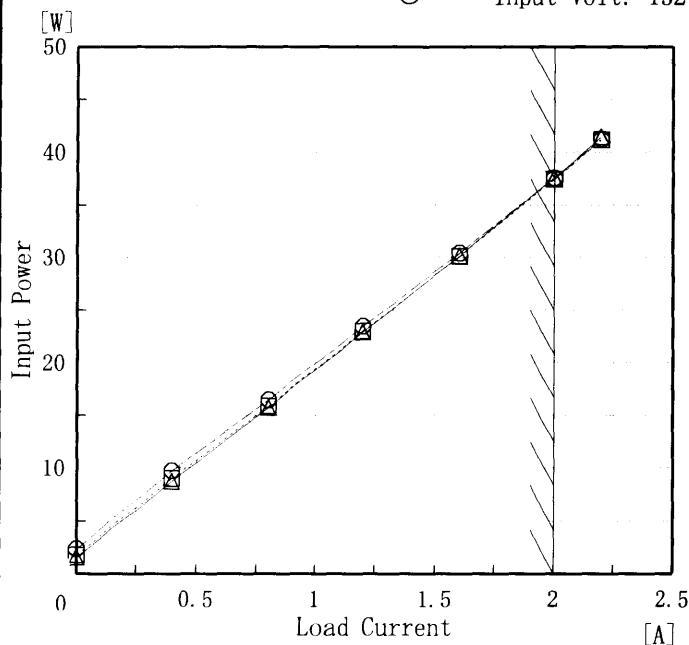
Model LDA30F-15

Item Input Power (by Load Current)
入力電力 (負荷特性)

Output _____

1. Graph

—△— Input Volt. 85V
 —□— Input Volt. 100V
 —○— Input Volt. 132V



Note: Slanted line shows the range of the rated load current

(注) 斜線は定格負荷電流範囲を示す。

Temperature 25°C
Testing Circuitry Figure A

2. Values

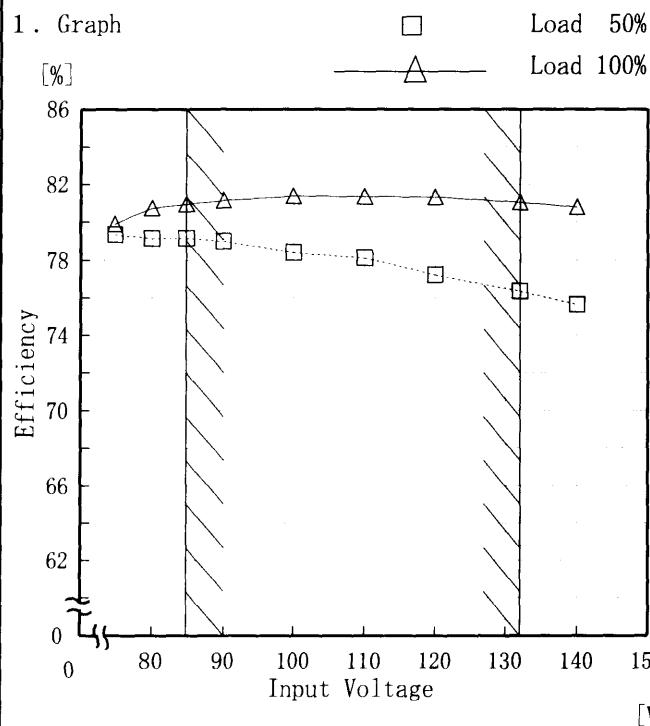
Load Current [A]	Input Power [W]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	1.52	1.76	2.36
0.4	8.71	9.01	9.71
0.8	15.71	15.87	16.46
1.2	22.90	23.00	23.45
1.6	30.16	30.09	30.42
2.0	37.55	37.41	37.52
2.2	41.42	41.17	41.21
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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Model LDA30F-15

Item Efficiency 効率

Object



Temperature 25°C
Testing Circuitry Figure A

2. Values

Input Voltage [V]	Efficiency [%]	
	Load 50%	Load 100%
75	79.3	79.9
80	79.1	80.7
85	79.1	81.0
90	79.0	81.2
100	78.4	81.4
110	78.1	81.4
120	77.2	81.3
132	76.4	81.0
140	75.6	80.8

Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

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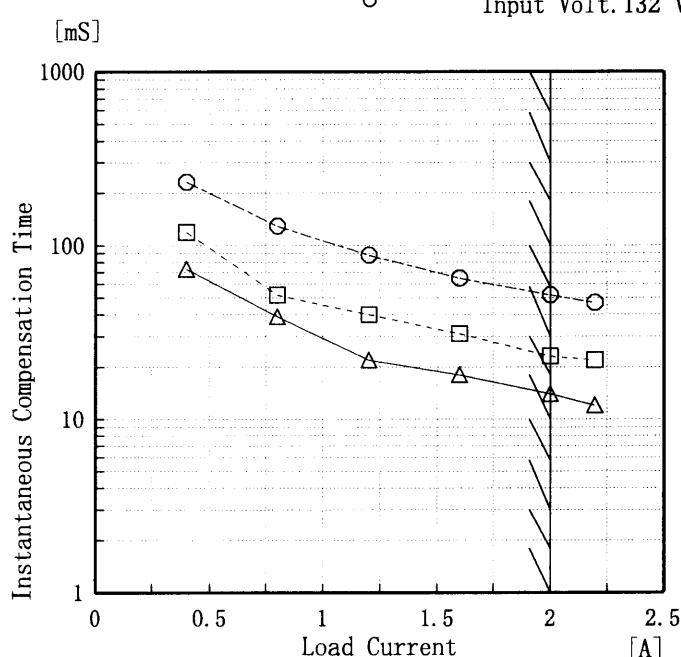
Model	LDA30F-15																																																									
Item	Efficiency (by Load Current) 効率(負荷電流特性)	Temperature 25°C	Testing Circuitry Figure A																																																							
Output	—																																																									
1. Graph	<p>Efficiency [%]</p> <p>Load Current [A]</p> <p>Legend: Input Volt. 85V (triangle), Input Volt. 100V (square), Input Volt. 132V (circle)</p>																																																									
2. Values	<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Efficiency [%]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0.4</td><td>69.6</td><td>67.6</td><td>62.9</td></tr> <tr><td>0.8</td><td>77.2</td><td>76.6</td><td>73.9</td></tr> <tr><td>1.2</td><td>79.8</td><td>79.5</td><td>77.9</td></tr> <tr><td>1.6</td><td>80.5</td><td>80.8</td><td>79.9</td></tr> <tr><td>2.0</td><td>80.9</td><td>81.2</td><td>80.9</td></tr> <tr><td>2.2</td><td>80.8</td><td>81.3</td><td>81.2</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>			Load Current [A]	Efficiency [%]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0.4	69.6	67.6	62.9	0.8	77.2	76.6	73.9	1.2	79.8	79.5	77.9	1.6	80.5	80.8	79.9	2.0	80.9	81.2	80.9	2.2	80.8	81.3	81.2	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
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Model	LDA30F-15		Temperature Testing Circuitry	25°C Figure A																																
Item	Hold-Up Time 出力保持時間																																			
Object	+15.0V2A																																			
1. Graph	□ Load 50%	Load 100%	2. Values																																	
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Input Voltage [V]	Hold-Up Time [mS]																																			
	Load 50%	Load 100%																																		
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<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.</p> <p>Note: Slanted line shows the range of the rated input voltage.</p> <p>出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。</p> <p>(注)斜線は定格入力電圧範囲を示す。</p>																																				

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Model	LDA30F-15	Temperature Testing Circuitry	25°C Figure A																																																				
Item	Instantaneous Interruption Compensation 瞬時停電保障																																																						
Object	+15.0V 2A	2. Values																																																					
1. Graph	<p>—△— Input Volt. 85 V —□— Input Volt. 100 V —○— Input Volt. 132 V</p>	<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Time [mS]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>0.4</td><td>73</td><td>119</td><td>232</td></tr> <tr><td>0.8</td><td>39</td><td>52</td><td>129</td></tr> <tr><td>1.2</td><td>22</td><td>40</td><td>88</td></tr> <tr><td>1.6</td><td>18</td><td>31</td><td>65</td></tr> <tr><td>2.0</td><td>14</td><td>23</td><td>52</td></tr> <tr><td>2.2</td><td>12</td><td>22</td><td>47</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>			Load Current [A]	Time [mS]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0.0	—	—	—	0.4	73	119	232	0.8	39	52	129	1.2	22	40	88	1.6	18	31	65	2.0	14	23	52	2.2	12	22	47	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Load Current [A]	Time [mS]																																																						
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This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated load current.

瞬時停電保障時間とは、出力電圧が定電圧精度の規格範囲を保持している瞬時停電時間をいう。

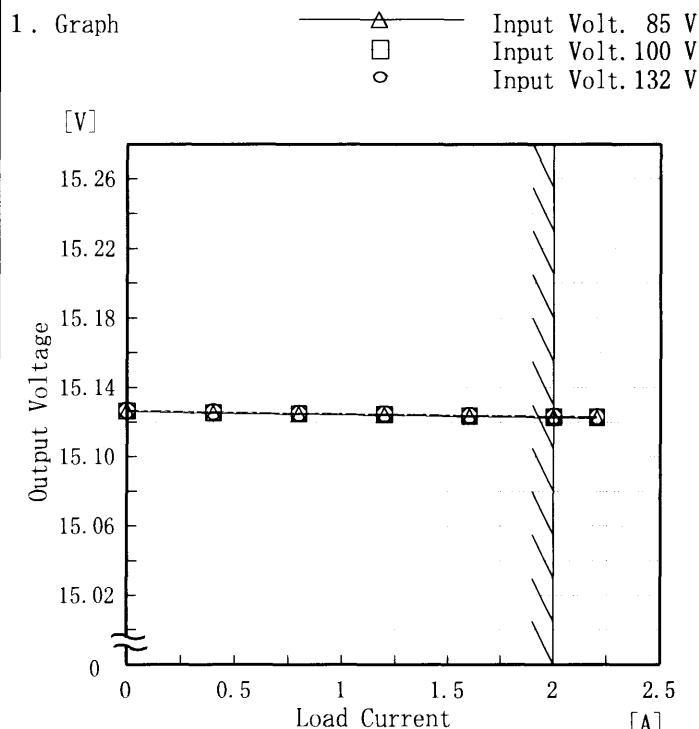
(注) 斜線は定格負荷電流範囲を示す。

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Model LDA30F-15

Item Load Regulation 静的負荷変動

Object +15.0V2A



Temperature 25°C
Testing Circuitry Figure A

2. Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	15.126	15.127	15.127
0.4	15.125	15.126	15.126
0.8	15.125	15.125	15.125
1.2	15.124	15.125	15.125
1.6	15.123	15.124	15.124
2.0	15.123	15.123	15.124
2.2	15.123	15.123	15.123
—	—	—	—
—	—	—	—
—	—	—	—

Note: Slanted line shows the range of the rated load current.

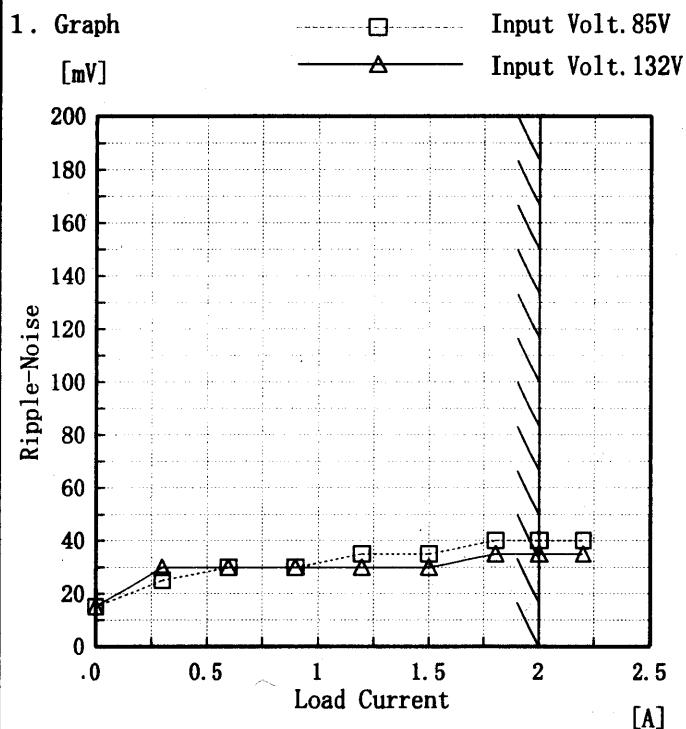
(注)斜線は定格負荷電流範囲を示す。

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Model	LDA30F-15	Temperature	25°C																																						
Item	Ripple Voltage (by Load Current) リップル電圧(負荷電流特性)	Testing Circuitry	Figure A																																						
Object	+15.0V2A																																								
1. Graph																																									
<p>Y-axis: Ripple Voltage [mV] (0 to 150). X-axis: Load Current [A] (0 to 2.5).</p> <p>Legend: □ Input Volt. 85V, △ Input Volt. 132V.</p>			2. Values																																						
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<p>Ripple Voltage is shown as p-p in the figure below.</p> <p>Note: Slanted line shows the range of the rated load current.</p> <p>リップル電圧は、下図 p - p 値で示される。 (注)斜線は定格負荷電流範囲を示す。</p> <p>T1: Due to AC Input Line T2: Due to Switching</p> <p>Ripple [mVp-p]</p> <p>Fig. Complex Ripple Wave Form 図 リップル波形詳細図</p>																																									

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Model	LDA30F-15
Item	Ripple-Noise リップルノイズ
Object	+15.0V2A



Temperature 25°C
Testing Circuitry Figure A

2. Values

Load current [A]	Input Volt. 85 [V]	Input Volt. 132 [V]
	Ripple-Noise [mV]	Ripple-Noise [mV]
0.00	15	15
0.30	25	30
0.60	30	30
0.90	30	30
1.20	35	30
1.50	35	30
1.80	40	35
2.00	40	35
2.20	40	35
—	—	—
—	—	—

Ripple-Noise is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

リップルノイズは、下図 p - p 値で示される。

(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line

入力商用周期

T2: Due to Switching

スイッチング周期

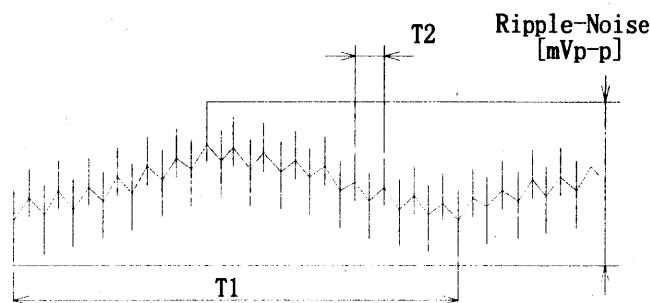


Fig. Complex Ripple Wave Form

図 リップル波形詳細図

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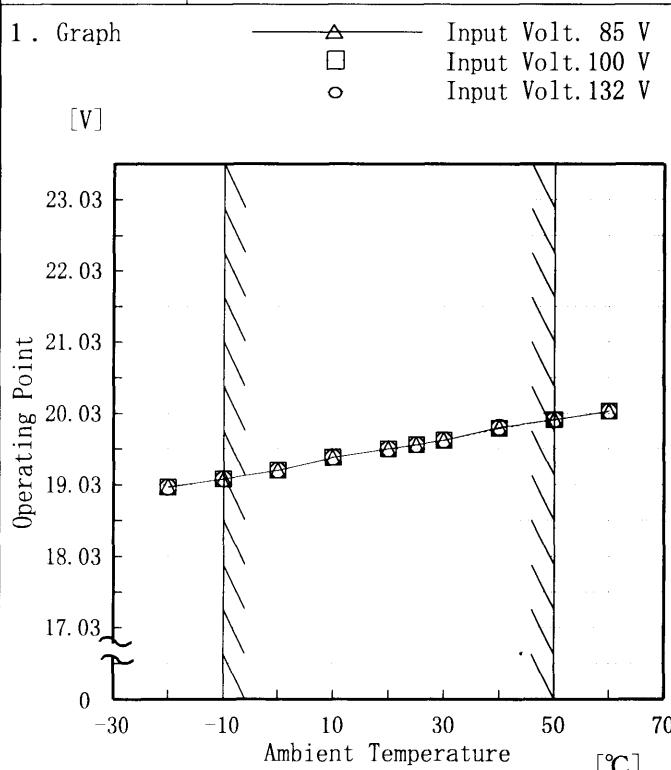
Model	LDA30F-15			Temperature 25°C Testing Circuitry Figure A																																																							
Item	Overcurrent Protection 過電流保護																																																										
Object	+15.0V 2A																																																										
1. Graph	<p>[V] Input Volt. 85 V Input Volt. 100 V Input Volt. 132 V</p> <p>Output Voltage [V]</p> <p>20.0 15.0 10.0 5.0 0.0</p> <p>0 1 2 3</p> <p>Load Current [A]</p>																																																										
2. Values	<table border="1"> <thead> <tr> <th rowspan="2">Output Voltage [V]</th> <th colspan="3">Load Current [A]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>15.00</td><td>2.51</td><td>2.46</td><td>2.45</td></tr> <tr><td>14.25</td><td>2.50</td><td>2.47</td><td>2.46</td></tr> <tr><td>13.50</td><td>2.51</td><td>2.48</td><td>2.48</td></tr> <tr><td>12.00</td><td>2.53</td><td>2.51</td><td>2.51</td></tr> <tr><td>10.50</td><td>2.55</td><td>2.54</td><td>2.54</td></tr> <tr><td>9.00</td><td>2.58</td><td>2.57</td><td>2.57</td></tr> <tr><td>7.50</td><td>2.61</td><td>2.60</td><td>2.61</td></tr> <tr><td>6.00</td><td>2.63</td><td>2.63</td><td>2.62</td></tr> <tr><td>4.50</td><td>2.64</td><td>2.63</td><td>2.62</td></tr> <tr><td>3.00</td><td>2.66</td><td>2.64</td><td>2.62</td></tr> <tr><td>1.50</td><td>2.63</td><td>2.59</td><td>2.53</td></tr> <tr><td>0.00</td><td>2.35</td><td>2.21</td><td>2.03</td></tr> </tbody> </table>				Output Voltage [V]	Load Current [A]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	15.00	2.51	2.46	2.45	14.25	2.50	2.47	2.46	13.50	2.51	2.48	2.48	12.00	2.53	2.51	2.51	10.50	2.55	2.54	2.54	9.00	2.58	2.57	2.57	7.50	2.61	2.60	2.61	6.00	2.63	2.63	2.62	4.50	2.64	2.63	2.62	3.00	2.66	2.64	2.62	1.50	2.63	2.59	2.53	0.00	2.35	2.21	2.03
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<p>Note: Slanted line shows the range of the rated load current.</p> <p>(注)斜線は定格負荷電流範囲を示す。</p>																																																											

COSEL

Model	LDA30F-15
Item	Overvoltage Protection 過電圧保護
Object	+15.0V2A

Testing Circuitry

Figure A



2. Values

Ambient Temperature [°C]	Operating Point [V]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
-20	19.00	18.99	18.99
-10	19.11	19.11	19.11
0	19.23	19.23	19.23
10	19.41	19.41	19.41
20	19.53	19.53	19.53
25	19.59	19.59	19.59
30	19.65	19.65	19.65
40	19.83	19.82	19.83
50	19.94	19.94	19.94
60	20.06	20.06	20.06
—	—	—	—

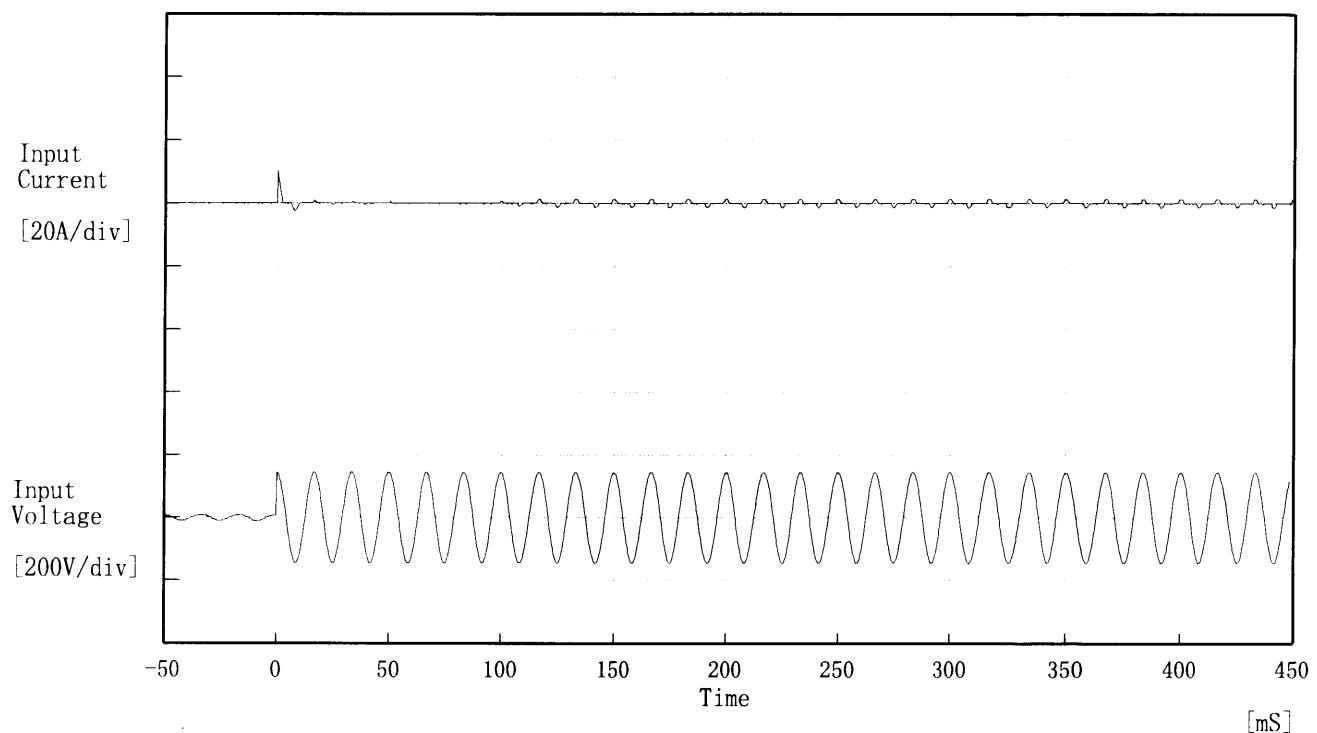
Load 0%

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

COSEL

Model	LDA30F-15	Temperature Testing Circuitry Figure A	25°C
Item	Inrush Current 突入電流		
Object	_____		



Input Voltage 100 V

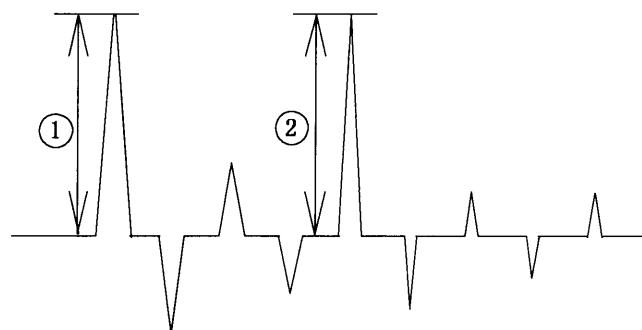
Frequency 60 Hz

Load 100 %

Inrush Current

① 10.10 [A]

② 1.50 [A]



COSEL

Model	LDA30F-15	Temperature Testing Circuitry Figure A	25°C
Item	Dynamic Load Responce 動的負荷變動		
Object	+15.0V2A		

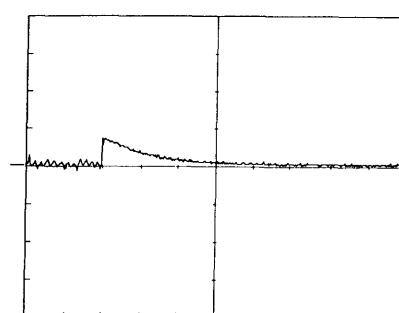
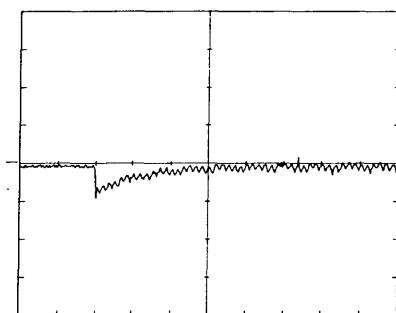
Input Volt. 100 V

Cycle 1000 mS

Load Current

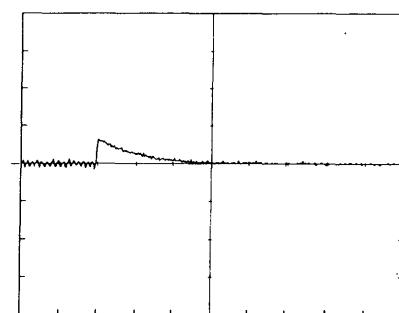
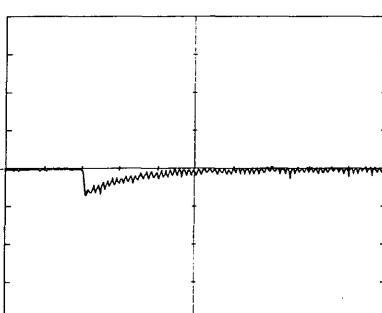
Load 0% ↔

Load 100 %



Load 0% ↔

Load 50 %



100 mV/div

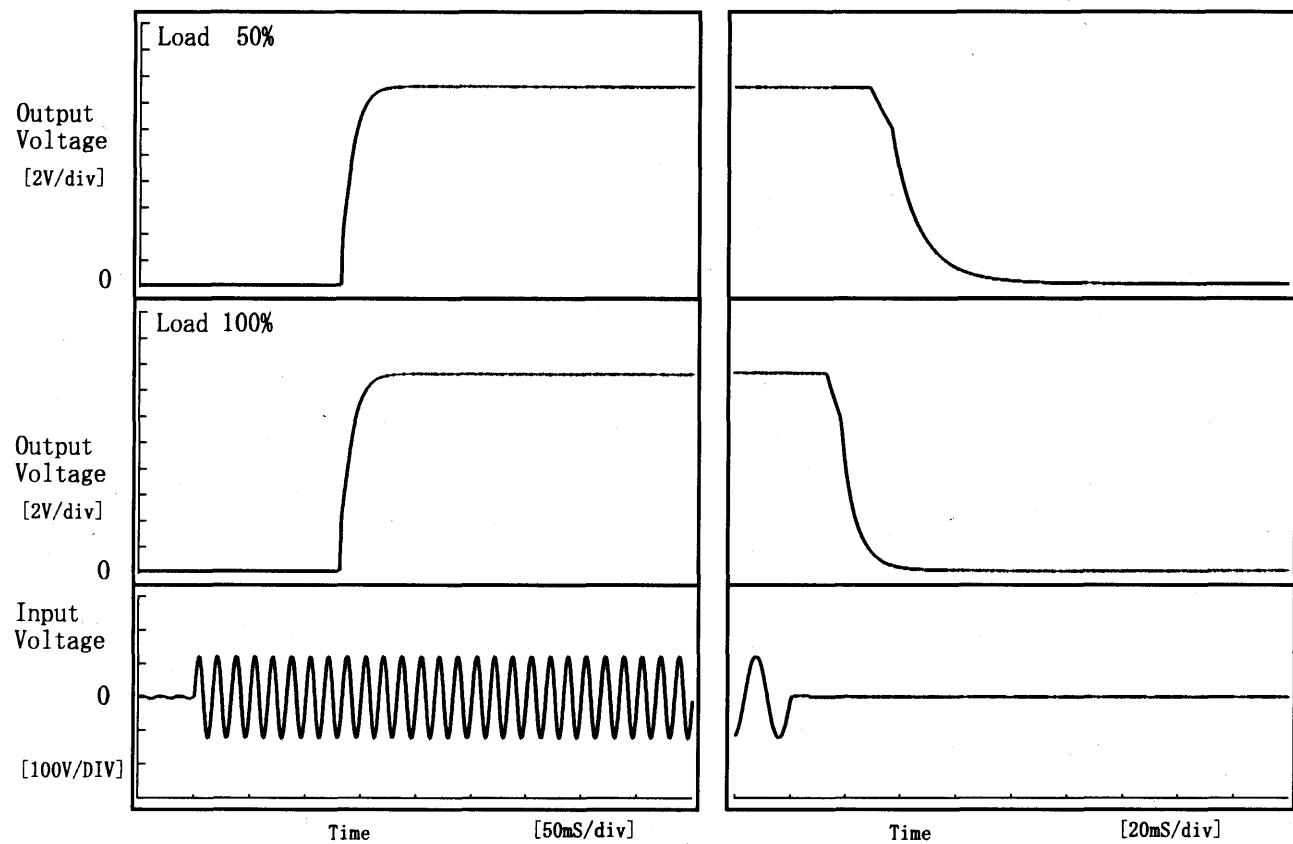
10 mS/div

COSEL

Model	LDA30F-15
Item	Rise and Fall Time 立上り、立下り時間
Object	+15.0V2A

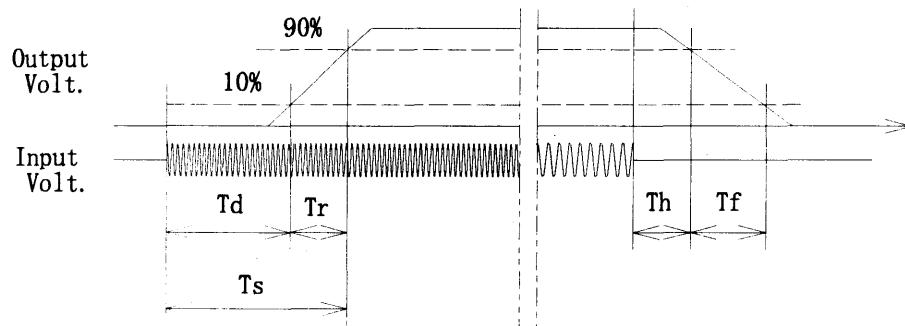
Temperature
Testing Circuitry 25°C
Figure A

1. Graph



2. Values

Load	Time	T d	T r	T s	T h	T f	[mS]
50 %		131.5	19.8	151.3	32.5	26.1	
100 %		131.3	20.0	151.3	15.7	14.3	



COSEL

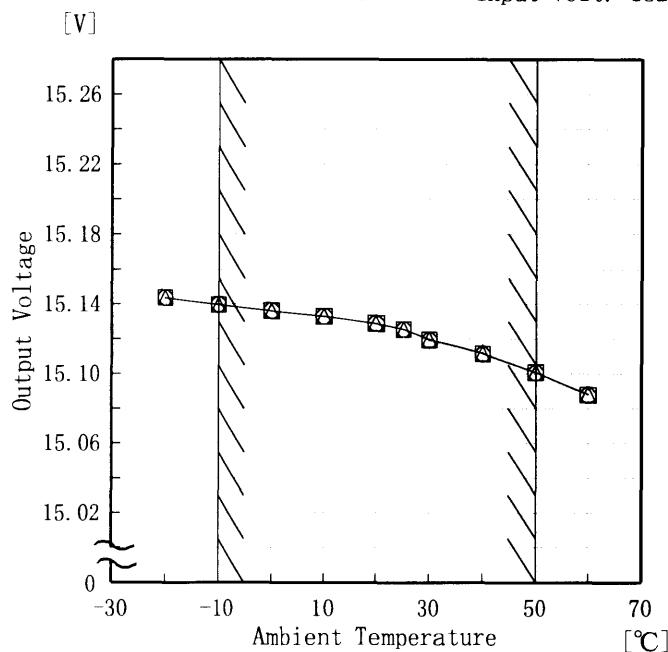
Model LDA30F-15

Item Ambient Temperature Drift
周围温度変動

Object + 15.0V2A

1. Graph

- △ Input Volt. 85V
 □ Input Volt. 100V
 ○ Input Volt. 132V



Load 100%

Note: Slanted line shows the range of the rated ambient temperature.

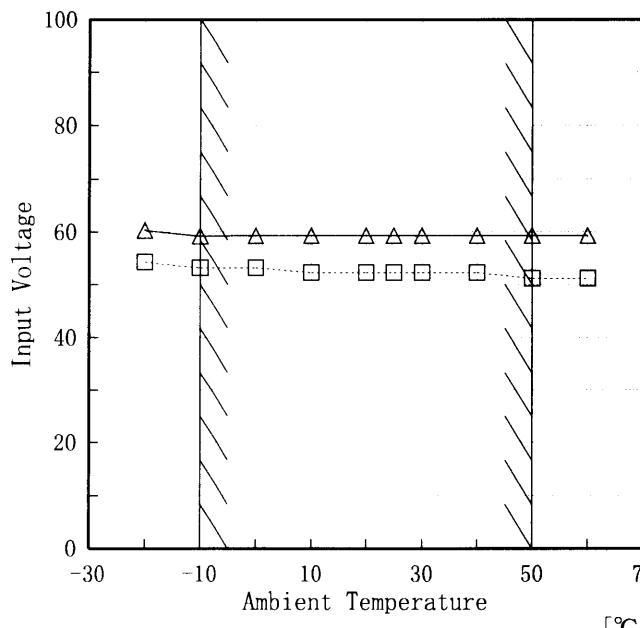
(注) 斜線は定格周囲温度範囲を示す。

Testing Circuitry Figure A

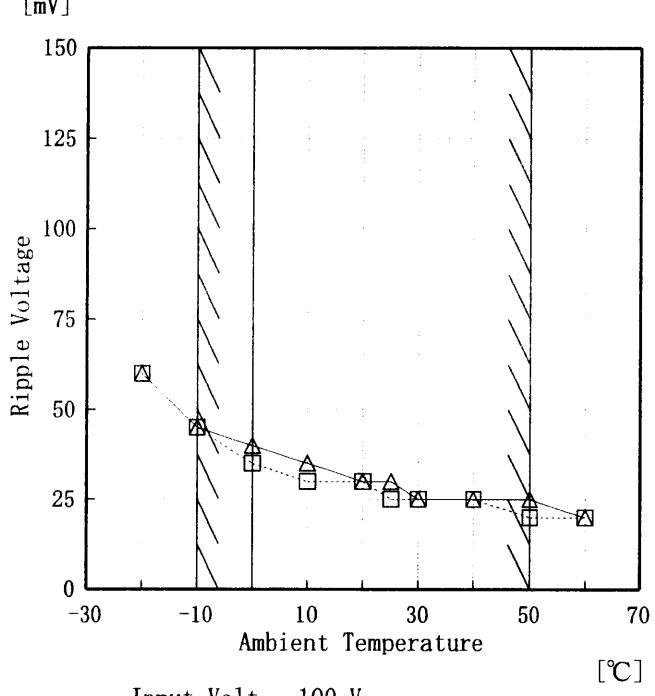
2. Values

Temperature [°C]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
-20	15.144	15.144	15.144
-10	15.140	15.140	15.140
0	15.136	15.136	15.136
10	15.133	15.133	15.133
20	15.129	15.129	15.129
25	15.126	15.125	15.125
30	15.120	15.119	15.119
40	15.112	15.111	15.111
50	15.101	15.101	15.101
60	15.088	15.088	15.087
—	—	—	—

COSEL

Model	LDA30F-15		Testing Circuitry Figure A			
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧					
Object	+15.0V2A					
1. Graph						
[V]	 Load 50% Load 100%					
Input Voltage [V]						
Ambient Temperature [°C]						
Note: Slanted line shows the range of the rated ambient temperature.						
(注)斜線は定格周囲温度範囲を示す。						
2. Values						
Ambient Temperature [°C]	Input Voltage [V]					
	Load 50%	Load 100%				
-20	54	60				
-10	53	59				
0	53	59				
10	52	59				
20	52	59				
25	52	59				
30	52	59				
40	52	59				
50	51	59				
60	51	59				
—	—	—				

COSEL

Model	LDA30F-15	Testing Circuitry	Figure A																																							
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)																																									
Object	+15.0V2A	1. Graph																																								
		 Load 50% Load 100%																																								
		 <p>[mV]</p> <p>Ambient Temperature [°C]</p> <p>Input Volt. 100 V</p>																																								
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		<table border="1"> <thead> <tr> <th>Ambient Temp. [°C]</th> <th>Load 50%</th> <th>Load 100%</th> </tr> <tr> <th></th> <th>Ripple Output Volt. [mV]</th> <th>Ripple Output Volt. [mV]</th> </tr> </thead> <tbody> <tr><td>-20</td><td>60</td><td>60</td></tr> <tr><td>-10</td><td>45</td><td>45</td></tr> <tr><td>0</td><td>35</td><td>40</td></tr> <tr><td>10</td><td>30</td><td>35</td></tr> <tr><td>20</td><td>30</td><td>30</td></tr> <tr><td>25</td><td>25</td><td>30</td></tr> <tr><td>30</td><td>25</td><td>25</td></tr> <tr><td>40</td><td>25</td><td>25</td></tr> <tr><td>50</td><td>20</td><td>25</td></tr> <tr><td>60</td><td>20</td><td>20</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>		Ambient Temp. [°C]	Load 50%	Load 100%		Ripple Output Volt. [mV]	Ripple Output Volt. [mV]	-20	60	60	-10	45	45	0	35	40	10	30	35	20	30	30	25	25	30	30	25	25	40	25	25	50	20	25	60	20	20	—	—	—
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COSEL

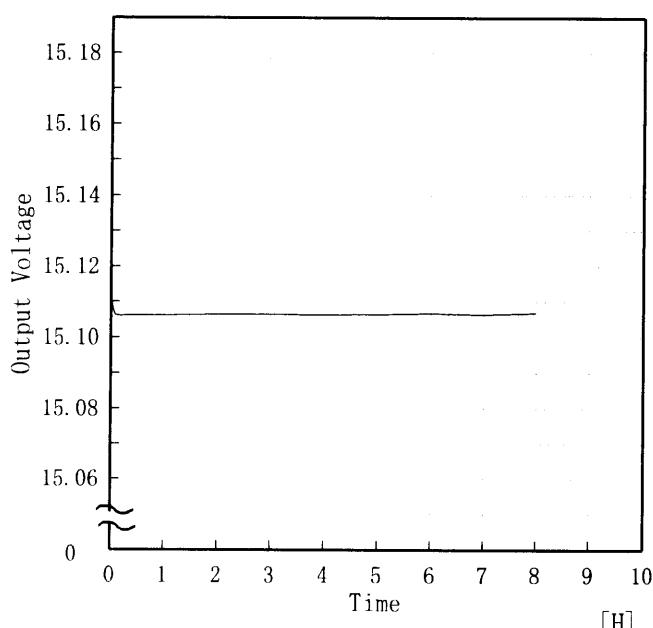
Model LDA30F-15

Item Time Lapse Drift 経時ドリフト

Object +15.0V 2A

1. Graph

[V]



Input Volt. 100V

Load 100%

Temperature 25°C
Testing Circuitry Figure A

2. Values

Time since start [H]	Output Voltage [V]
0.0	15.117
0.5	15.106
1.0	15.106
2.0	15.107
3.0	15.107
4.0	15.106
5.0	15.107
6.0	15.107
7.0	15.107
8.0	15.107



Model	LDA30F-15	Testing Circuitry Figure A
Item	Output Voltage Accuracy 定電圧精度	
Object	+15.0V 2A	

Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10~50 °C

Input Voltage : 85~132 V

Load Current : 0~2 A

* Output Voltage Accuracy = ±(Maximum of Output Voltage — Minimum of Output Voltage) / 2

$$* \text{ Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 -10~50 °C

入力電圧 85~132 V

負荷電流 0~2 A

* 定電圧精度(変動値) = ±(出力電圧の最高値—出力電圧の最低値) / 2

$$* \text{ 定電圧精度(変動率)} = \frac{\text{変動値}}{\text{定格出力電圧}} \times 100$$

Item	Temperature [°C]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy(Ration) [%]
Maximum Voltage	-10	132	0	15.147	±26	±0.2
Minimum Voltage	50	132	2	15.096		



Model	LDA30F-15		
Item	Condensation 結露特性	Testing Circuitry	Figure A
Object	+15.0V 2A		

1. Condensation test

Testing procedure is as follows.

- ① Keeping and cooling the unit in a tank at -10°C for an hour with the input off.
- ② Taking it out of the tank and dewing itself in a room where the temperature is 25°C and the humidity is 40%RH.
- ③ Testing electrical characteristics of the unit to confirm there be no fault.

1. 結露特性試験

入力を切った状態で、恒温槽で-10°Cに冷却しておき、約1時間後に恒温槽から取り出し、室温25°C、湿度40%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

2. Values

Item	Data	Testing Conditions
Output Voltage [V]	15.125	Input Volt.: 100V, Load Current:2A
Line Regulation [mV]	4	Input Volt.: 85~132V, Load Current:2A
Load Regulation [mV]	6	Input Volt.: 100V, Load Current:0~2A



Model	LDA30F-15	Temperature	25°C
Item	Leakage Current 漏洩電流	Testing Circuitry	Figure B
Object	_____		

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DENTORI	0.16	0.20	0.26
(B) IEC60950	0.16	0.20	0.26

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力の両相について測定し、その大きい方を漏洩電流測定値とする。

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B) IEC60950	—	—	—



Model	LDA30F-15	Temperature Testing Circuitry	25°C Figure C
Item	Line Noise Tolerance 入力雑音耐量		
Object	+ 15.0V2A		

1. Results

Pulse Width [nS]	MODE	No protection failure should occur 保護回路の誤動作がない	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation
1000	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation

2. Conditions

Input Voltage : 100 V
 Pulse Voltage : 2000 V
 Pulse Cycle : 10 mS
 Pulse Input Duration : 1 min. or more
 Load : 100 %

COSEL

Model	LDA30F-15
Item	Conducted Emission 雜音端子電圧
Object	_____

Temperature
Testing Circuitry 25°C
Figure D

1. Graph

Remarks

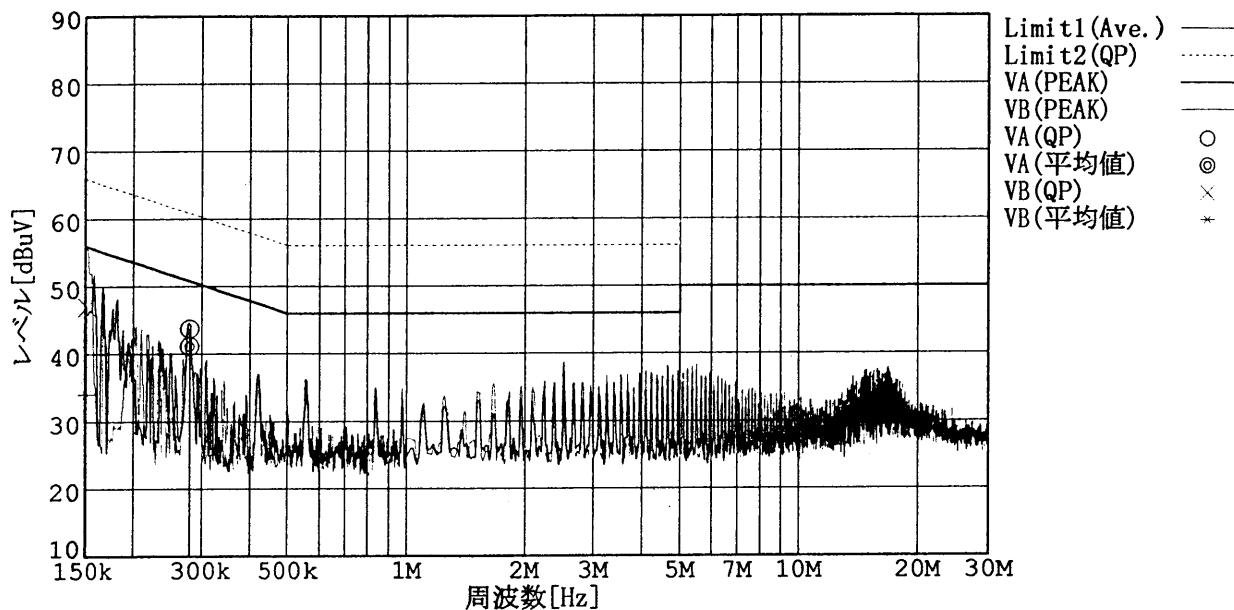
Input Volt. 100 V (VCCI Class B)

120 V (FCC Class B)

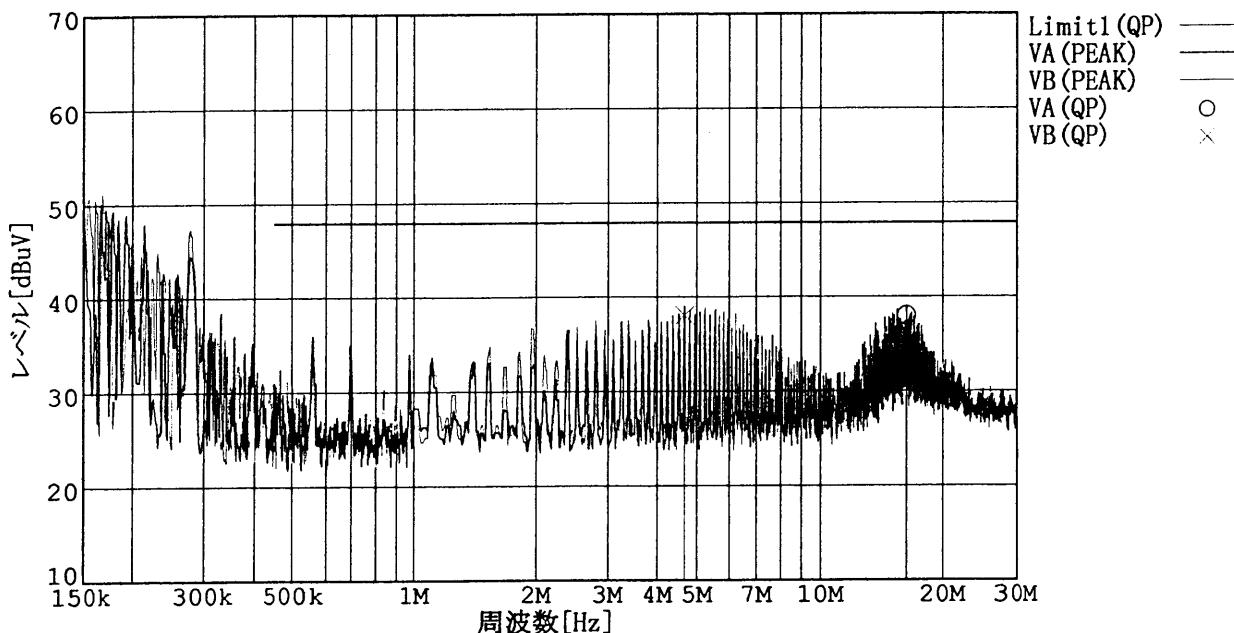
Load 100 %

規格 1: [VCCI] Class B(平均値)

規格 2: [VCCI] Class B(QP)



規格 1: [FCC Part15] Class B



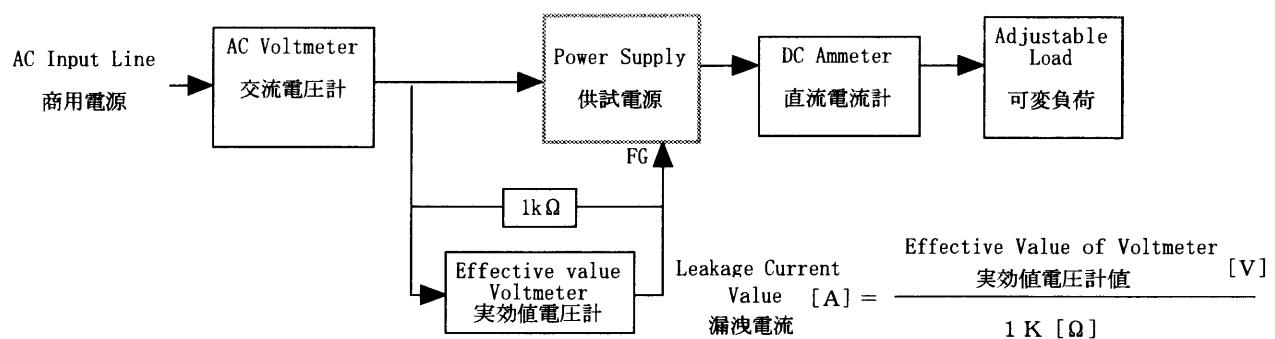
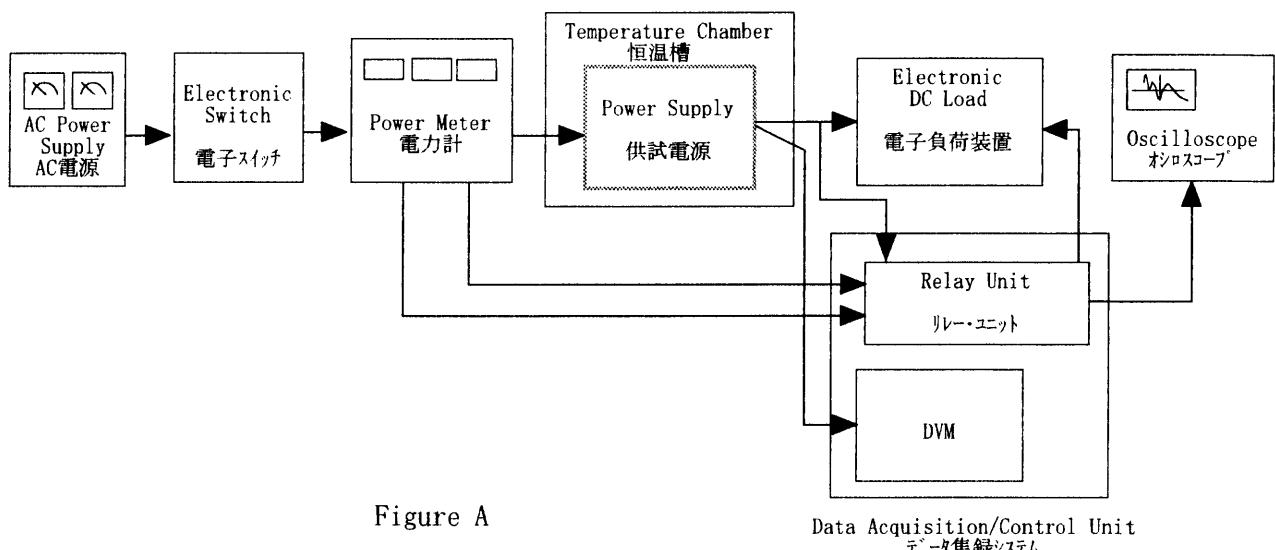


Figure B (DENTORI)

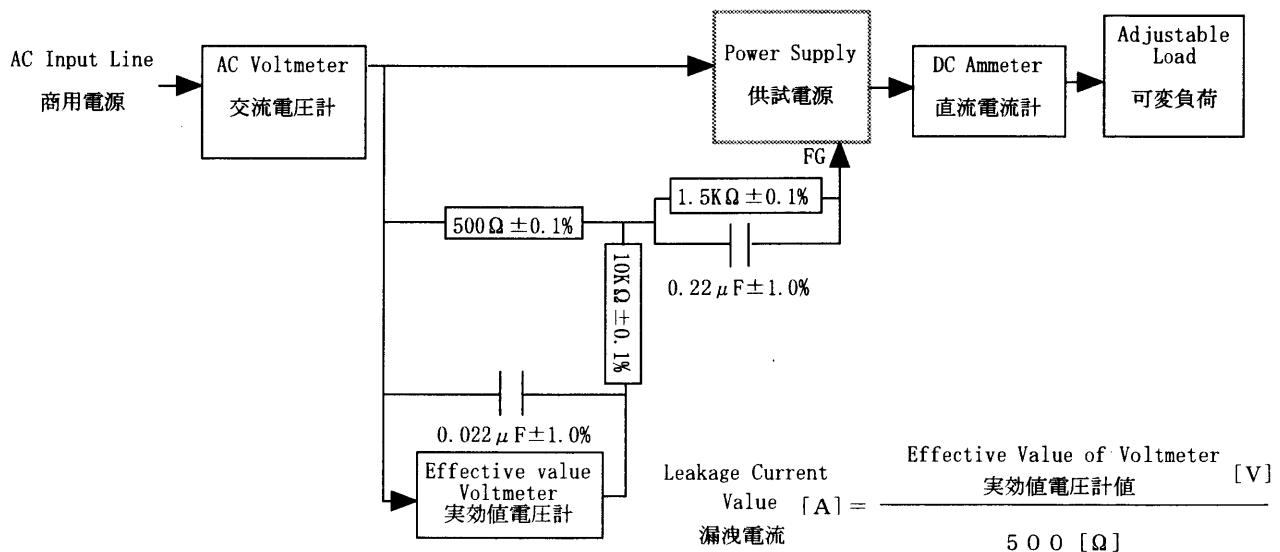


Figure B (IEC 60950)

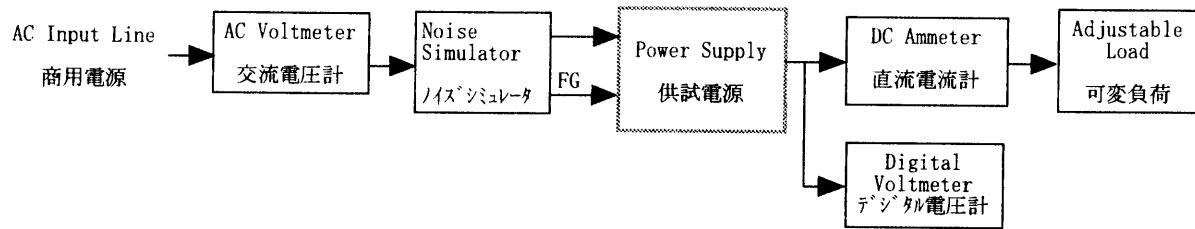


Figure C

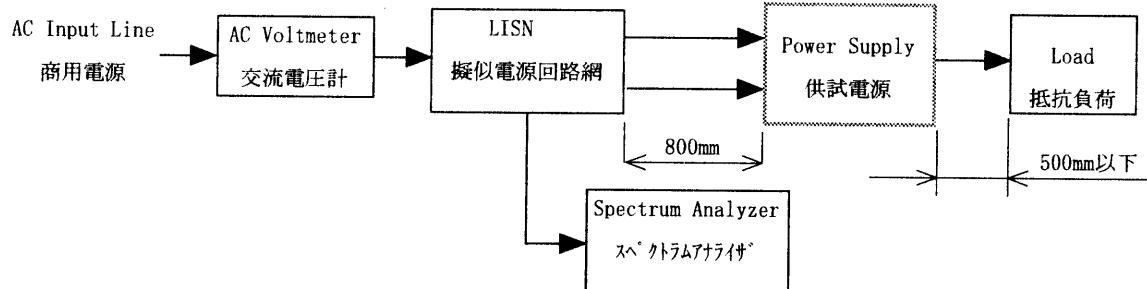


Figure D

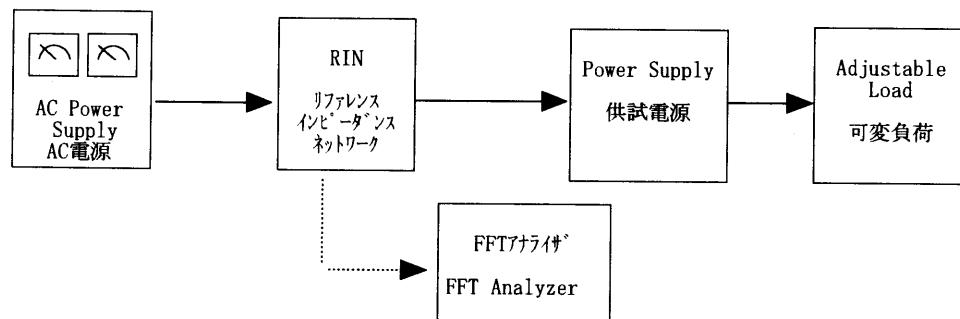


Figure E