参考資料

COSEL

TEST DATA OF LDA15F-12

(100V INPUT)

Regulated DC Power Supply

Date : June 23. 1999

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Design Manager

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Design Engineer

コーセル株式会社

COSEL CO., LTD.

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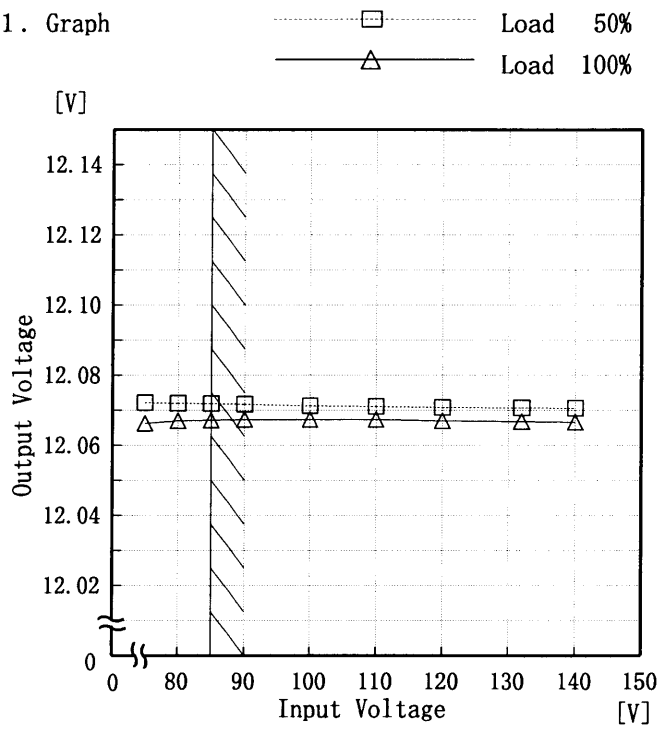
Model	LDA15F-12
Item	Line Regulation 静的入力変動
Object	+12.0V1.3A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
75	12.072	12.066
80	12.072	12.067
85	12.072	12.067
90	12.072	12.067
100	12.071	12.067
110	12.071	12.067
120	12.071	12.067
132	12.071	12.067
140	12.071	12.067

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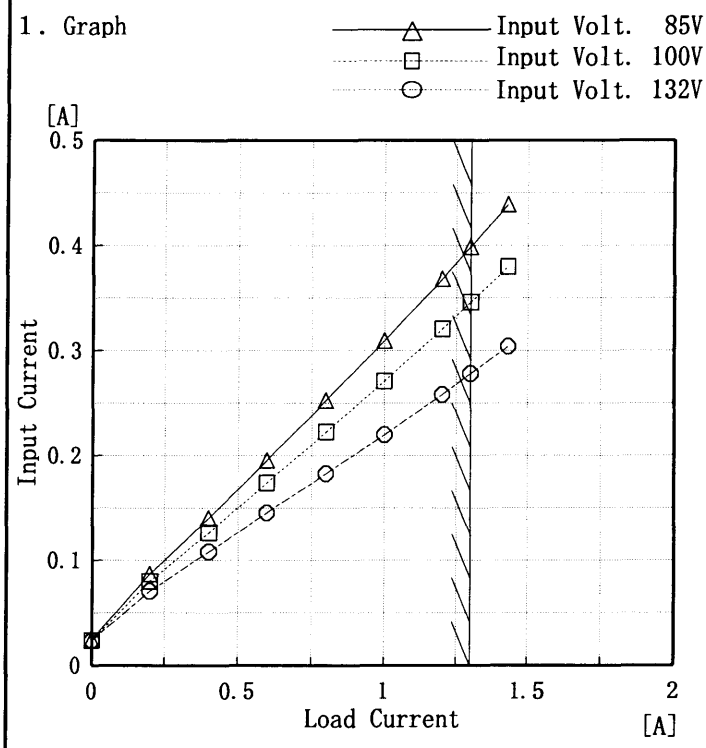
Model	LDA15F-12
Item	Input Current (by Load Current) 入力電流 (負荷特性)
Output	———

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Input Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	0.025	0.024	0.024
0.20	0.087	0.080	0.071
0.40	0.140	0.127	0.109
0.60	0.196	0.174	0.145
0.80	0.253	0.223	0.183
1.00	0.310	0.271	0.220
1.20	0.368	0.321	0.258
1.30	0.399	0.346	0.278
1.43	0.439	0.380	0.304
—	—	—	—
—	—	—	—
—	—	—	—

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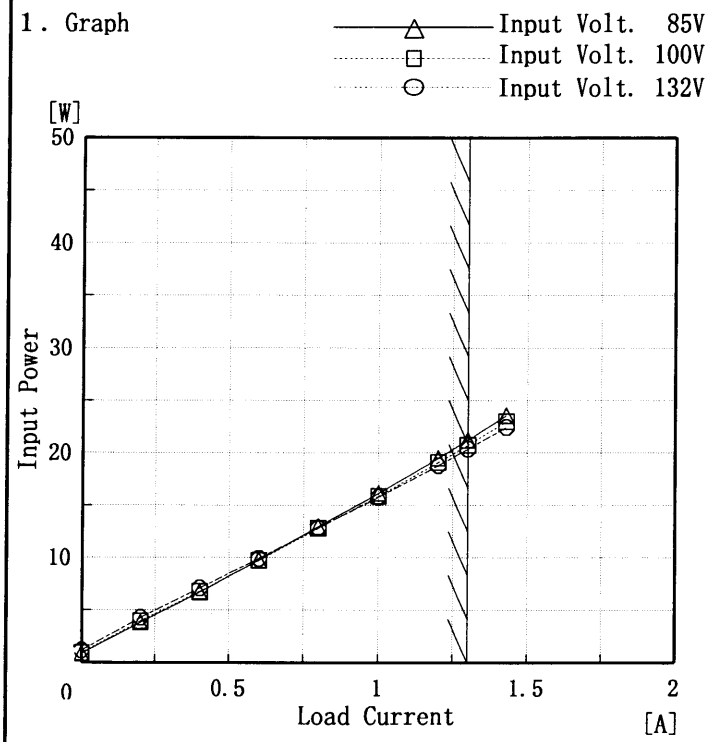
COSEL

Model	LDA15F-12
Item	Input Power (by Load Current) 入力電力 (負荷特性)
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Input Power [W] Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	0.83	0.90	1.13
0.20	3.82	3.94	4.27
0.40	6.70	6.77	7.10
0.60	9.76	9.74	9.92
0.80	12.95	12.82	12.85
1.00	16.16	15.89	15.77
1.20	19.50	19.11	18.81
1.30	21.23	20.75	20.37
1.43	23.57	22.98	22.43
—	—	—	—
—	—	—	—
—	—	—	—

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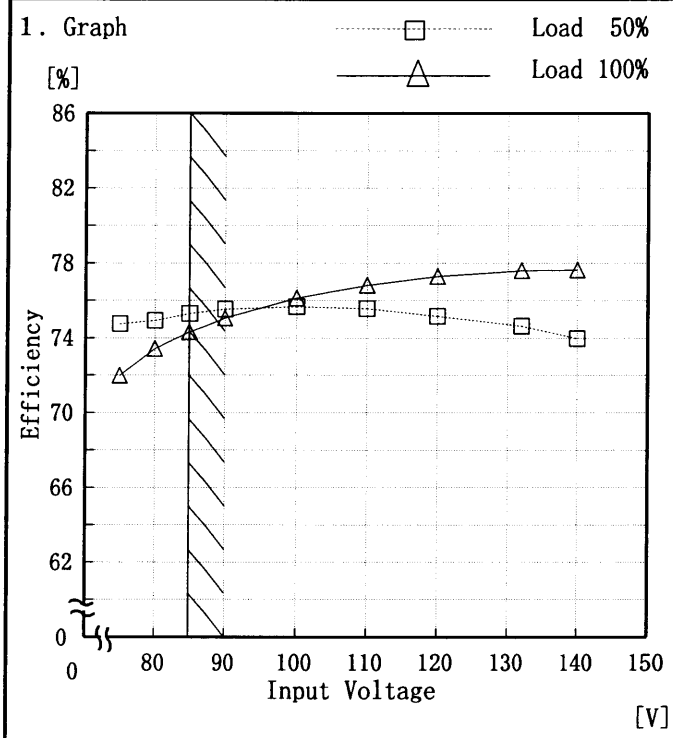
Model	LDA15F-12
Item	Efficiency 効率
Object	———————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Efficiency [%]	
	Load 50%	Load 100%
75	74.8	72.0
80	74.9	73.4
85	75.3	74.3
90	75.5	75.1
100	75.7	76.1
110	75.6	76.8
120	75.2	77.3
132	74.6	77.6
140	74.0	77.6

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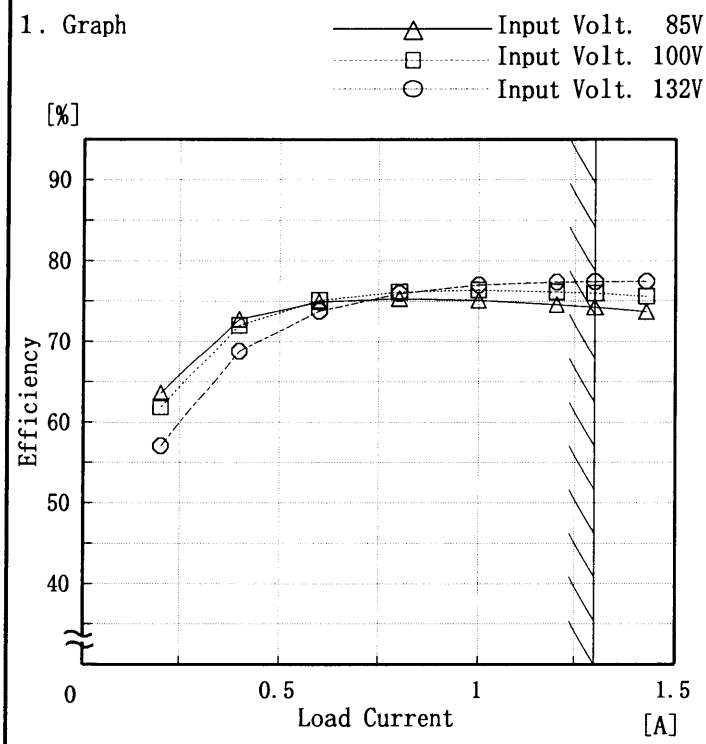
Model	LDA15F-12
Item	Efficiency (by Load Current) 効率 (負荷電流特性)
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Efficiency [%] Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.20	63.6	61.9	57.1
0.40	72.7	72.0	68.8
0.60	74.9	75.1	73.8
0.80	75.3	76.2	75.9
1.00	75.1	76.4	77.0
1.20	74.6	76.1	77.4
1.30	74.3	76.0	77.4
1.43	73.7	75.6	77.4
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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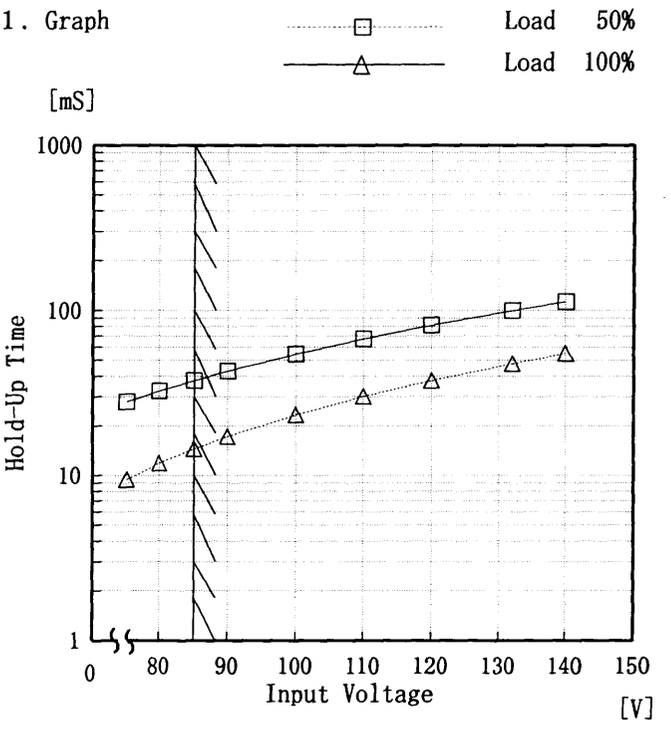
Model	LDA15F-12
Item	Hold-Up Time 出力保持時間
Object	+12.0V1.3A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated input voltage.

出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Hold-Up Time [mS]	
	Load 50%	Load 100%
75	28	9
80	33	12
85	38	15
90	43	17
100	54	23
110	67	30
120	81	38
132	100	48
140	113	55

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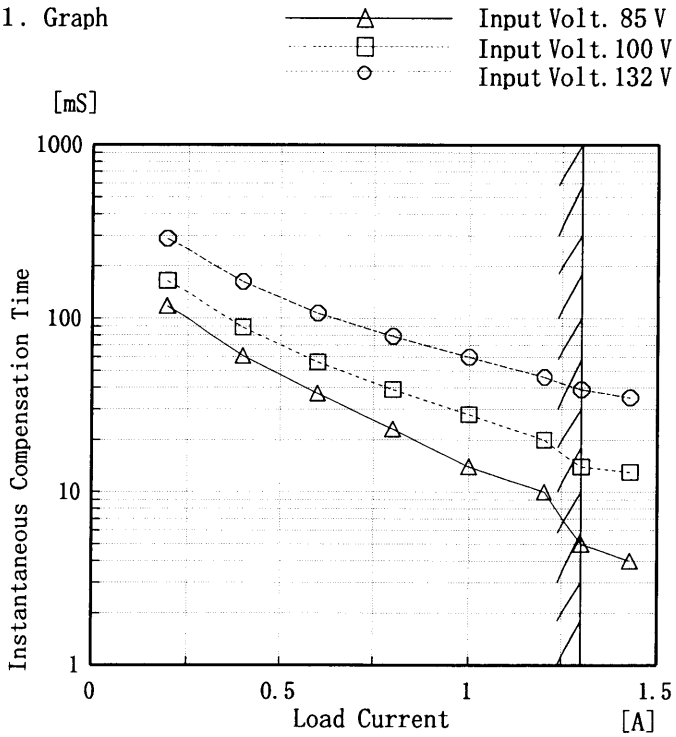
Model	LDA15F-12
Item	Instantaneous Interruption Compensation 瞬時停電保障
Object	+12.0V1.3A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.
Note:Slanted line shows the range of the rated load current.

瞬時停電保障時間とは、出力電圧が定電圧精度の規格範囲を保持している瞬時停電時間をいう。
(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Time [mS]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	—	—	—
0.20	118	165	288
0.40	61	89	163
0.60	37	56	107
0.80	23	39	79
1.00	14	28	60
1.20	10	20	46
1.30	5	14	39
1.43	4	13	35
—	—	—	—
—	—	—	—

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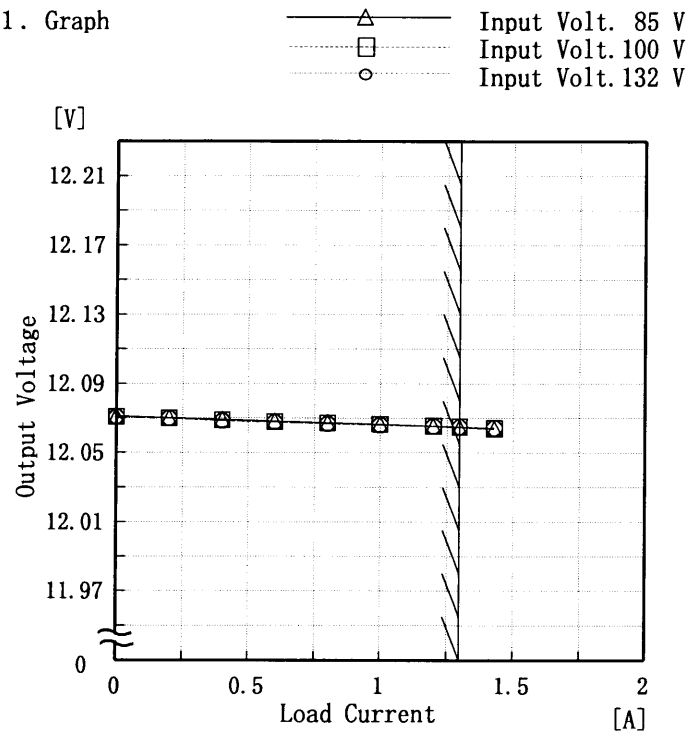
Model	LDA15F-12
Item	Load Regulation　静的負荷変動
Object	+12.0V1.3A

Temperature　25℃
Testing Circuitry　Figure A

1. Graph



Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	12.071	12.071	12.071
0.20	12.070	12.070	12.070
0.40	12.069	12.069	12.069
0.60	12.068	12.068	12.068
0.80	12.067	12.067	12.067
1.00	12.067	12.066	12.066
1.20	12.065	12.066	12.065
1.30	12.065	12.065	12.065
1.43	12.064	12.064	12.064
—	—	—	—

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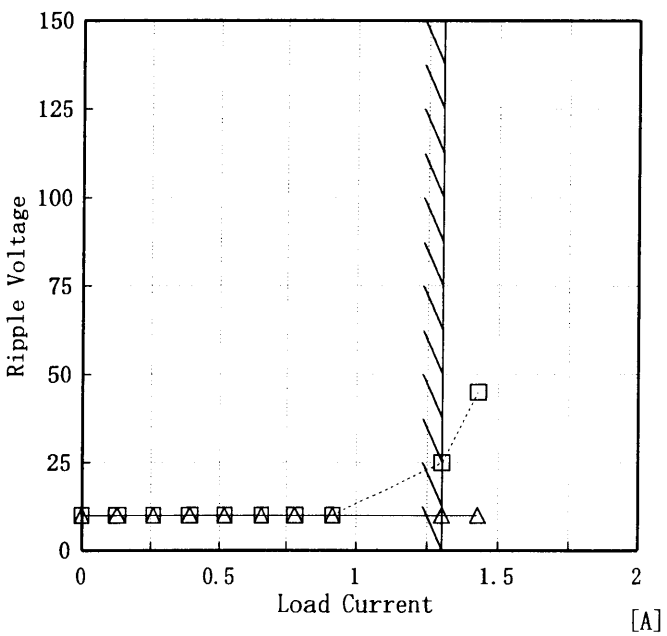
Model	LDA15F-12
Item	Ripple Voltage (by Load Current) リップル電圧(負荷電流特性)
Object	+12.0V1.3A

Temperature 25℃
Testing Circuitry Figure A

1. Graph

□ Input Volt. 85V
△ Input Volt. 132V



Ripple Voltage is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

リップル電圧は、下図ｐ－ｐ値で示される。

(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
ｽｲｯﾁﾝｸﾞ周期

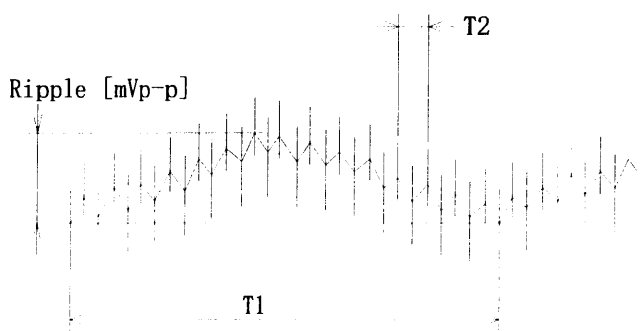


Fig. Complex Ripple Wave Form
図 リップル波形詳細図

2. Values

Load Current [A]	Input Volt. 85 [V] Ripple Output Volt. [mV]	Input Volt. 132 [V] Ripple Output Volt. [mV]
0.00	10	10
0.13	10	10
0.26	10	10
0.39	10	10
0.52	10	10
0.65	10	10
0.78	10	10
0.91	10	10
1.30	25	10
1.43	45	10
—	—	—

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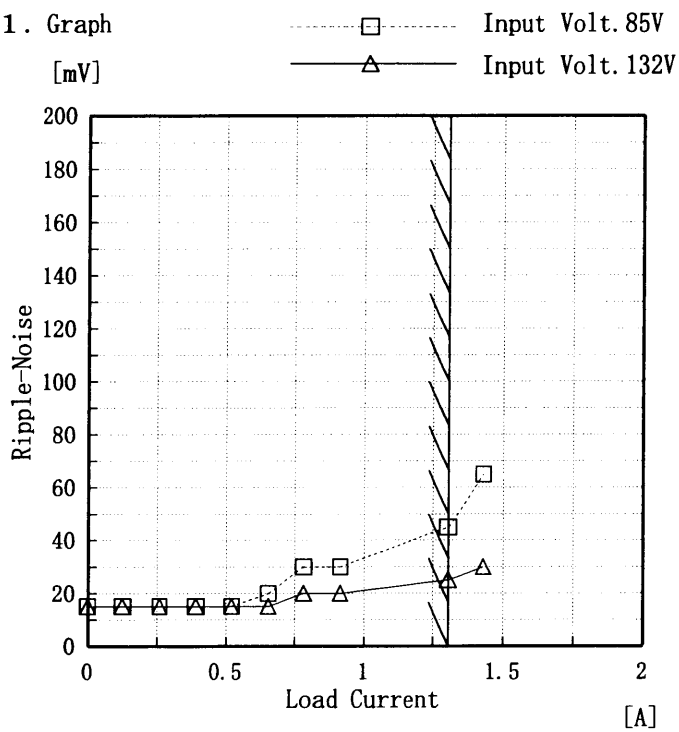
Model	LDA15F-12
Item	Ripple-Noise　リップルノイズ
Object	+12.0V1.3A

Temperature　25℃
Testing Circuitry　Figure A

1. Graph



Ripple-Noise is shown as p-p in the figure below.
Note: Slanted line shows the range of the rated load current.

リップルノイズは、下図ｐ－ｐ値で示される。
(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
ｽｲｯﾁﾝｸﾞ周期

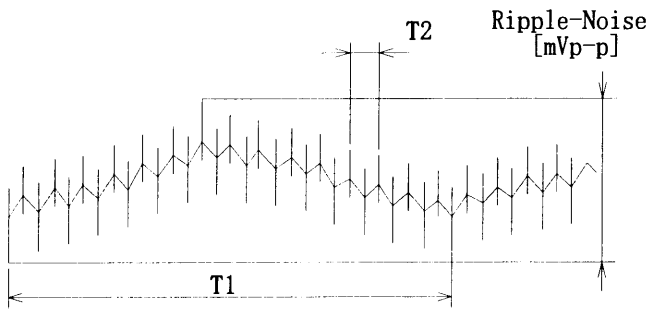


Fig. Complex Ripple Wave Form
図　リップル波形詳細図

2. Values

Load current [A]	Input Volt. 85 [V] Ripple-Noise [mV]	Input Volt. 132 [V] Ripple-Noise [mV]
0.00	15	15
0.13	15	15
0.26	15	15
0.39	15	15
0.52	15	15
0.65	20	15
0.78	30	20
0.91	30	20
1.30	45	25
1.43	65	30
—	—	—

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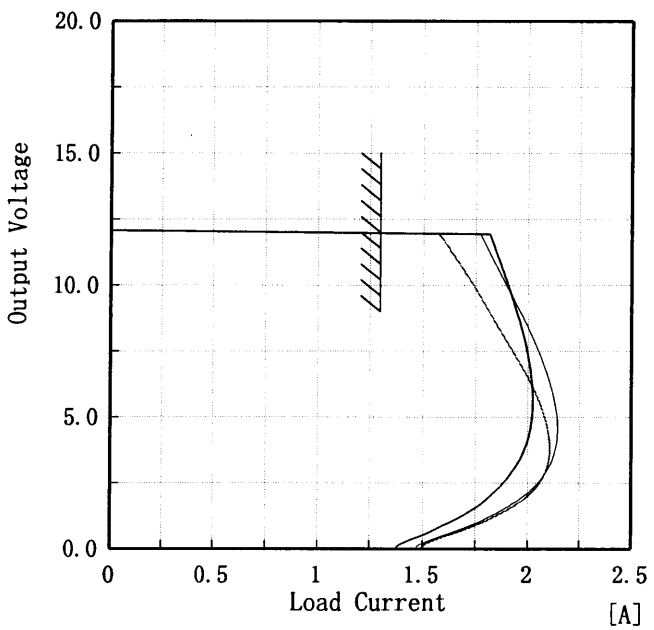
Model	LDA15F-12
Item	Overcurrent Protection 過電流保護
Object	+12.0V1.3A

Temperature 25℃
Testing Circuitry Figure A

1. Graph

Input Volt. 85 V
Input Volt. 100 V
Input Volt. 132 V



Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Output Voltage [V]	Load Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
12.00	1.57	1.77	1.82
11.40	1.62	1.80	1.84
10.80	1.67	1.84	1.87
9.60	1.77	1.92	1.92
8.40	1.86	2.00	1.97
7.20	1.95	2.06	2.00
6.00	2.03	2.12	2.02
4.80	2.09	2.15	2.02
3.60	2.11	2.13	1.98
2.40	2.05	2.04	1.88
1.20	1.86	1.84	1.69
0.00	1.50	1.47	1.38

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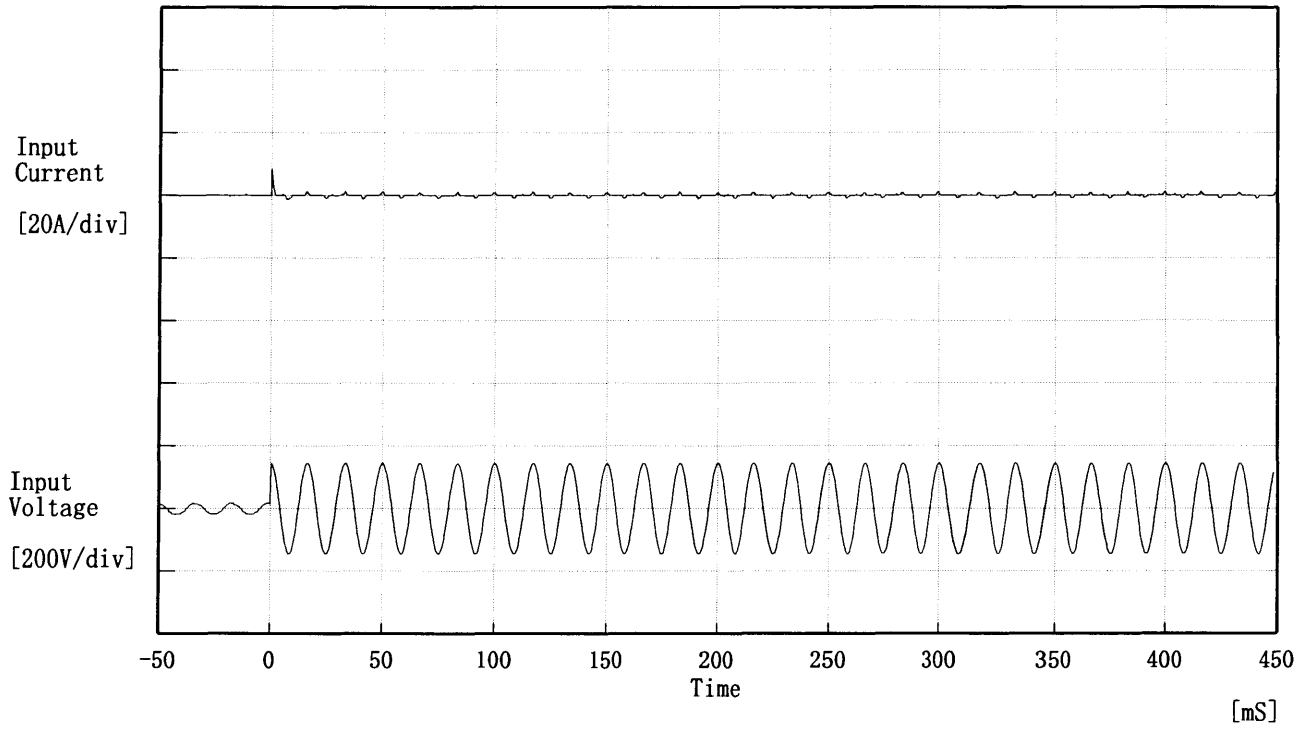
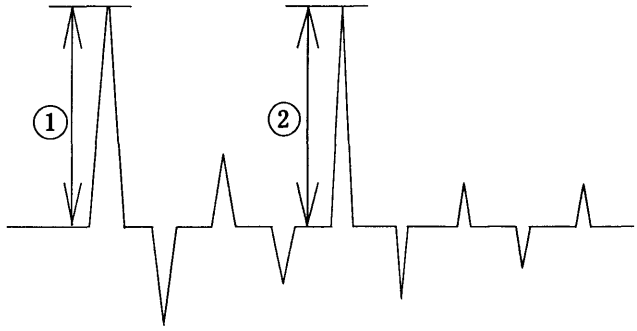
Model	LDA15F-12	
Item	Inrush Current 突入電流	Temperature 25℃ Testing Circuitry Figure A
Object	――――――	



Input Voltage 100 V
Frequency 60 Hz
Load 100 %
Inrush Current
① 8.42 [A]
② 1.22 [A]



COSEL

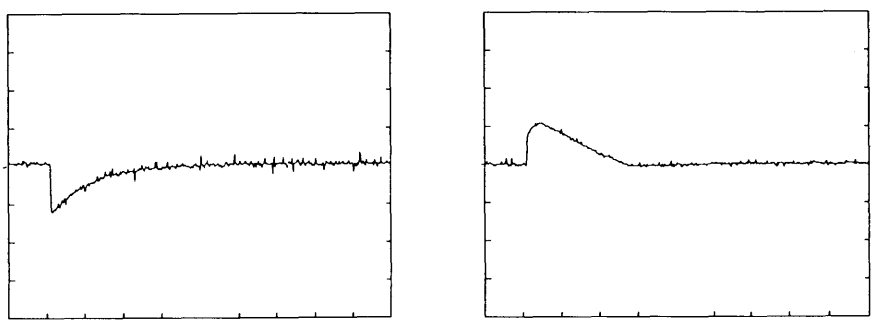
Model	LDA15F-12
Item	Dynamic Load Responce 動的負荷変動
Object	+12.0V1.3A

Temperature 25℃
Testing Circuitry Figure A

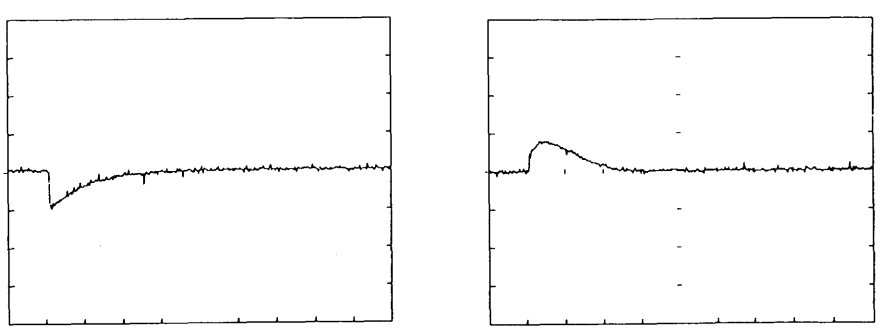
Input Volt. 100 V
Cycle 1000 mS

Load Current

Load 0% ←→
Load 100 %



Load 0% ←→
Load 50 %



200 mV/div

10 mS/div

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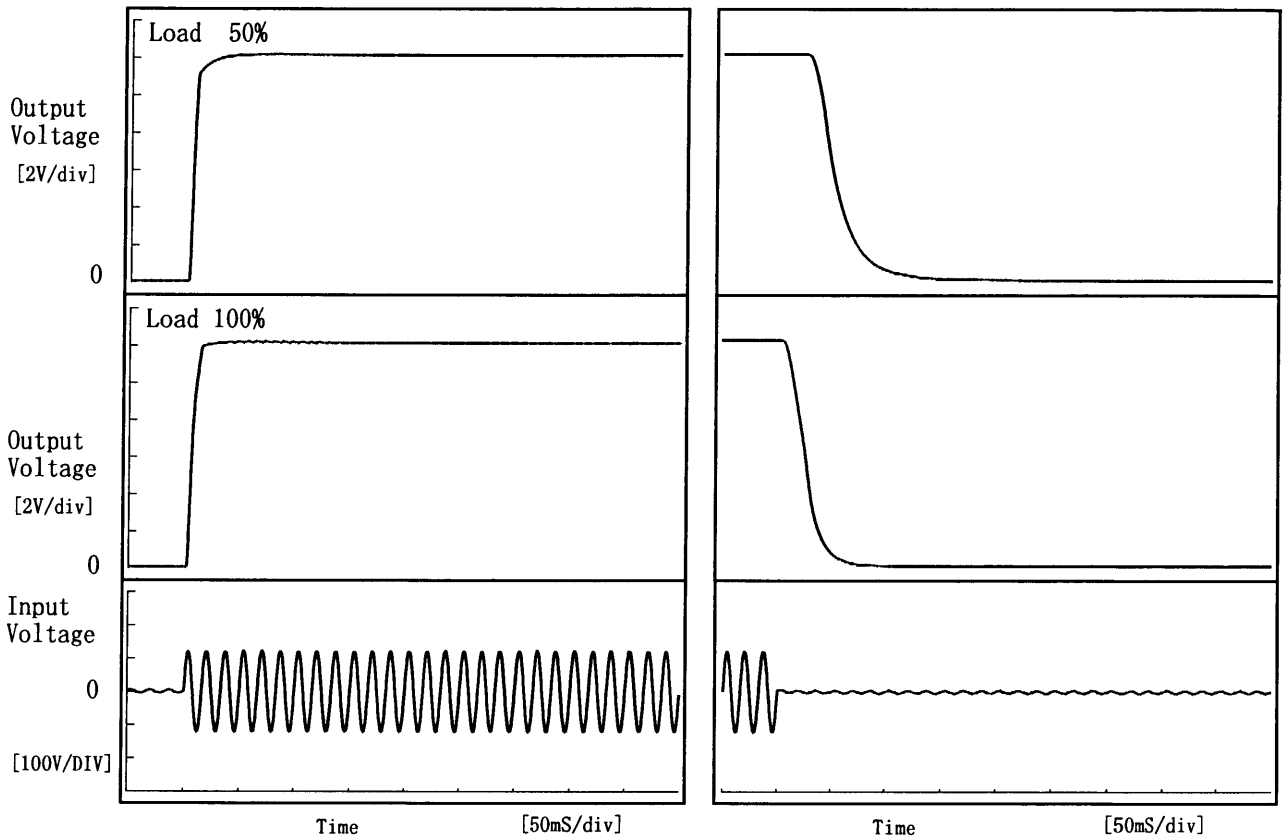
Model	LDA15F-12		
Item	Rise and Fall Time 立上り、立下り時間	Temperature	25℃
		Testing Circuitry	Figure A
Object	+12.0V1.3A		

1. Graph

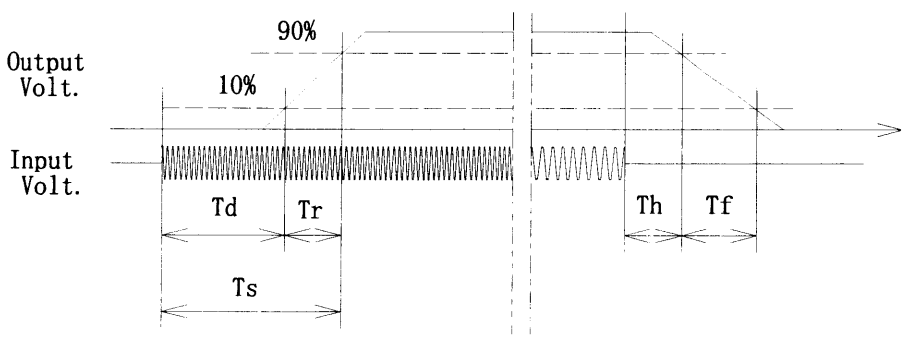
Input Volt. 85 V



2. Values

[mS]

Load \ Time	T d	T r	T s	T h	T f
50 %	2.8	7.3	10.0	38.0	46.5
100 %	2.8	10.3	13.0	14.3	31.5



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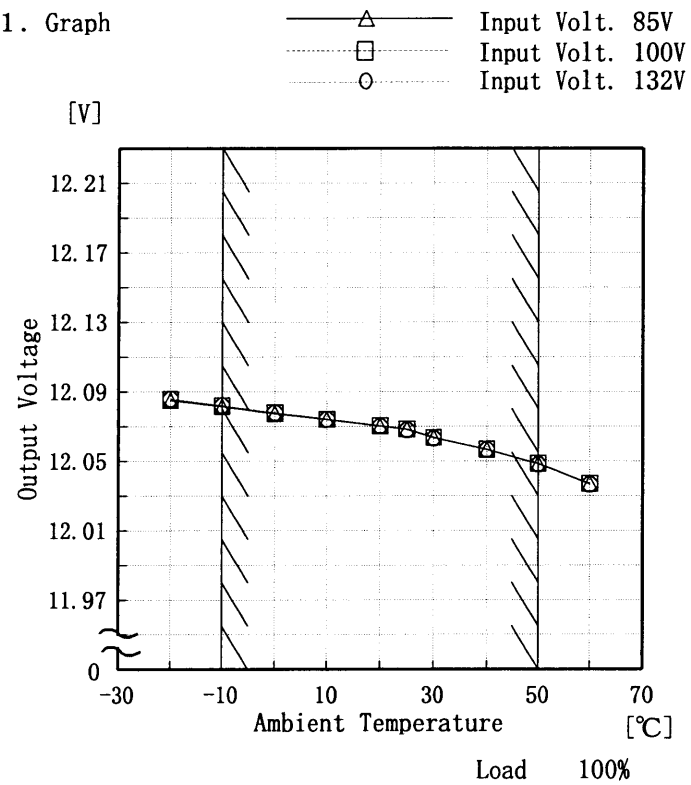
Model	LDA15F-12
Item	Ambient Temperature Drift 周囲温度変動
Object	+12.0V1.3A

Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

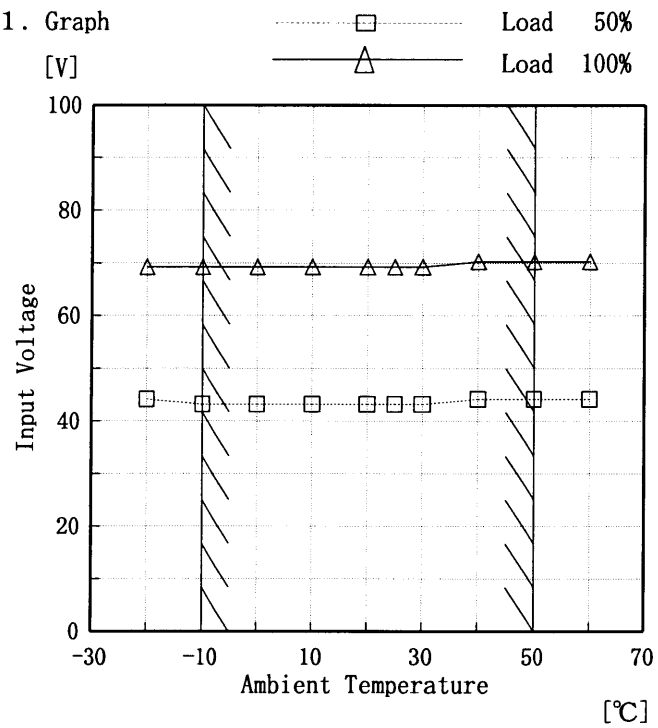
Temperature [℃]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
-20	12.085	12.086	12.086
-10	12.081	12.082	12.082
0	12.077	12.078	12.078
10	12.074	12.074	12.074
20	12.070	12.070	12.070
25	12.068	12.068	12.068
30	12.064	12.064	12.063
40	12.057	12.057	12.057
50	12.049	12.049	12.048
60	12.037	12.037	12.036
—	—	—	—

COSEL

Model	LDA15F-12
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+12.0V1.3A

Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

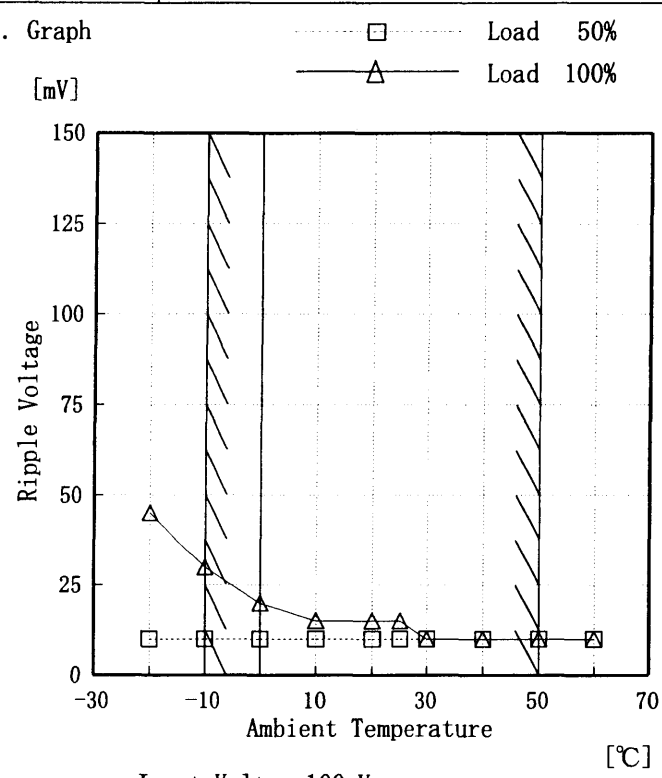
Ambient Temperature [℃]	Input Voltage [V]	
	Load 50%	Load 100%
-20	44	69
-10	43	69
0	43	69
10	43	69
20	43	69
25	43	69
30	43	69
40	44	70
50	44	70
60	44	70
—	—	—

COSEL

Model	LDA15F-12
Item	Ripple Voltage (by Ambient Temp.) リップル電圧（周囲温度特性）
Object	+12.0V1.3A

Testing Circuitry Figure A

1. Graph



Input Volt. 100 V

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

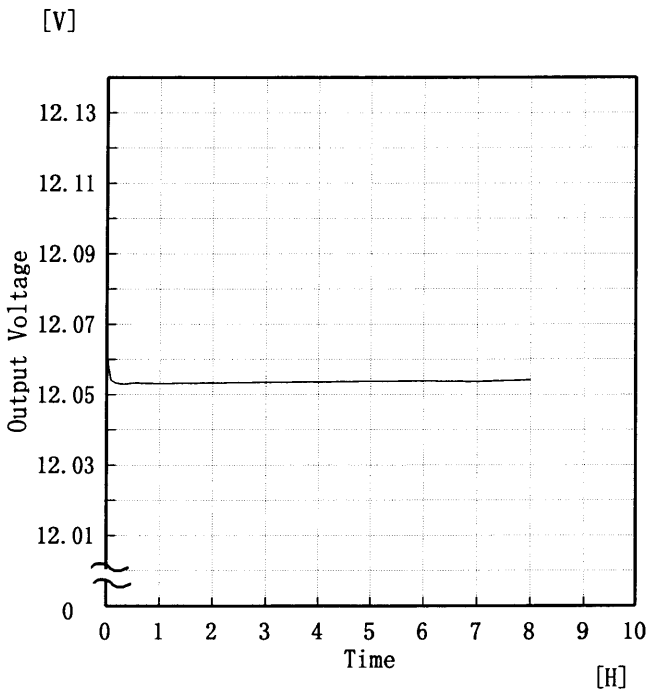
Ambient Temp. [℃]	Load 50% Ripple Output Volt. [mV]	Load 100% Ripple Output Volt. [mV]
-20	10	45
-10	10	30
0	10	20
10	10	15
20	10	15
25	10	15
30	10	10
40	10	10
50	10	10
60	10	10
—	—	—

COSEL

Model	LDA15F-12
Item	Time Lapse Drift 経時ドリフト
Object	+12.0V1.3A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Input Volt. 100V
Load 100%

2. Values

Time since start [H]	Output Voltage [V]
0.0	12.068
0.5	12.053
1.0	12.053
2.0	12.053
3.0	12.053
4.0	12.054
5.0	12.054
6.0	12.054
7.0	12.054
8.0	12.054

COSEL

Model	LDA15F-12	
Item	Output Voltage Accuracy　定電圧精度	Testing Circuitry　Figure A
Object	+12.0V1.3A	

Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature　-10～50 ℃
Input Voltage :　85～132 V
Load Current :　0～1.3 A

* Output Voltage Accuracy = ±(Maximum of Output Voltage − Minimum of Output Voltage)／2

$$* \text{Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度　-10～50 ℃
入力電圧　85～132 V
負荷電流　0～1.3 A

* 定電圧精度(変動値) ＝ ±(出力電圧の最高値−出力電圧の最低値)／2

$$* \text{定電圧精度(変動率)} = \frac{\text{変動値}}{\text{定格出力電圧}} \times 100$$

Item	Temperature [℃]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy (Ration) [%]
Maximum Voltage	-10	132	0.0	12.088	±23	±0.2
Minimum Voltage	50	132	1.3	12.043		

COSEL

Model	LDA15F-12
Item	Condensation 結露特性
Object	+12.0V1.3A

Testing Circuitry Figure A

1. Condensation test

Testing procedure is as follows.

① Keeping and cooling the unit in a tank at -10℃ for an hour with the input off.

② Taking it out of the tank and dewing itself in a room where the temperature is 25℃ and the humidity is 40%RH.

③ Testing electrical characteristics of the unit to confirm there be no fault.

1．結露特性試験

入力を切った状態で、恒温槽で－１０℃に冷却しておき、約１時間後に恒温槽から取り出し、室温２５℃、湿度４０%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

2．Values

Item	Data	Testing Conditions
Output Voltage [V]	12.07	Input Volt.: 100V, Load Current:1.3A
Line Regulation [mV]	5	Input Volt.: 85～132V, Load Current:1.3A
Load Regulation [mV]	17	Input Volt.: 100V, Load Current:0～1.3A

COSEL

Model	LDA15F-12		
Item	Leakage Current 漏洩電流	Temperature Testing Circuitry	25℃ Figure B
Object	————————		

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DENTORI	0.12	0.16	0.24
(B) IEC60950	0.12	0.16	0.25

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B) IEC60950	—	—	—

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力の両相について測定し、その大きい方を漏洩電流測定値とする。

COSEL

Model	LDA15F-12		
Item	Line Noise Tolerance 入力雑音耐量	Temperature Testing Circuitry	25℃ Figure C
Object	+12.0V1.3A		

1. Results

Pulse Width [nS]	MODE	No protection failure should occur 保護回路の誤動作がない	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation
1000	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation

2. Conditions

Input Voltage : 100 V
Pulse Voltage : 2000 V
Pulse Cycle : 10 mS
Pulse Input Duration : 1 min. or more
Load : 100 %

COSEL

Model	LDA15F-12		
Item	Conducted Emission 雑音端子電圧	Temperature Testing Circuitry	25℃ Figure D
Object	――――――		

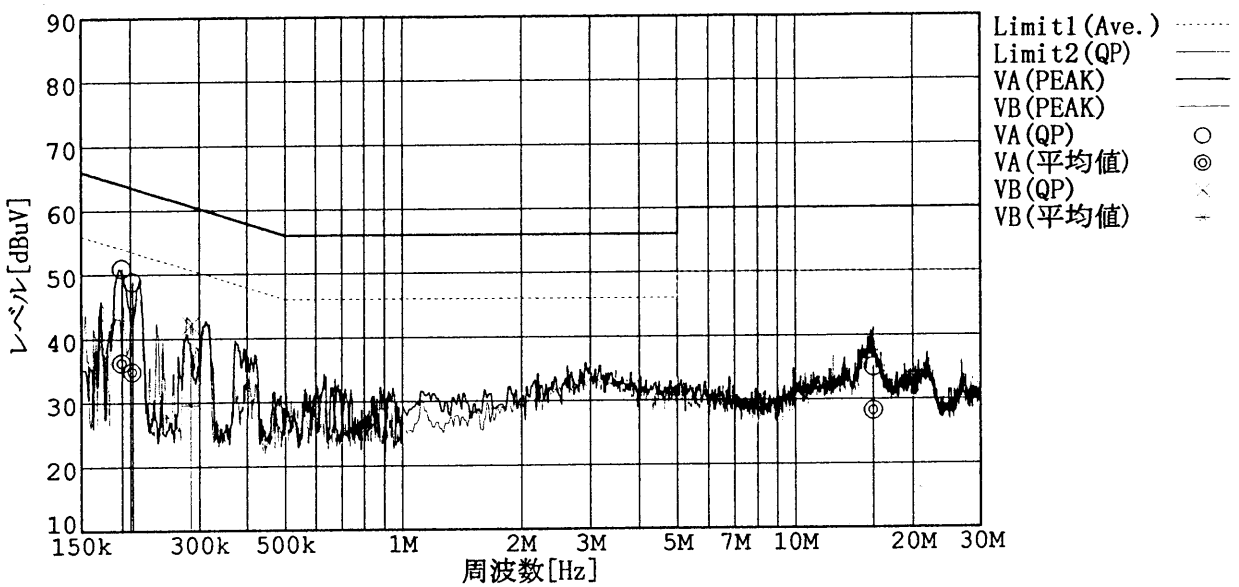
1. Graph

Remarks

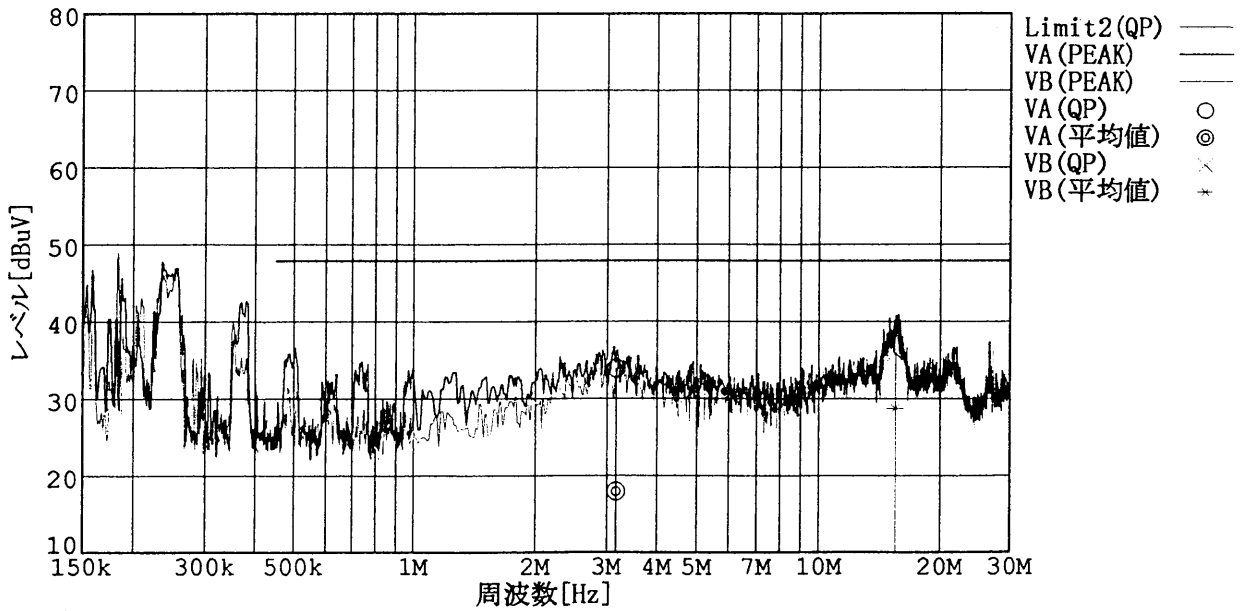
Input Volt. 100 V (VCCI Class B)
120 V (FCC Class B)

Load 100 %

規格1: [VCCI] Class B(平均値)
規格2: [VCCI] Class B(QP)



規格2: [FCC Part15] Class B



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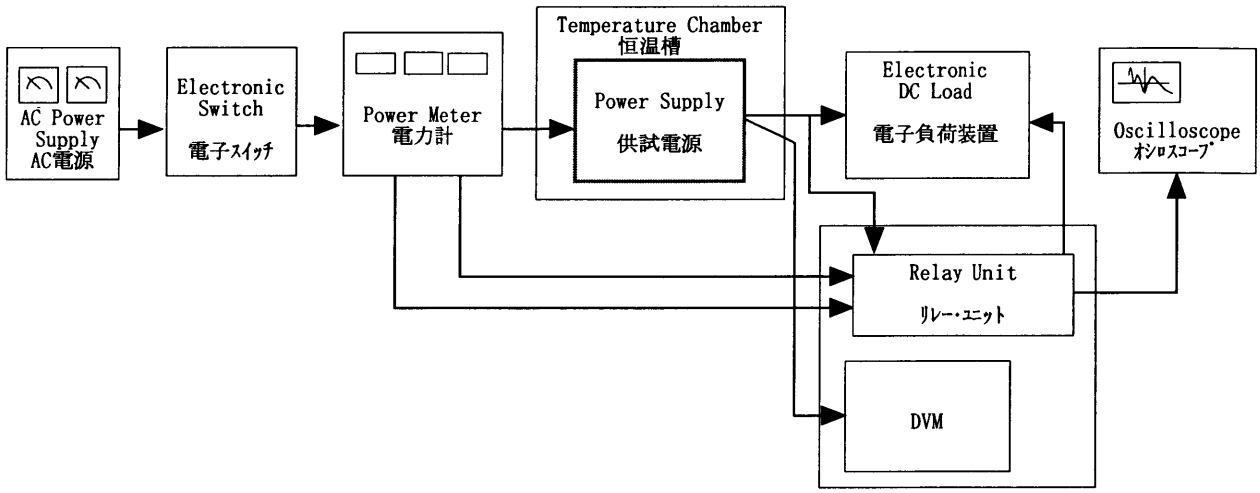

Figure A

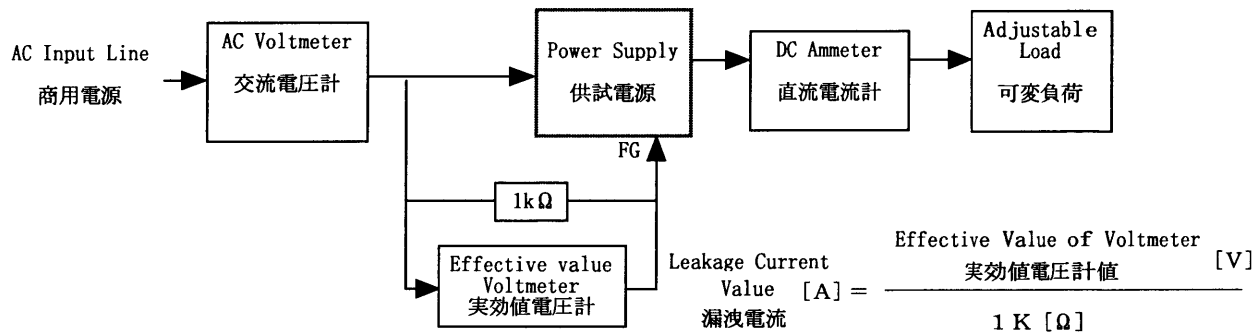

Figure B(DENTORI)

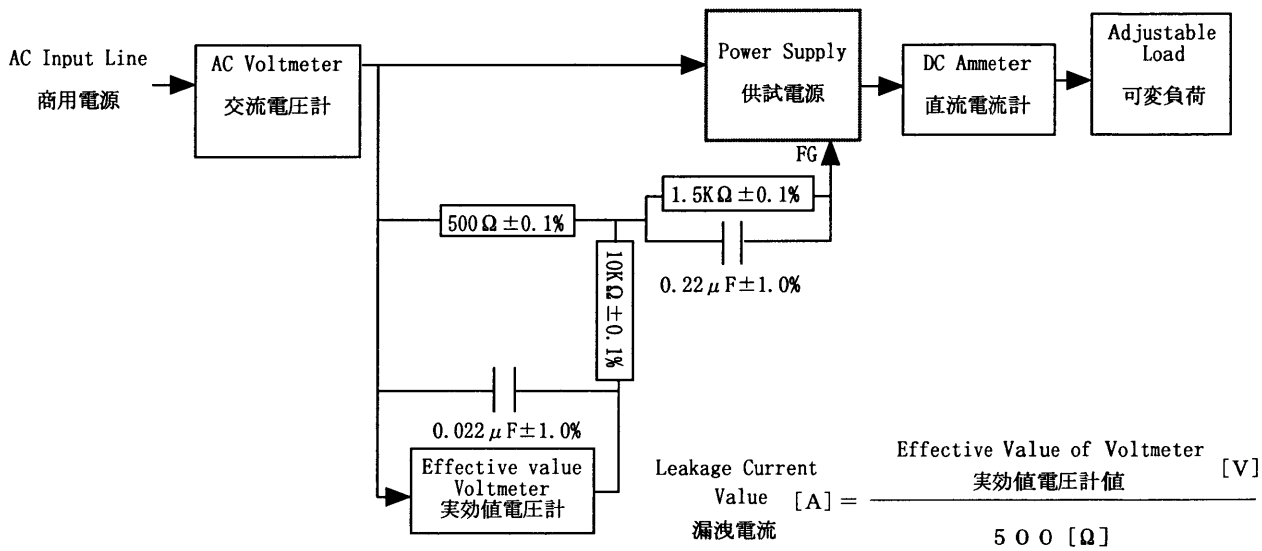

Figure B (IEC60950)

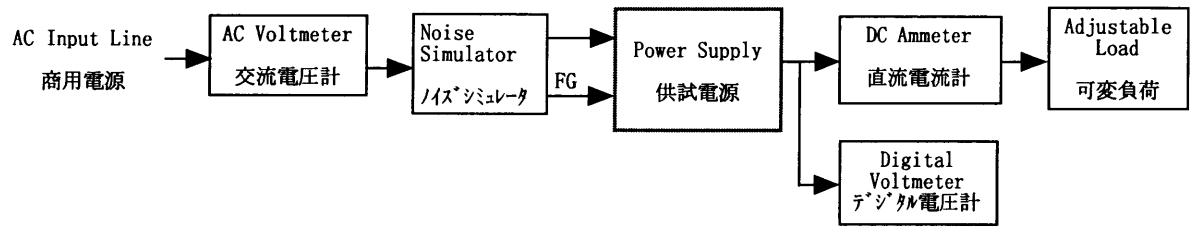

Figure C

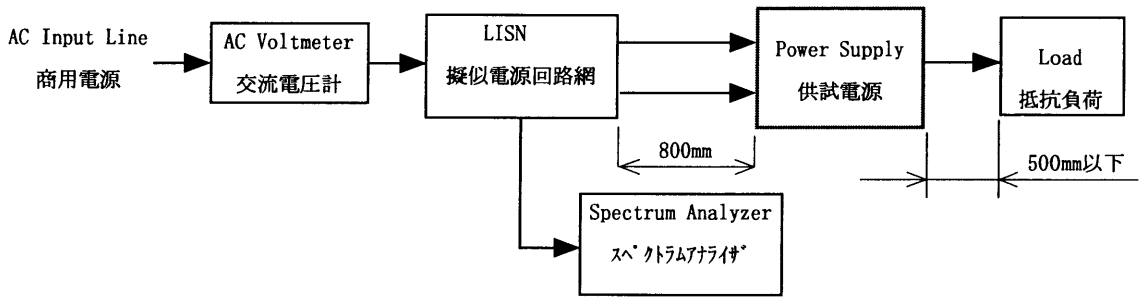

Figure D

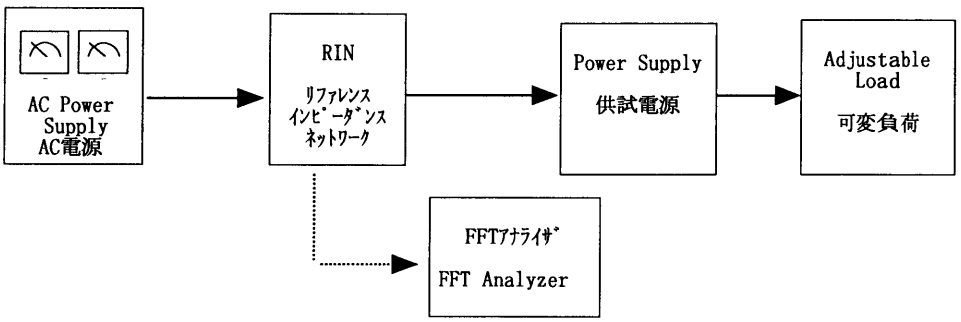

Figure E