

COSEL

TEST DATA OF LDA10F-5
(100V INPUT)

Regulated DC Power Supply

Date : June 18. 1999

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Design Manager

Prepared by : S. Ashihara
Design Engineer

コーワセル株式会社

COSEL CO., LTD.



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Model	LDA10F-5		Temperature Testing Circuitry 25°C Figure A																															
Item	Line Regulation 靜的入力変動																																	
Object	+5.0V2A																																	
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Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

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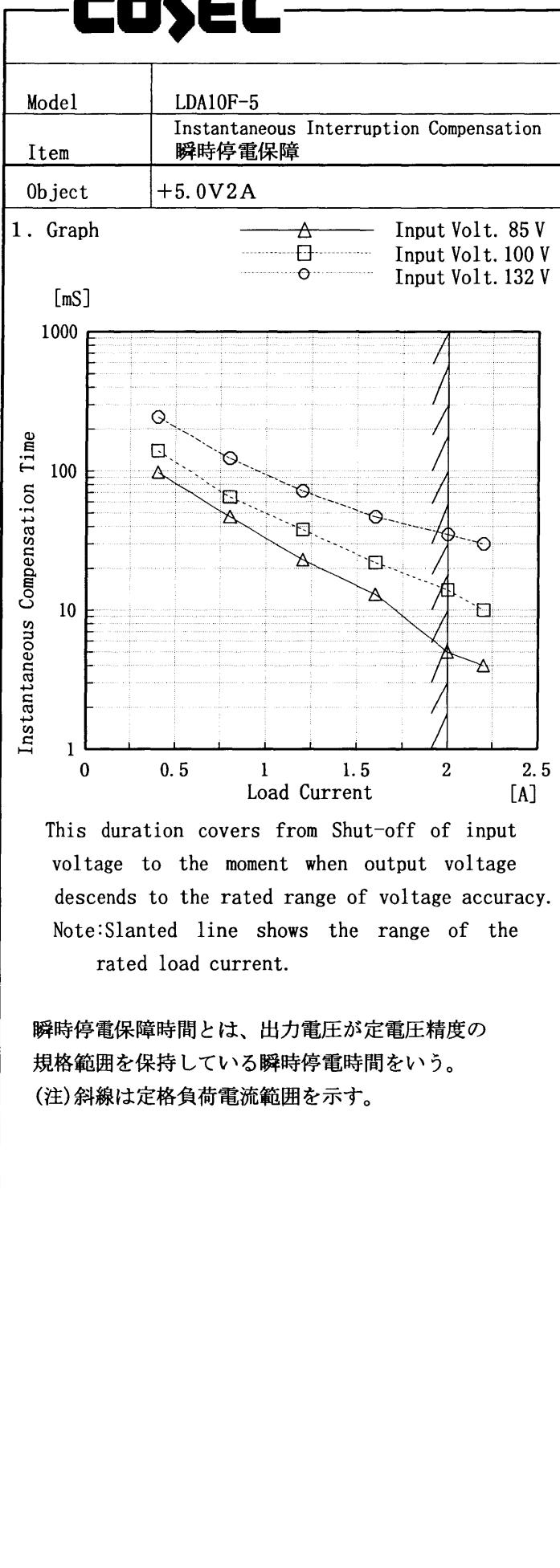
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<p>The graph plots Hold-Up Time [mS] on a logarithmic y-axis (1 to 1000) against Input Voltage [V] on the x-axis (0 to 150). A slanted line marks the rated input voltage range from approximately 85V to 140V. Data points are shown for Load 50% (squares) and Load 100% (triangles).</p> <table border="1"> <thead> <tr> <th>Input Voltage [V]</th> <th>Hold-Up Time [mS] (Load 50%)</th> <th>Hold-Up Time [mS] (Load 100%)</th> </tr> </thead> <tbody> <tr><td>85</td><td>~30</td><td>~10</td></tr> <tr><td>90</td><td>~40</td><td>~20</td></tr> <tr><td>100</td><td>~60</td><td>~30</td></tr> <tr><td>110</td><td>~70</td><td>~40</td></tr> <tr><td>120</td><td>~80</td><td>~50</td></tr> <tr><td>130</td><td>~90</td><td>~60</td></tr> <tr><td>140</td><td>~100</td><td>~70</td></tr> </tbody> </table>		Input Voltage [V]	Hold-Up Time [mS] (Load 50%)	Hold-Up Time [mS] (Load 100%)	85	~30	~10	90	~40	~20	100	~60	~30	110	~70	~40	120	~80	~50	130	~90	~60	140	~100	~70								
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<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.</p> <p>Note: Slanted line shows the range of the rated input voltage.</p> <p>出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。</p> <p>(注)斜線は定格入力電圧範囲を示す。</p>																																	

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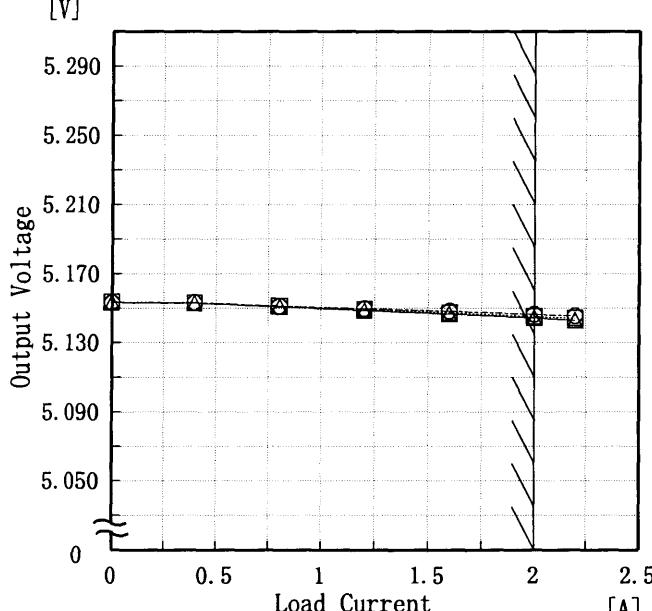
Note: Slanted line shows the range of the rated load current.

瞬時停電保障時間とは、出力電圧が定電圧精度の規格範囲を保持している瞬時停電時間をいう。

(注) 斜線は定格負荷電流範囲を示す。

Temperature 25°C
Testing Circuitry Figure A

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Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

COSEL

Model	LDA10F-5																																							
Item	Ripple Voltage(by Load Current) リップル電圧(負荷電流特性)	Temperature Testing Circuitry 25°C Figure A																																						
Object	+5.0V2A																																							
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COSEL

Model	LDA10F-5	Temperature Testing Circuitry	25°C Figure A																																						
Item	Ripple-Noise リップルノイズ																																								
Object	+5.0V2A																																								
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- T1: Due to AC Input Line
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- 入力商用周期
スイッチング周期

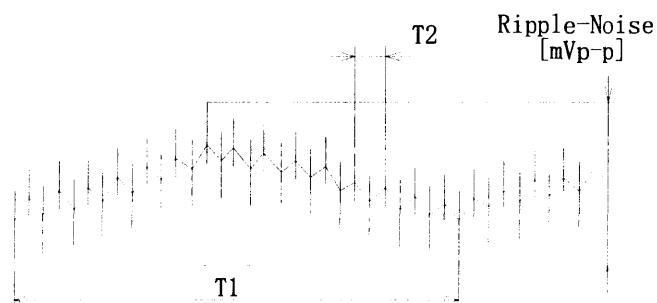


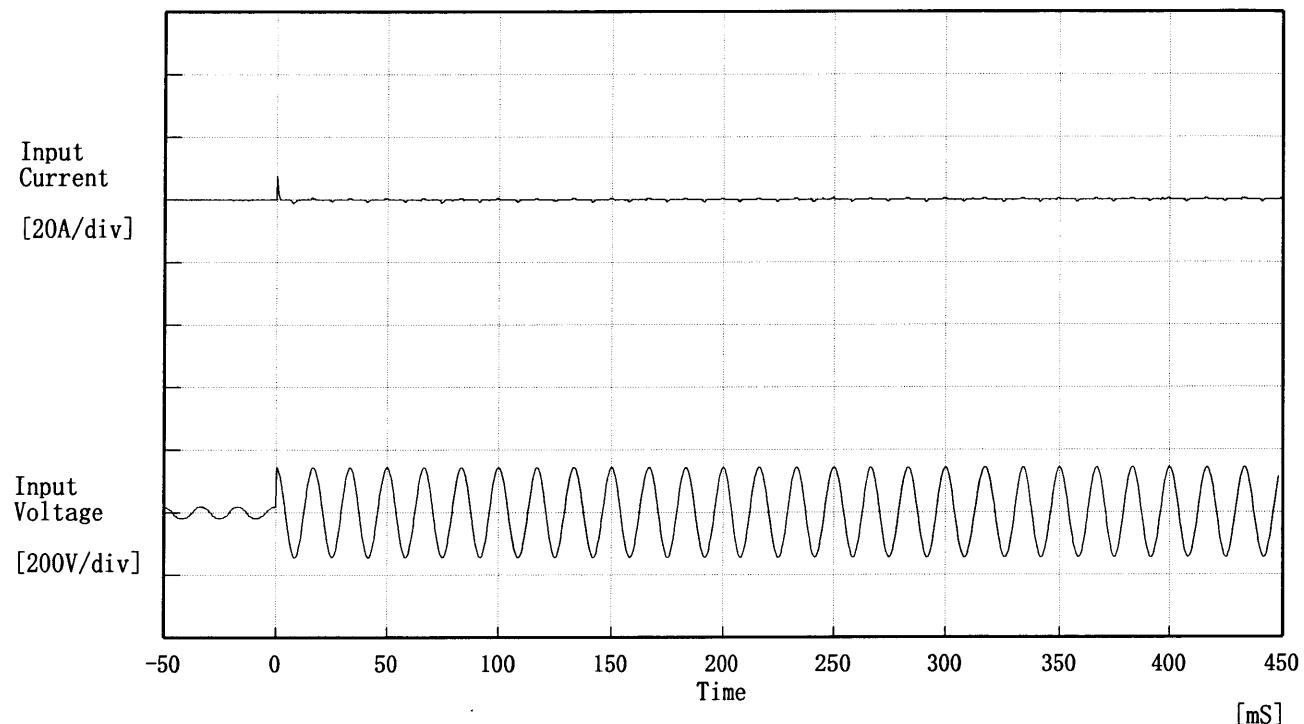
Fig. Complex Ripple Wave Form
図 リップル波形詳細図

COSEL

Model	LDA10F-5		Temperature Testing Circuitry 25°C Figure A																																																								
Item	Overcurrent Protection 過電流保護																																																										
Object	+5.0V2A																																																										
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COSEL

Model	LDA10F-5	Temperature Testing Circuitry Figure A	25°C
Item	Inrush Current 突入電流		
Object	_____		



Input Voltage 100 V

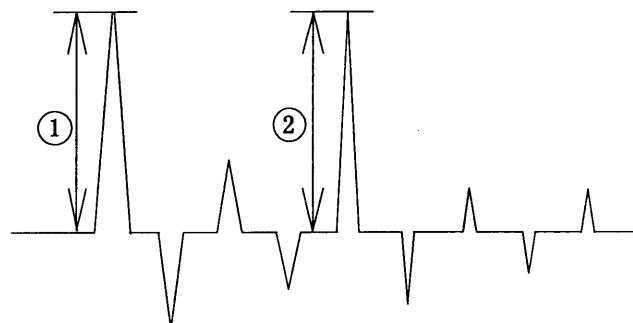
Frequency 60 Hz

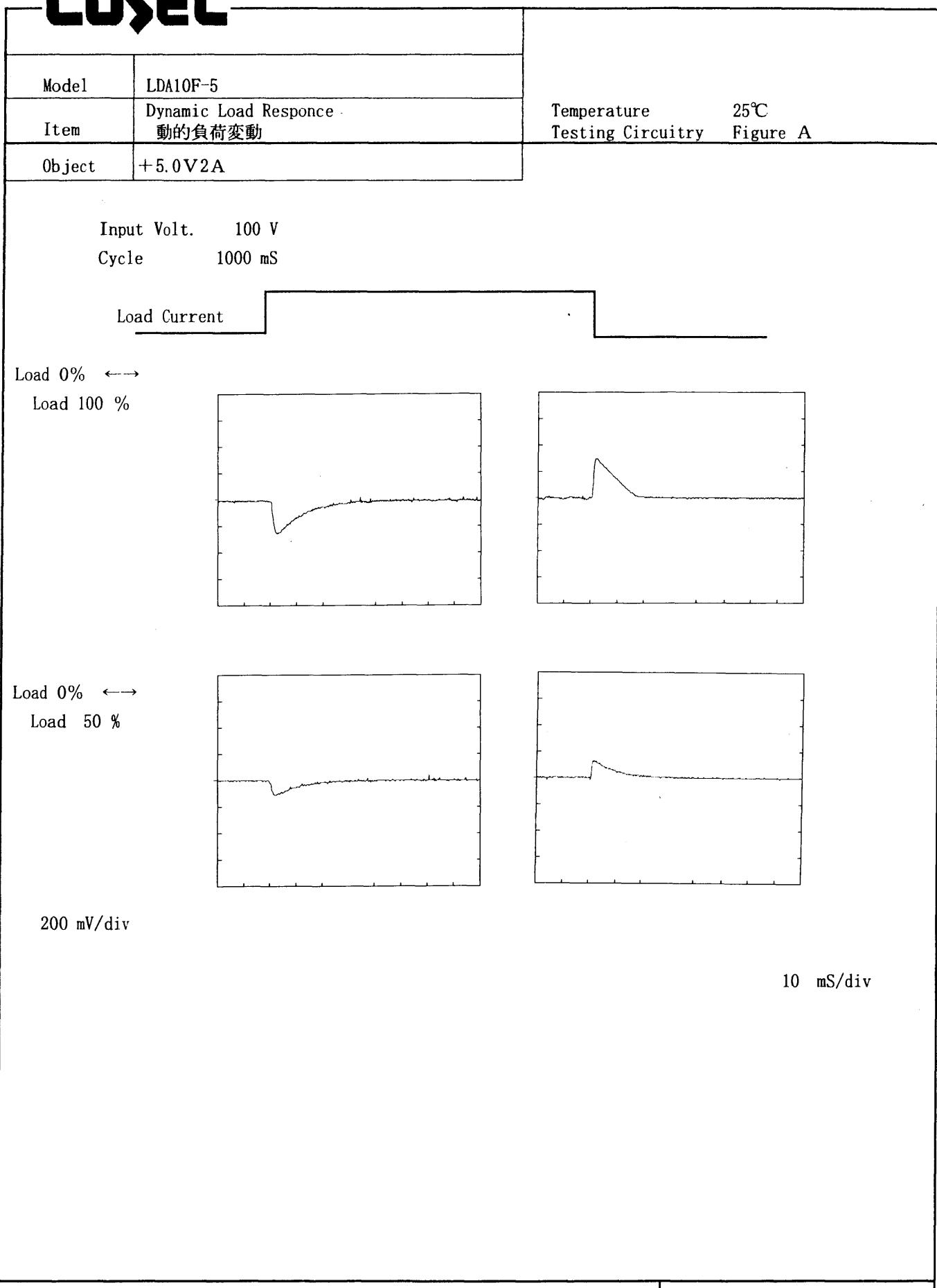
Load 100 %

Inrush Current

① 7.61 [A]

② 1.19 [A]



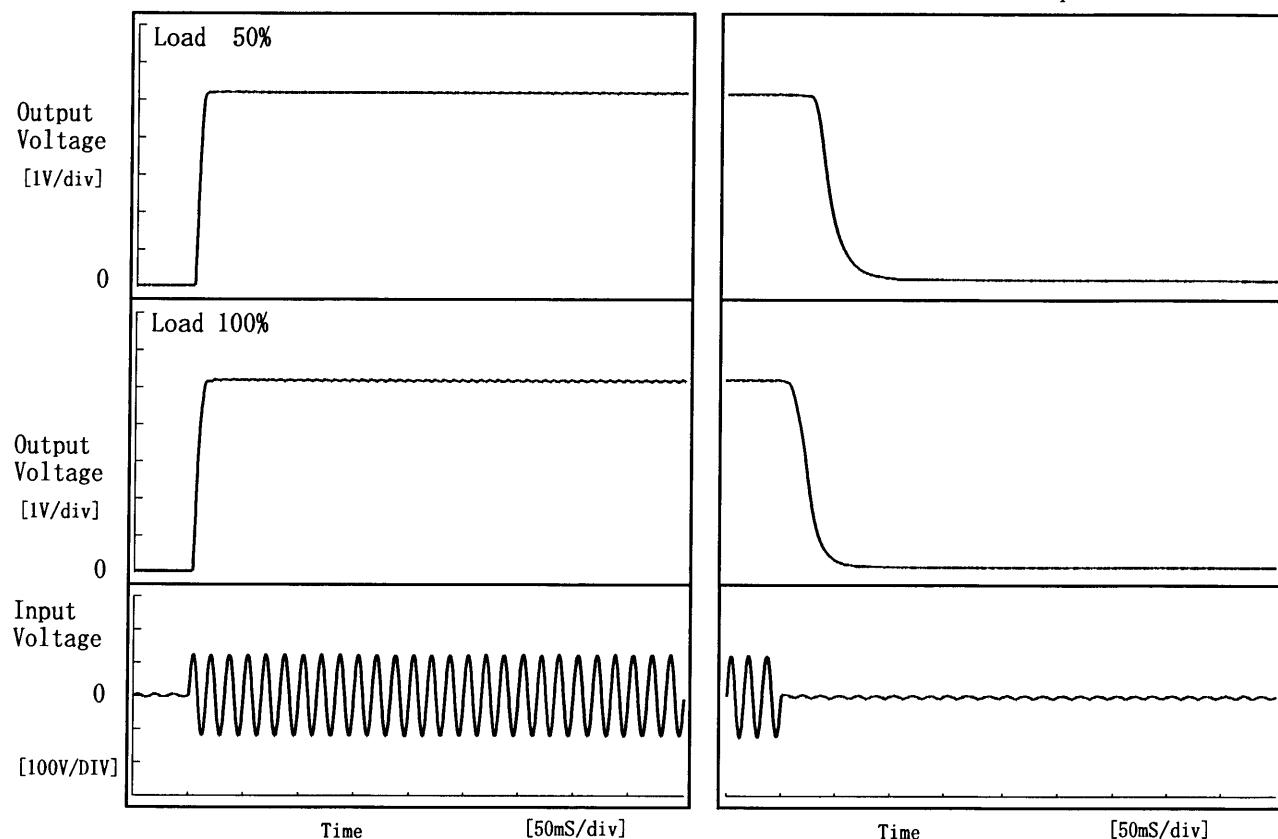
COSEL

COSEL

Model	LDA10F-5
Item	Rise and Fall Time 立上り、立下り時間
Object	+5.0V2A

Temperature 25°C
Testing Circuitry Figure A

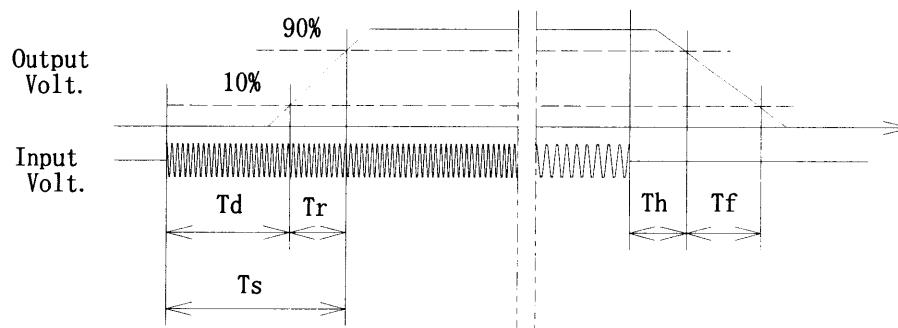
1. Graph



2. Values

[mS]

Load	Time	T d	T r	T s	T h	T f
50 %		3.5	6.5	10.0	34.3	36.3
100 %		3.5	8.0	11.5	14.5	29.5



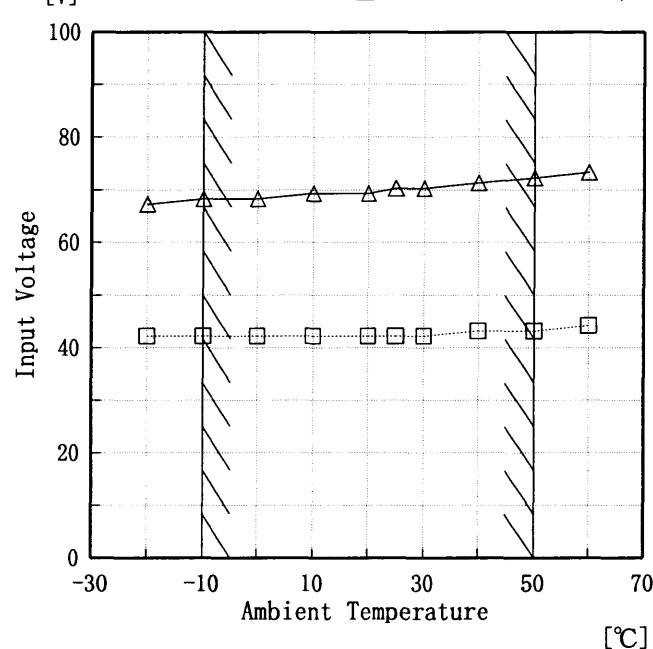
COSEL

Model	LDA10F-5	Testing Circuitry Figure A																																																					
Item	Ambient Temperature Drift 周囲温度変動																																																						
Object	+5.0V2A																																																						
1. Graph	<p style="text-align: center;"> △ Input Volt. 85V □ Input Volt. 100V ○ Input Volt. 132V </p> <p style="text-align: center;">Output Voltage [V]</p> <p style="text-align: center;">Ambient Temperature [°C]</p> <p style="text-align: center;">Load 100%</p>	2. Values																																																					
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(注) 斜線は定格周囲温度範囲を示す。

COSEL

Model	LDA10F-5		
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧	Testing Circuitry	Figure A
Object	+5.0V2A		
1. Graph		2. Values	
[V]		Ambient Temperature [°C]	Input Voltage [V]
			Load 50% Load 100%
100		-20	42 67
80		-10	42 68
60		0	42 68
40		10	42 69
20		20	42 69
0		25	42 70
		30	42 70
		40	43 71
		50	43 72
		60	44 73
		—	— —

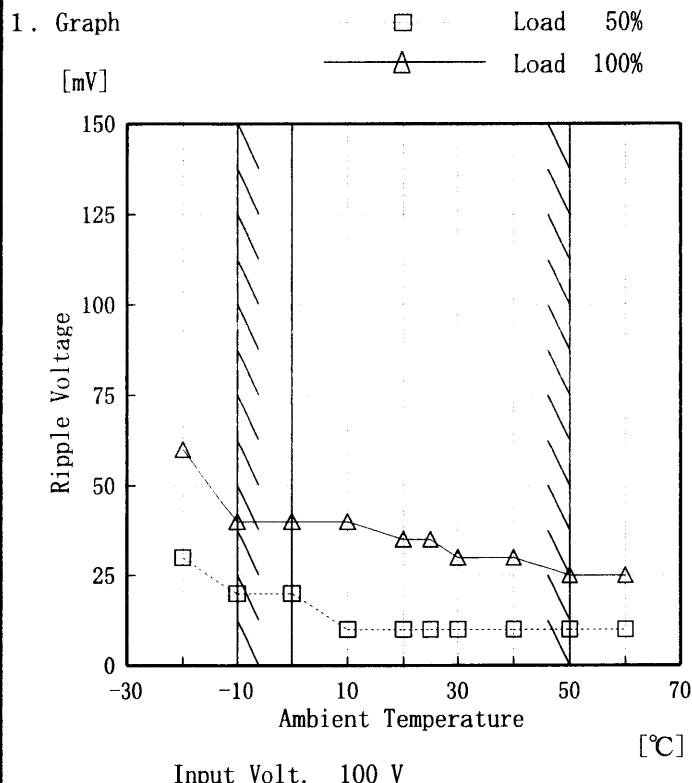


Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

COSEL

Model	LDA10F-5
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)
Object	+5.0V 2A



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

Testing Circuitry Figure A

2. Values

Ambient Temp. [°C]	Load 50%	Load 100%
	Ripple Output Volt. [mV]	Ripple Output Volt. [mV]
-20	30	60
-10	20	40
0	20	40
10	10	40
20	10	35
25	10	35
30	10	30
40	10	30
50	10	25
60	10	25
—	—	—

COSEL

Model	LDA10F-5	Temperature Testing Circuitry 25°C Figure A																					
Item	Time Lapse Drift 経時ドリフト																						
Object	+5.0V2A																						
1. Graph		2. Values																					
<p>[V]</p> <table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>5.149</td></tr> <tr><td>0.5</td><td>5.146</td></tr> <tr><td>1.0</td><td>5.146</td></tr> <tr><td>2.0</td><td>5.147</td></tr> <tr><td>3.0</td><td>5.146</td></tr> <tr><td>4.0</td><td>5.146</td></tr> <tr><td>5.0</td><td>5.146</td></tr> <tr><td>6.0</td><td>5.146</td></tr> <tr><td>7.0</td><td>5.147</td></tr> <tr><td>8.0</td><td>5.147</td></tr> </tbody> </table>		Time since start [H]	Output Voltage [V]	0.0	5.149	0.5	5.146	1.0	5.146	2.0	5.147	3.0	5.146	4.0	5.146	5.0	5.146	6.0	5.146	7.0	5.147	8.0	5.147
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7.0	5.147																						
8.0	5.147																						
<p>Output Voltage [V]</p> <p>Input Volt. 100V</p> <p>Load 100%</p>																							



Model	LDA10F-5	Testing Circuitry Figure A
Item	Output Voltage Accuracy 定電圧精度	
Object	+5.0V2A	

Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature -10~50 °C

Input Voltage : 85~132 V

Load Current : 0~2 A

* Output Voltage Accuracy = ±(Maximum of Output Voltage - Minimum of Output Voltage) / 2

$$* \text{Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 -10~50 °C

入力電圧 85~132 V

負荷電流 0~2 A

* 定電圧精度(変動値) = ±(出力電圧の最高値-出力電圧の最低値) / 2

$$* \text{定電圧精度(変動率)} = \frac{\text{変動値}}{\text{定格出力電圧}} \times 100$$

Item	Temperature [°C]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy(Ration) [%]
Maximum Voltage	50	132	0	5.155	±7	±0.2
Minimum Voltage	-10	85	2	5.142		



Model	LDA10F-5		
Item	Condensation 結露特性	Testing Circuitry	Figure A
Object	+5.0V 2A		

1. Condensation test

Testing procedure is as follows.

- ① Keeping and cooling the unit in a tank at -10°C for an hour with the input off.
- ② Taking it out of the tank and dewing itself in a room where the temperature is 25°C and the humidity is 40%RH.
- ③ Testing electrical characteristics of the unit to confirm there be no fault.

1. 結露特性試験

入力を切った状態で、恒温槽で-10°Cに冷却しておき、約1時間後に恒温槽から取り出し、室温25°C、湿度40%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

2. Values

Item	Data	Testing Conditions
Output Voltage [V]	5.144	Input Volt.: 100V, Load Current:2A
Line Regulation [mV]	8	Input Volt.: 85~132V, Load Current:2A
Load Regulation [mV]	10	Input Volt.: 100V, Load Current:0~2A



Model	LDA10F-5	Temperature	25°C
Item	Leakage Current 漏洩電流	Testing Circuitry	Figure B
Object	—	—	—

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DENTORI	0.11	0.14	0.17
(B) IEC60950	0.12	0.14	0.18

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力の両相について測定し、その大きい方を漏洩電流測定値とする。

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B) IEC60950	—	—	—



Model	LDA10F-5	Temperature Testing Circuitry	25°C Figure C
Item	Line Noise Tolerance 入力雑音耐量		
Object	+5.0V 2A		

1. Results

Pulse Width [nS]	MODE	No protection failure should occur 保護回路の誤動作がない	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation
1000	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation

2. Conditions

Input Voltage : 100 V
 Pulse Voltage : 2000 V
 Pulse Cycle : 10 mS
 Pulse Input Duration : 1 min. or more
 Load : 100 %

COSSEL

Model	LDA10F-5	Temperature Testing Circuitry	25°C Figure D
Item	Conducted Emission 雜音端子電圧		
Object	<hr/>		

1. Graph

Remarks

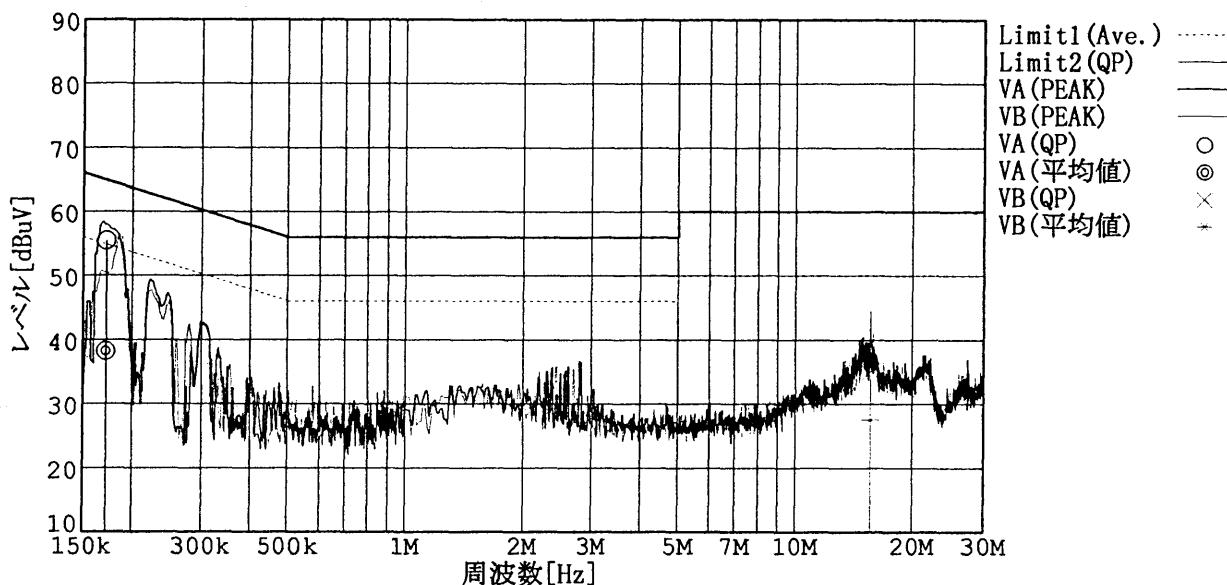
Input Volt. 100 V (VCCI Class B)

120 V (FCC Class B)

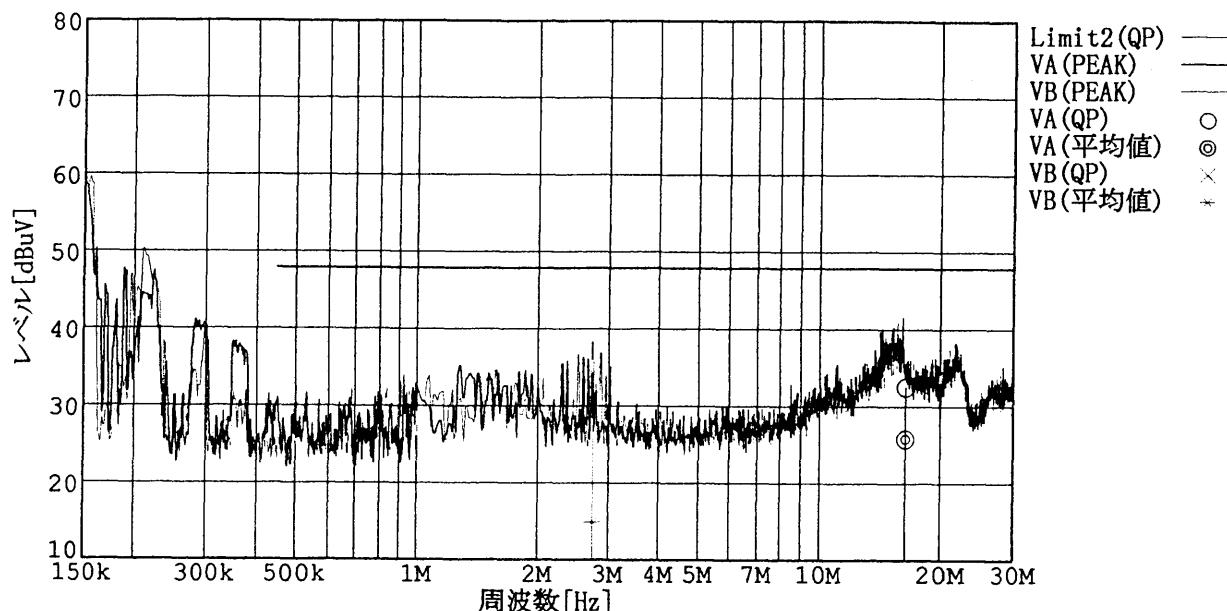
Load 100 %

規格 1: [VCCI] Class B(平均値)

規格 2: [VCCI] Class B(QP)



規格 2: [FCC Part15] Class B



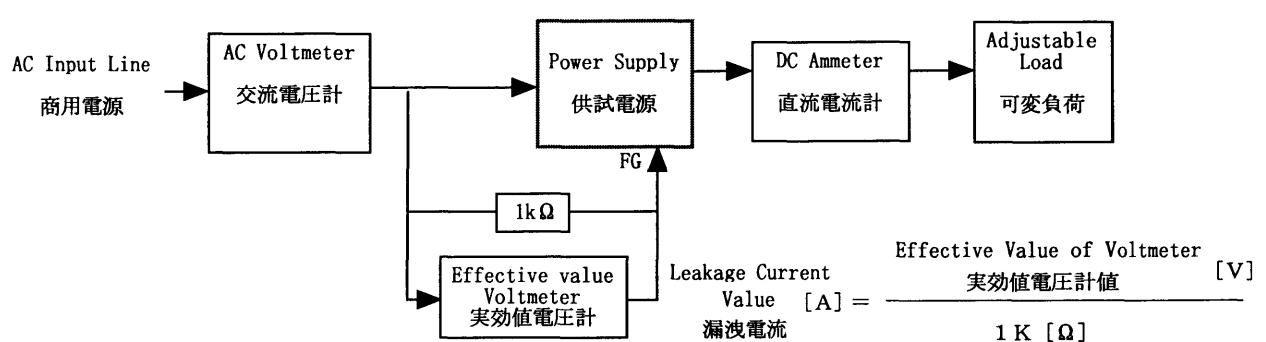
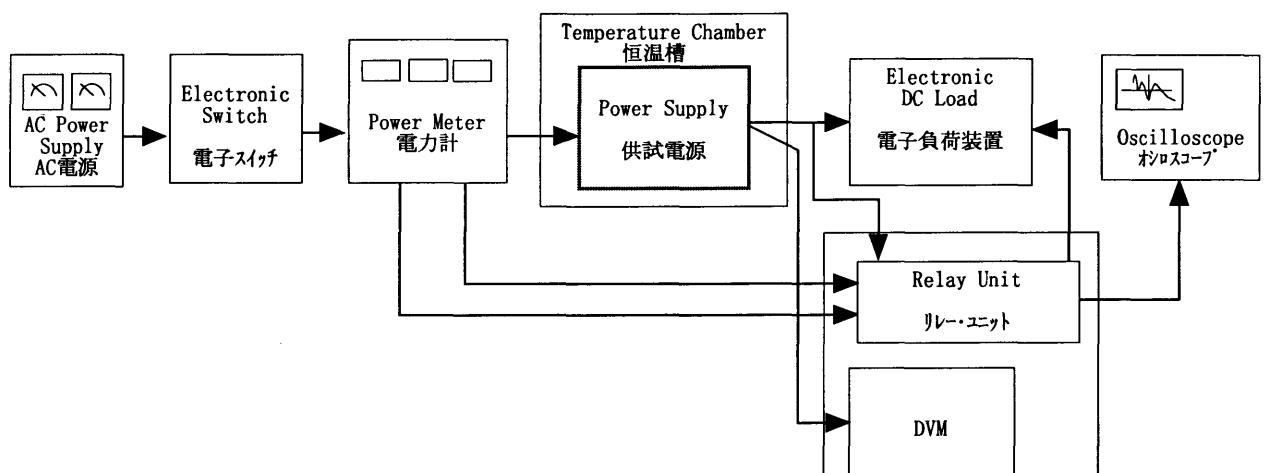


Figure B (DENTORI)

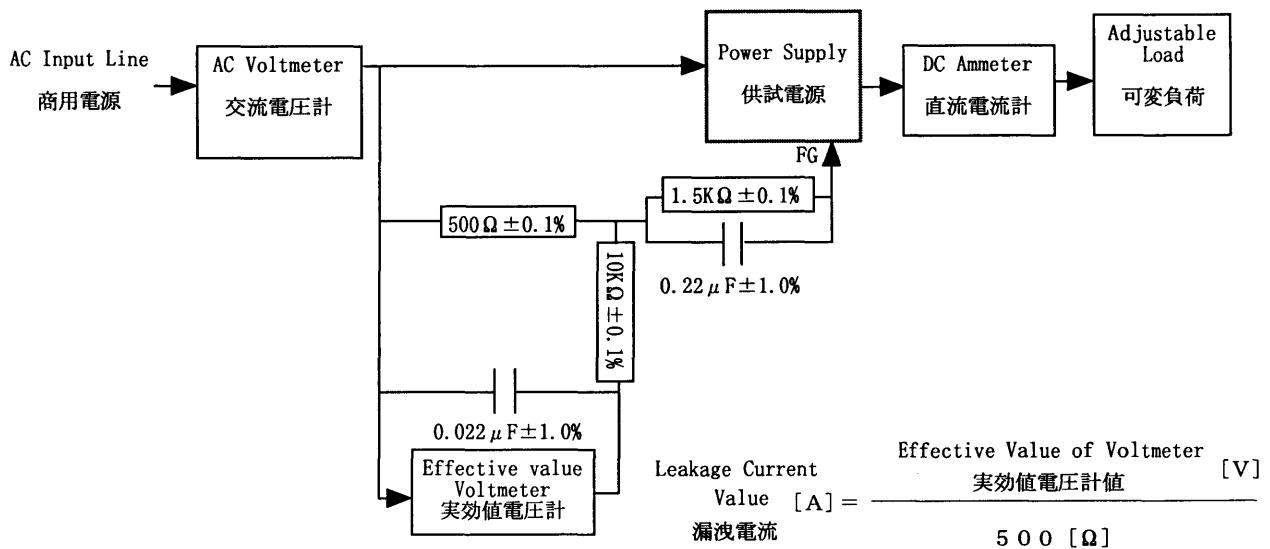


Figure B (IEC 60950)

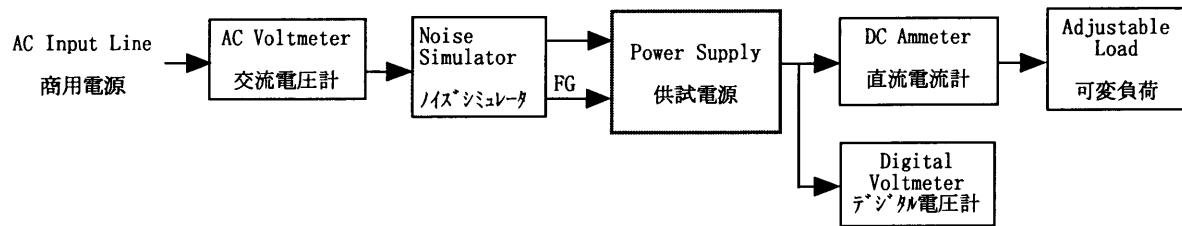


Figure C

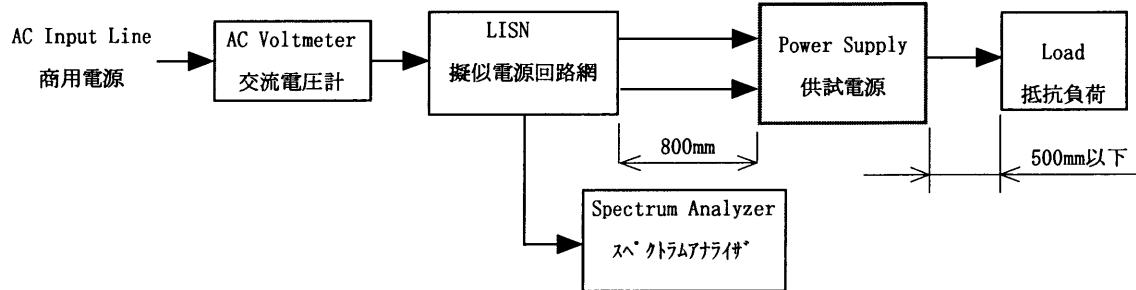


Figure D

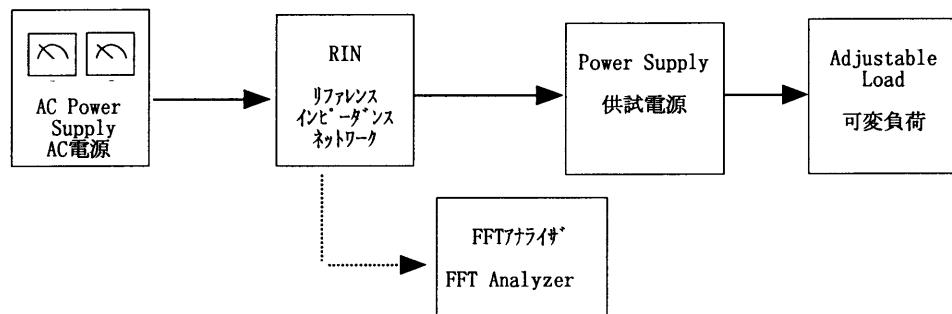


Figure E