



TEST DATA OF LCA75S-36
(100V INPUT)

Regulated DC Power Supply

Apr. 10, 2000

Approved by : K. Yamaguchi
Design Manager

Prepared by : J. Asano
Design Engineer

コーワセル株式会社
COSEL CO., LTD.



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Model	LCA75S-36
Item	Line Regulation 静的入力変動
Object	+36.0V 2.1A
1. Graph	
<p>Note: Slanted line shows the range of the rated input voltage.</p> <p>(注)斜線は定格入力電圧範囲を示す。</p>	

 Temperature 25°C
 Testing Circuitry Figure A

2. Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
75	36.188	36.187
80	36.186	36.187
85	36.186	36.186
90	36.186	36.186
100	36.186	36.186
110	36.186	36.186
120	36.186	36.185
132	36.186	36.185
140	36.185	36.185

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Note: Slanted line shows the range of the rated load current.

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Model	LCA75S-36			
Item	Efficiency (by Input Voltage) 効率(入力電圧特性)		Temperature Testing Circuitry	25°C Figure A
Object				
1. Graph				
	□	Load 50%	△	Load 100%
[%]				
Efficiency				
86				
82				
78				
74				
70				
66				
62				
58				
70 80 90 100 110 120 130 140 150				
Input Voltage [V]				
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2.00	36.188	36.189	36.189																																				
2.10	36.188	36.188	36.188																																				
2.31	36.188	36.188	36.187																																				

2. Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	36.191	36.193	36.193
0.40	36.190	36.190	36.190
0.80	36.189	36.189	36.190
1.20	36.189	36.189	36.189
1.60	36.189	36.189	36.189
2.00	36.188	36.189	36.189
2.10	36.188	36.188	36.188
2.31	36.188	36.188	36.187
—	—	—	—
—	—	—	—

Note: Slanted line shows the range of the rated load current.

(注) 斜線は定格負荷電流範囲を示す。

COSEL

Model	LCA75S-36																																																									
Item	Overcurrent Protection 過電流保護	Temperature Testing Circuitry	25°C Figure A																																																							
Object	+36.0V 2.1A																																																									
1. Graph																																																										
<p>Graph showing Output Voltage [V] vs Load Current [A]. The graph plots Output Voltage against Load Current for three different Input Voltages: 85V, 100V, and 132V. The output voltage remains constant at approximately 36V until a load current of about 2.1A, after which it drops sharply. A diagonal line indicates the rated load current range.</p>																																																										
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<table border="1"> <thead> <tr> <th rowspan="2">Output Voltage [V]</th> <th colspan="3">Load Current [A]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>36.00</td><td>2.663</td><td>2.659</td><td>2.671</td></tr> <tr><td>34.20</td><td>2.673</td><td>2.669</td><td>2.686</td></tr> <tr><td>32.40</td><td>2.682</td><td>2.679</td><td>2.700</td></tr> <tr><td>28.80</td><td>2.706</td><td>2.707</td><td>2.722</td></tr> <tr><td>25.20</td><td>2.731</td><td>2.734</td><td>2.742</td></tr> <tr><td>21.60</td><td>2.749</td><td>2.760</td><td>2.753</td></tr> <tr><td>18.00</td><td>2.762</td><td>2.762</td><td>2.769</td></tr> <tr><td>14.40</td><td>2.776</td><td>2.773</td><td>2.782</td></tr> <tr><td>10.80</td><td>2.784</td><td>2.778</td><td>2.786</td></tr> <tr><td>7.20</td><td>2.779</td><td>2.768</td><td>2.762</td></tr> <tr><td>3.60</td><td>2.728</td><td>2.709</td><td>2.670</td></tr> <tr><td>0.00</td><td>2.731</td><td>2.773</td><td>2.856</td></tr> </tbody> </table>				Output Voltage [V]	Load Current [A]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	36.00	2.663	2.659	2.671	34.20	2.673	2.669	2.686	32.40	2.682	2.679	2.700	28.80	2.706	2.707	2.722	25.20	2.731	2.734	2.742	21.60	2.749	2.760	2.753	18.00	2.762	2.762	2.769	14.40	2.776	2.773	2.782	10.80	2.784	2.778	2.786	7.20	2.779	2.768	2.762	3.60	2.728	2.709	2.670	0.00	2.731	2.773	2.856
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Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

COSEL

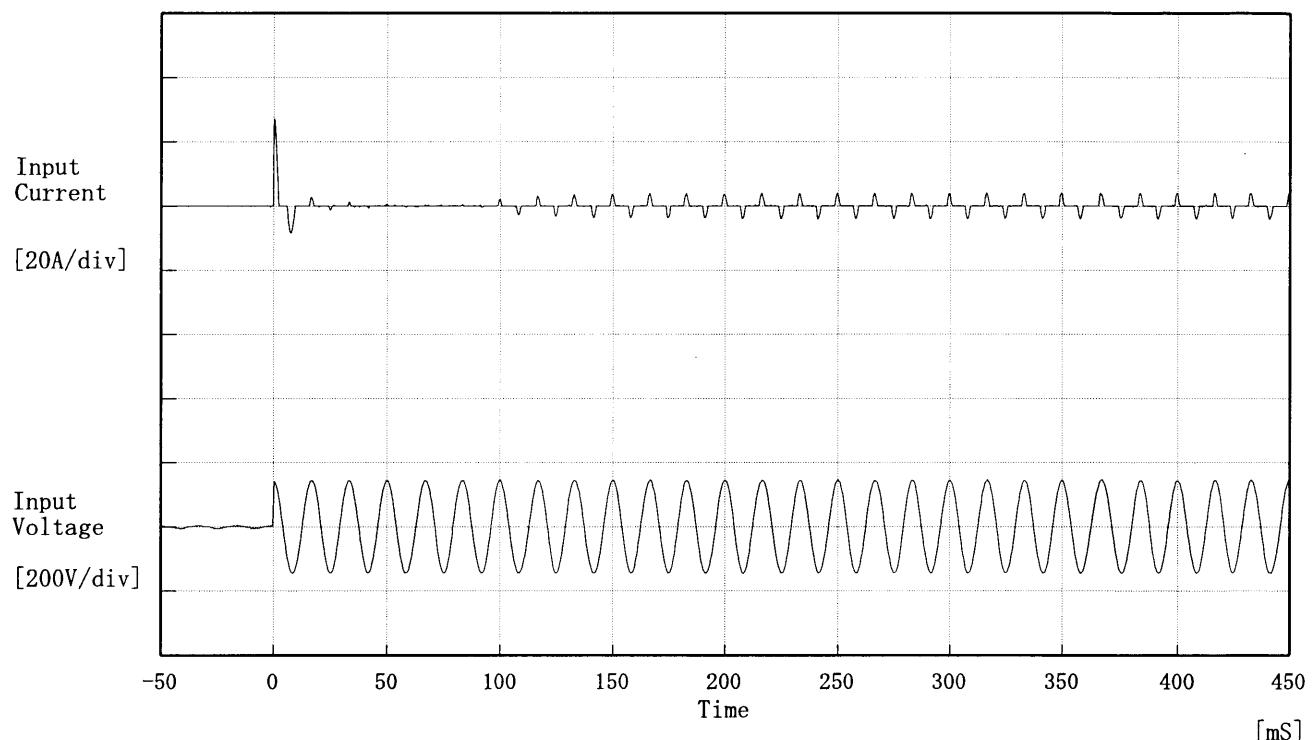
Model	LCA75S-36	Testing Circuitry Figure A																																																					
Item	Overvoltage Protection 過電圧保護																																																						
Object	+36.0V 2.1A																																																						
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Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。

COSEL

Model	LCA75S-36	Temperature	25°C
Item	Inrush Current 突入電流	Testing Circuitry	Figure A
Object	_____		



Input Voltage 100 V

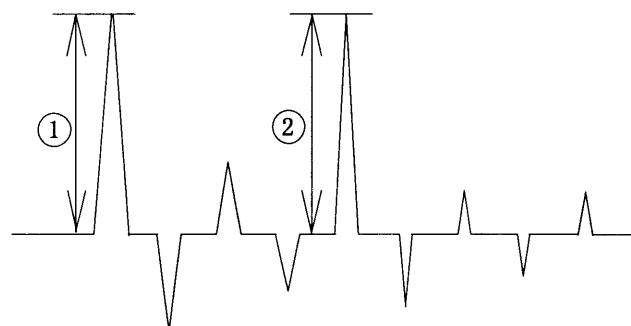
Frequency 60 Hz

Load 100 %

Inrush Current

① 27.21 [A]

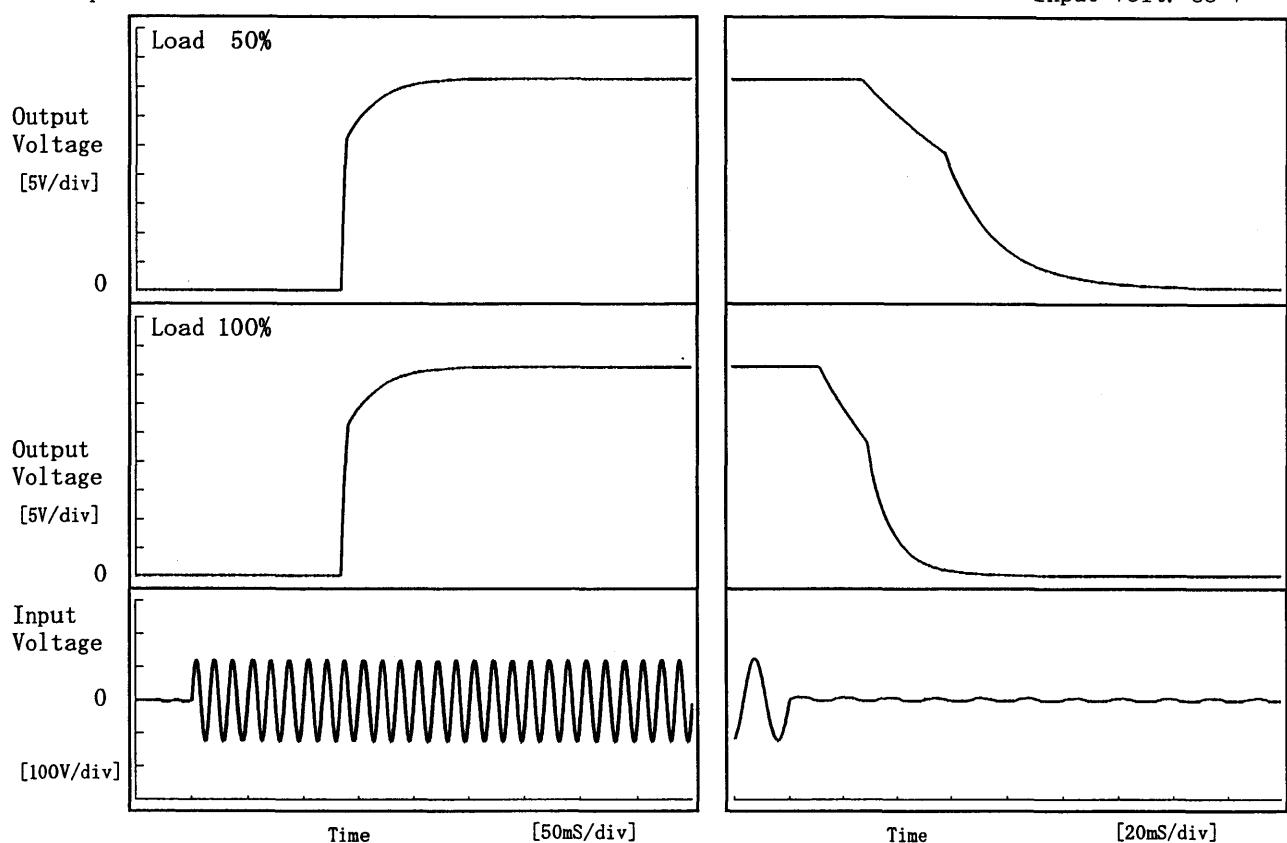
② 4.01 [A]



COSEL

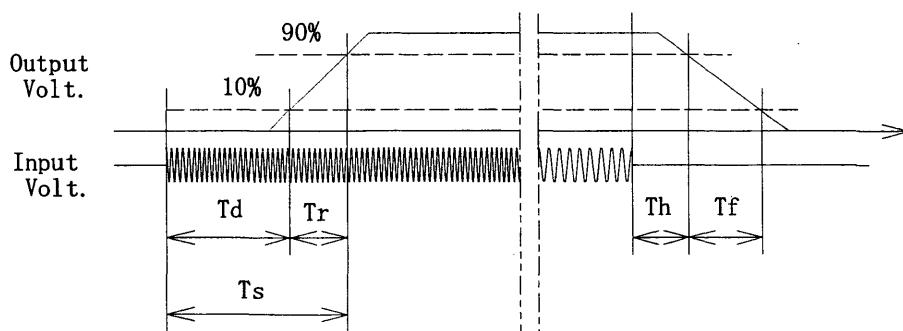
Model	LCA75S-36	Temperature	25°C
Item	Rise and Fall Time 立上り、立下り時間	Testing Circuitry	Figure A
Object	+36.0 V 2.1 A		

1. Graph



2. Values

Load	Time	T _d	T _r	T _s	T _h	T _f	[mS]
50 %		133.8	31.5	165.3	35.4	58.4	
100 %		134.0	33.0	167.0	16.1	29.7	



COSEL

Model	LCA75S-36	Testing Circuitry Figure A																																																					
Item	Ambient Temperature Drift 周囲温度変動																																																						
Object	+36.0V 2.1A																																																						
1. Graph	<p>Output Voltage [V]</p> <p>Ambient Temperature [°C]</p> <p>Load 100%</p> <p>Note: Slanted line shows the range of the rated ambient temperature.</p>	2. Values																																																					
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—	—	—	—																																																				

COSEL

Model	LCA75S-36					
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧					
Object	+36.0V 2.1A					
1. Graph						
[V]						
Note: Slanted line shows the range of the rated ambient temperature.						
(注)斜線は定格周囲温度範囲を示す。						
Testing Circuitry Figure A						
2. Values						
Ambient Temperature [°C]	Input Voltage [V]					
	Load 50%	Load 100%				
-20	65	69				
-10	64	69				
0	64	69				
10	63	68				
20	63	68				
25	63	68				
30	63	68				
40	63	69				
50	63	69				
60	63	69				
—	—	—				

COSEL

Model	LCA75S-36	Temperature Testing Circuitry 25°C Figure A																					
Item	Time Lapse Drift 経時ドリフト																						
Object	+36.0V 2.1A																						
1. Graph		2. Values																					
<p>[V]</p> <table border="1"> <caption>Data points from Figure A graph</caption> <thead> <tr> <th>Time [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>36.220</td></tr> <tr><td>0.5</td><td>36.205</td></tr> <tr><td>1.0</td><td>36.206</td></tr> <tr><td>2.0</td><td>36.205</td></tr> <tr><td>3.0</td><td>36.204</td></tr> <tr><td>4.0</td><td>36.204</td></tr> <tr><td>5.0</td><td>36.204</td></tr> <tr><td>6.0</td><td>36.204</td></tr> <tr><td>7.0</td><td>36.204</td></tr> <tr><td>8.0</td><td>36.204</td></tr> </tbody> </table>		Time [H]	Output Voltage [V]	0.0	36.220	0.5	36.205	1.0	36.206	2.0	36.205	3.0	36.204	4.0	36.204	5.0	36.204	6.0	36.204	7.0	36.204	8.0	36.204
Time [H]	Output Voltage [V]																						
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5.0	36.204																						
6.0	36.204																						
7.0	36.204																						
8.0	36.204																						
<p>Output Voltage [V]</p> <p>Input Volt. 100V</p> <p>Load 100%</p>																							



Model	LCA75S-36	Testing Circuitry Figure A
Item	Output Voltage Accuracy 定電圧精度	
Object	+36.0V 2.1A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10~50 °C

Input Voltage : 85~132 V

Load Current : 0~2.1 A

* Output Voltage Accuracy = ±(Maximum of Output Voltage — Minimum of Output Voltage) / 2

$$* \text{Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

1. 定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 -10~50 °C

入力電圧 85~132 V

負荷電流 0~2.1 A

* 定電圧精度(変動値) = ±(出力電圧の最高値—出力電圧の最低値) / 2

$$* \text{定電圧精度(変動率)} = \frac{\text{変動値}}{\text{定格出力電圧}} \times 100$$

2. Values

Item	Temperature [°C]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy(Ration) [%]
Maximum Voltage	-10	132	0.0	36.238	±50	±0.2
Minimum Voltage	50	132	2.1	36.139		

COSEL

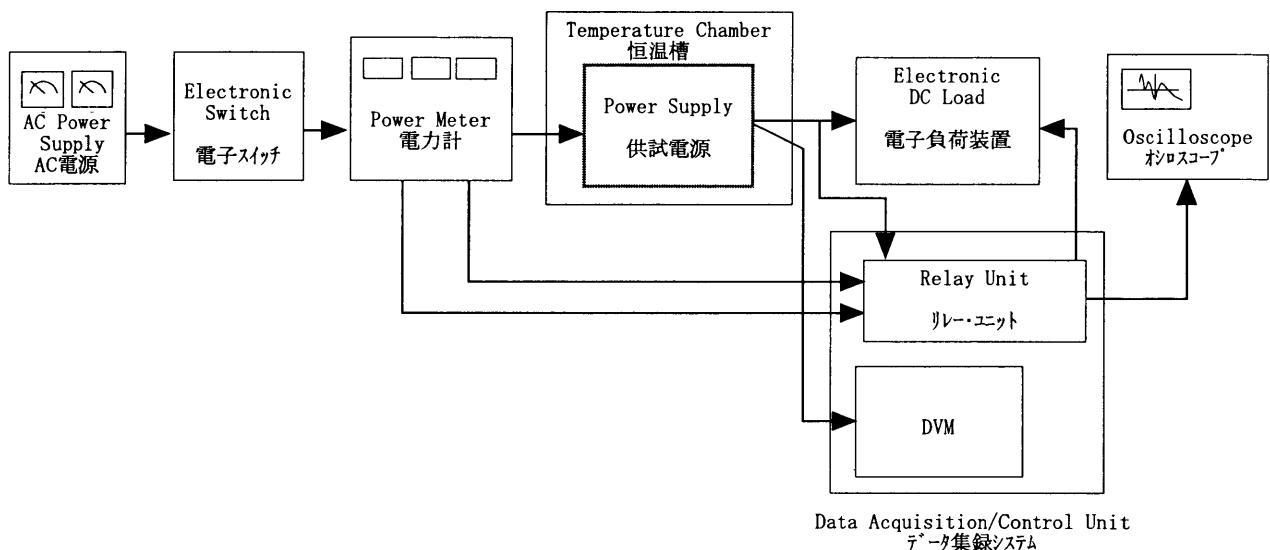


Figure A

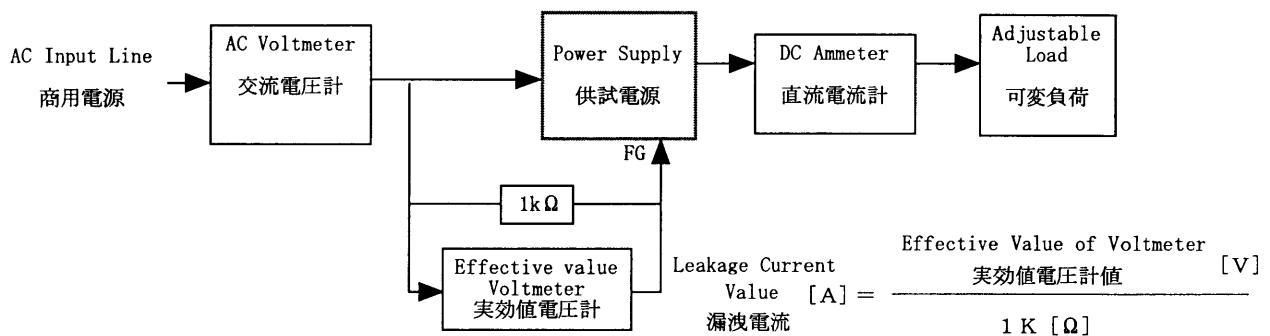


Figure B (DENTORI)

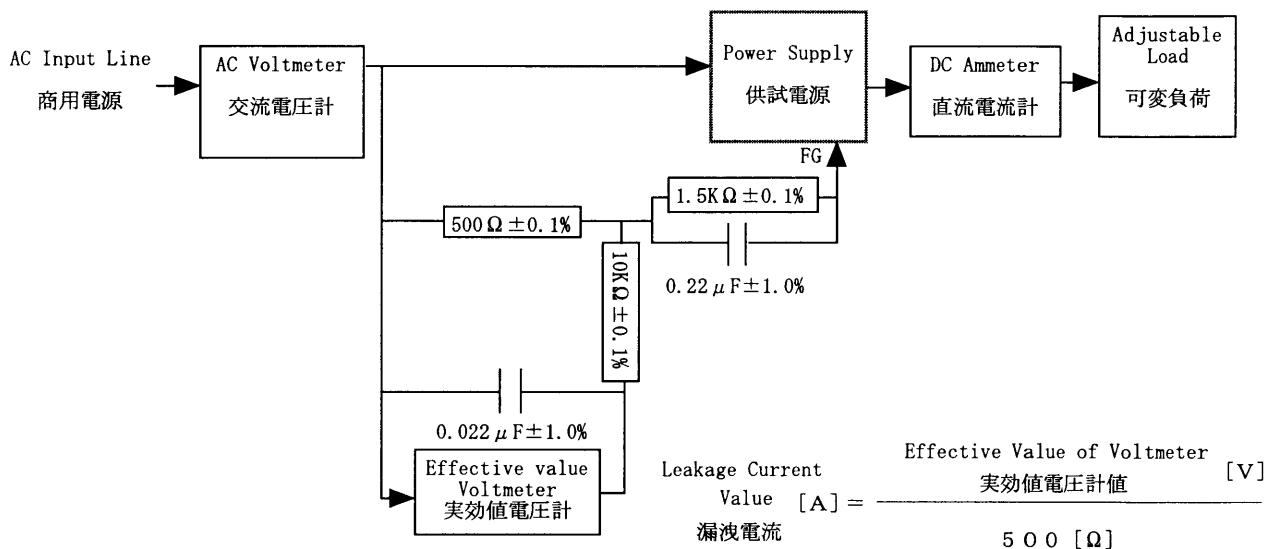


Figure B (IEC60950)

COSEL

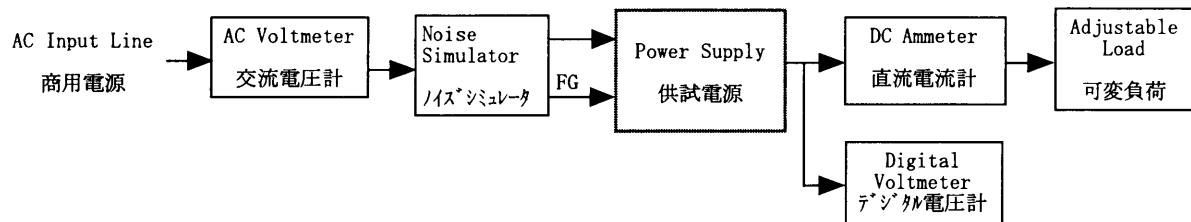


Figure C

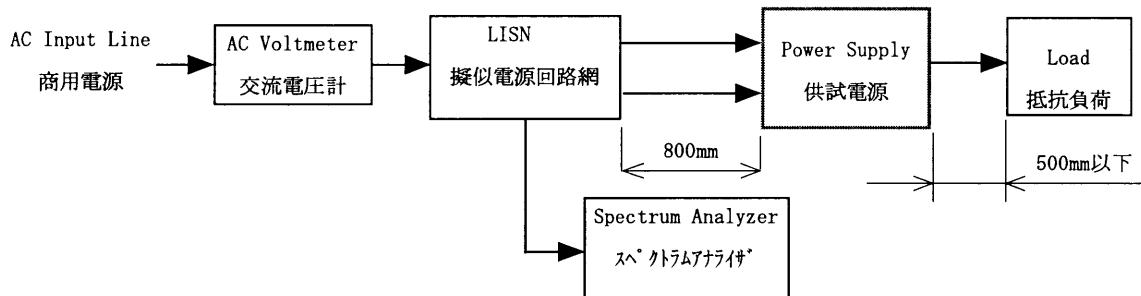


Figure D

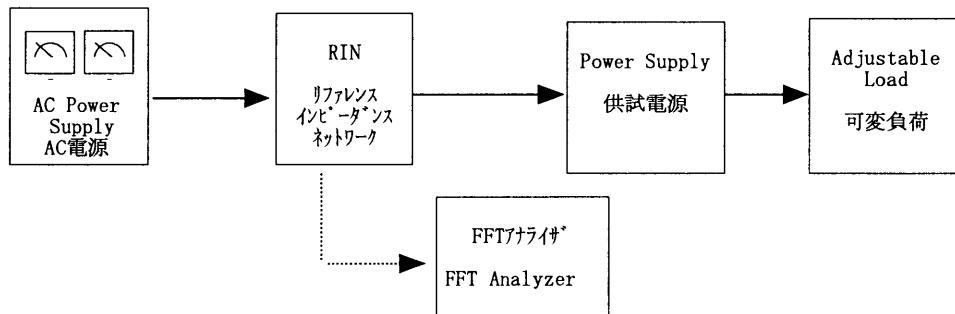


Figure E