

TEST DATA OF HCA3500TF-48

Regulated DC Power Supply

Approved by : _____ T.Yamamine

Design Manager

Prepared by : _____ Y.Fukumura

Design Engineer

COSEL CO.,LTD.



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Model	HCA3500TF-48	Ambient Temperature 25°C Baseplate Temperature 25°C Testing Circuitry Figure A																																																		
Item	Input Current (by Load Current)																																																			
Object	_____																																																			
1.Graph	<p>—△— Input Voltage 200V - - □ - - Input Voltage 400V - - ○ - - Input Voltage 480V</p> <p>Input Current [A]</p> <p>Load Current [A]</p>	2.Values																																																		
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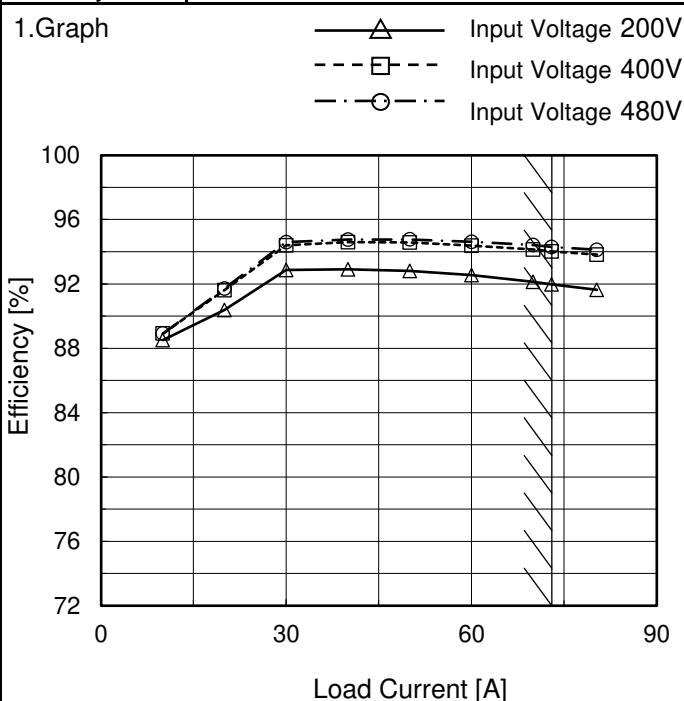
Note: Hatched line shows the range of the rated load current.

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Model	HCA3500TF-48
Item	Efficiency (by Load Current)
Object	_____

 Ambient Temperature 25°C
 Baseplate Temperature 25°C
 Testing Circuitry Figure A

1.Graph



2.Values

Load Current [A]	Efficiency [%]		
	Input Voltage 200[V]	Input Voltage 400[V]	Input Voltage 480[V]
0.0	-	-	-
10.0	88.5	88.9	88.9
20.0	90.4	91.6	91.7
30.0	92.9	94.4	94.6
40.0	92.9	94.6	94.8
50.0	92.8	94.6	94.8
60.0	92.6	94.4	94.6
70.0	92.1	94.1	94.4
73.0	92.0	94.0	94.3
80.3	91.6	93.8	94.1
-	-	-	-

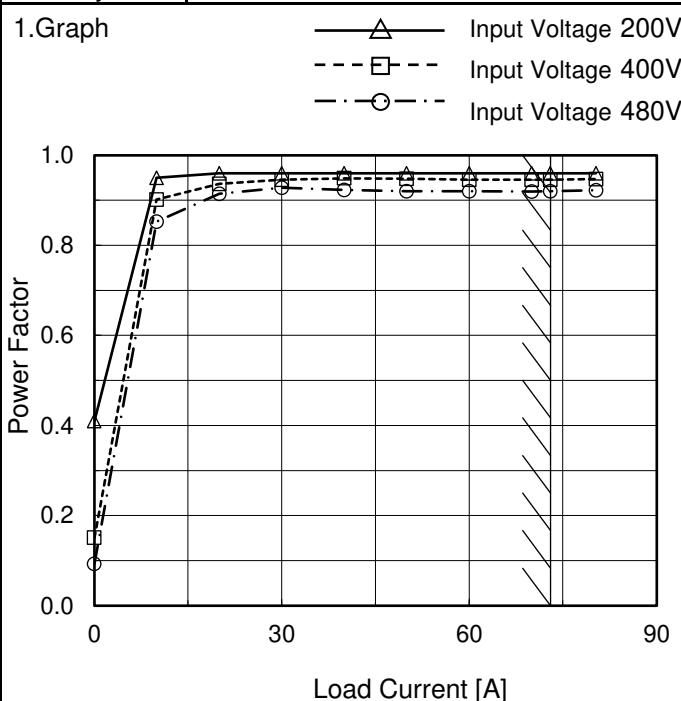
Note: Hatched line shows the range of the rated load current.

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Model	HCA3500TF-48
Item	Power Factor (by Load Current)
Object	_____

 Ambient Temperature 25°C
 Baseplate Temperature 25°C
 Testing Circuitry Figure A

1.Graph



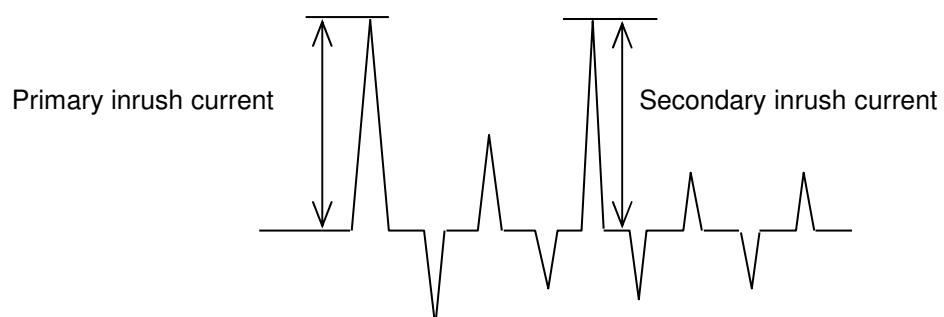
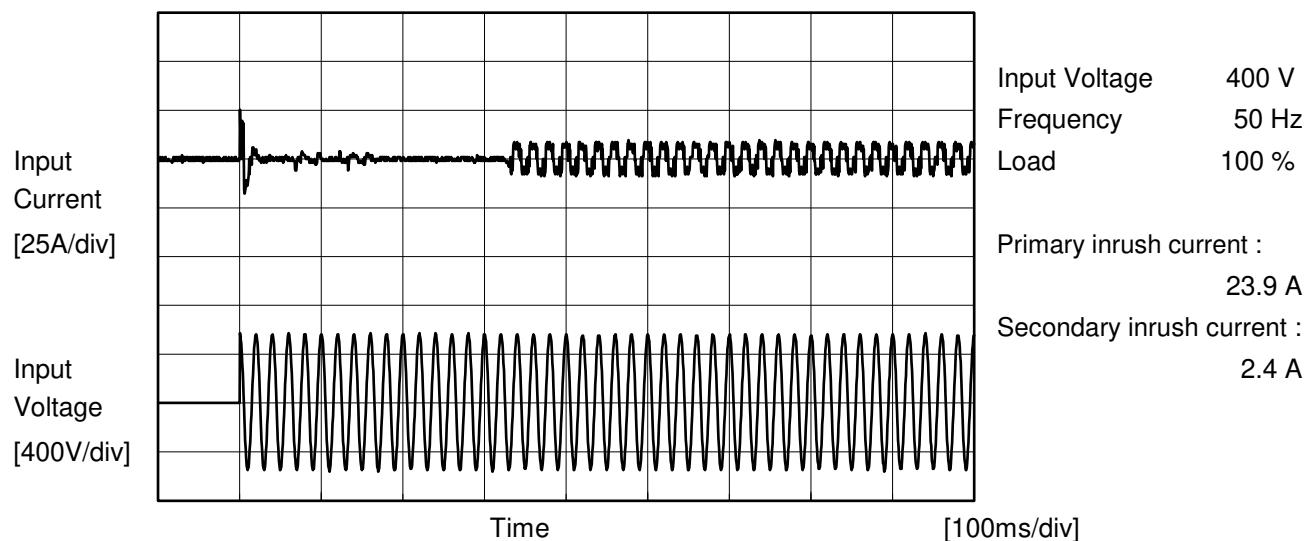
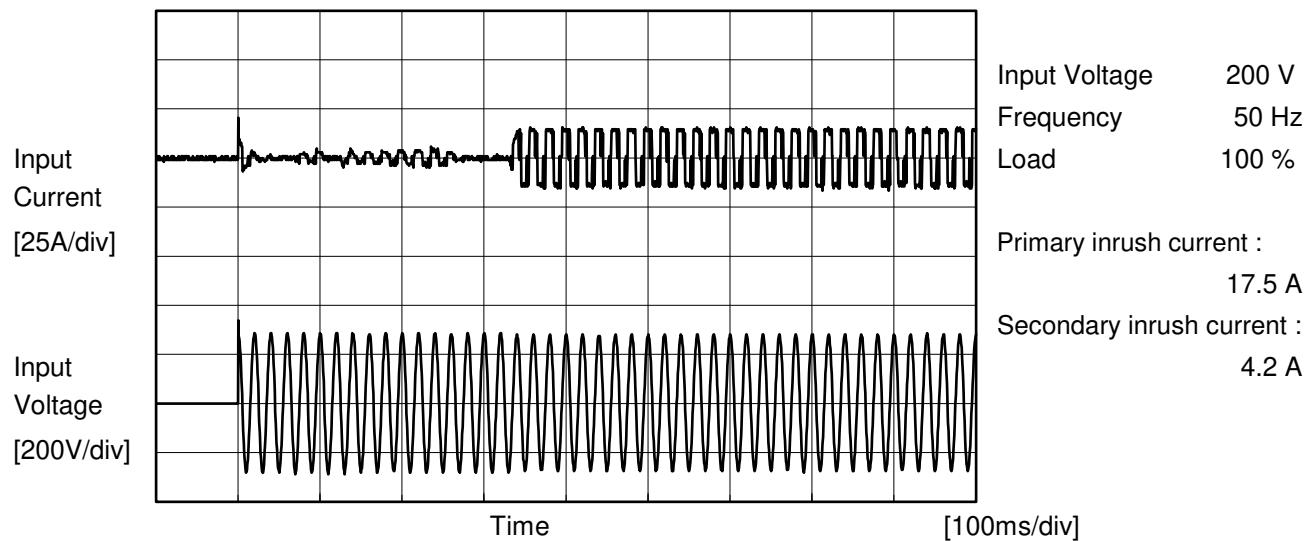
2.Values

Load Current [A]	Power Factor		
	Input Voltage 200[V]	Input Voltage 400[V]	Input Voltage 480[V]
0.0	0.410	0.151	0.093
10.0	0.950	0.902	0.853
20.0	0.960	0.937	0.914
30.0	0.960	0.945	0.928
40.0	0.960	0.949	0.923
50.0	0.960	0.948	0.920
60.0	0.960	0.945	0.920
70.0	0.960	0.945	0.920
73.0	0.960	0.946	0.920
80.3	0.960	0.947	0.922
-	-	-	-

Note: Hatched line shows the range of the rated load current.

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Model	HCA3500TF-48	Temperature	25°C
Item	Inrush Current	Testing Circuitry	Figure A
Object	_____		





Model	HCA3500TF-48	Ambient Temperature 25°C
Item	Leakage Current	Baseplate Temperature 25°C Testing Circuitry Figure B
Object	_____	

1. Results

[mA]

Standards	Testing Circuitry	Input Voltage			Note
		200[V]	400[V]	480[V]	
IEC62368-1	FigureB-1	0.58	1.17	1.37	
	FigureB-2	0.58	1.17	1.37	

2. Condition

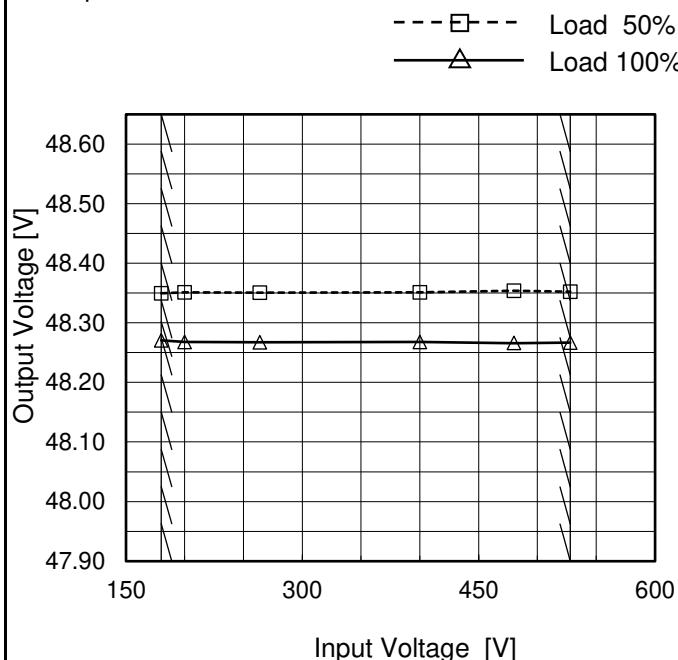
Leakage current value is concluded after measuring all phases of AC input and by choosing the larger one

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Model	HCA3500TF-48
Item	Line Regulation
Object	+48V73A

 Ambient Temperature 25°C
 Baseplate Temperature 25°C
 Testing Circuitry Figure A

1.Graph



2.Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
180	48.350	48.271
200	48.351	48.268
264	48.351	48.267
400	48.351	48.268
480	48.354	48.266
528	48.352	48.267
-	-	-
-	-	-
-	-	-

Note: Hatched line shows the range of the rated input voltage.

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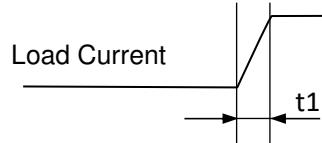
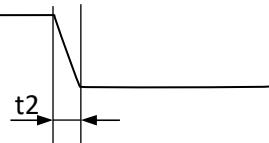
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Item	Load Regulation																																												
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1.Graph	<p>Input Voltage 200V Input Voltage 400V Input Voltage 480V</p> <table border="1"> <caption>Data points estimated from Figure A graph</caption> <thead> <tr> <th>Load Current [A]</th> <th>Output Voltage [V] (200V)</th> <th>Output Voltage [V] (400V)</th> <th>Output Voltage [V] (480V)</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>48.380</td><td>48.385</td><td>48.386</td></tr> <tr><td>10.0</td><td>48.410</td><td>48.405</td><td>48.408</td></tr> <tr><td>20.0</td><td>48.370</td><td>48.374</td><td>48.376</td></tr> <tr><td>30.0</td><td>48.360</td><td>48.358</td><td>48.356</td></tr> <tr><td>40.0</td><td>48.340</td><td>48.334</td><td>48.337</td></tr> <tr><td>50.0</td><td>48.320</td><td>48.321</td><td>48.324</td></tr> <tr><td>60.0</td><td>48.300</td><td>48.293</td><td>48.296</td></tr> <tr><td>70.0</td><td>48.280</td><td>48.282</td><td>48.280</td></tr> <tr><td>73.0</td><td>48.270</td><td>48.262</td><td>48.263</td></tr> <tr><td>80.3</td><td>48.270</td><td>48.265</td><td>48.265</td></tr> </tbody> </table>	Load Current [A]	Output Voltage [V] (200V)	Output Voltage [V] (400V)	Output Voltage [V] (480V)	0.0	48.380	48.385	48.386	10.0	48.410	48.405	48.408	20.0	48.370	48.374	48.376	30.0	48.360	48.358	48.356	40.0	48.340	48.334	48.337	50.0	48.320	48.321	48.324	60.0	48.300	48.293	48.296	70.0	48.280	48.282	48.280	73.0	48.270	48.262	48.263	80.3	48.270	48.265	48.265
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2.Values																																													
Item	Ripple-Noise	Ambient Temperature 25°C Baseplate Temperature 25°C Testing Circuitry Figure C																																											
Object	+48V73A																																												
1.Graph	<p>Input Voltage 400V Load 100%</p>																																												

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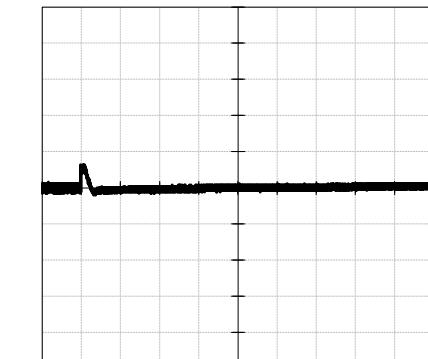
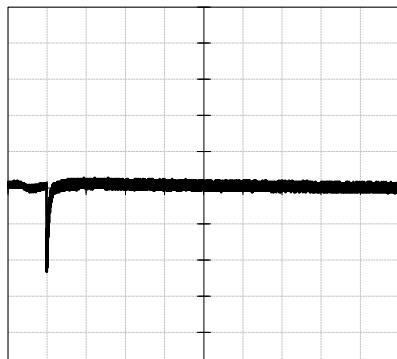
Model	HCA3500TF-48	Ambient Temperature 25°C
Item	Dynamic Load Response	Baseplate Temperature 25°C
Object	+48V73A	Testing Circuitry Figure A

Input Voltage 400 V

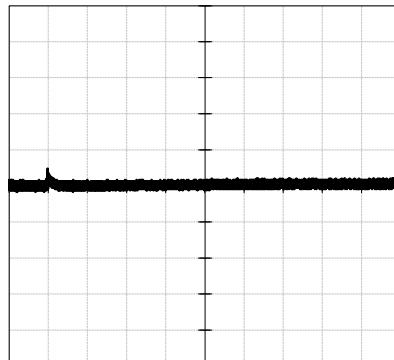
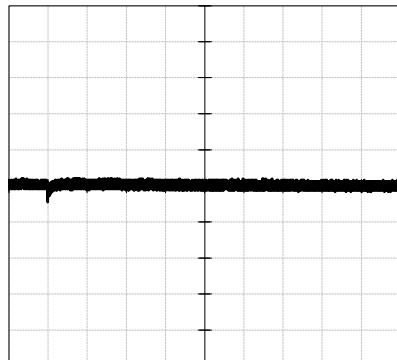
Cycle 1000 ms

Response. $t_1=t_2=50\mu s$. Typ

Load 0% ←→
Load 100%



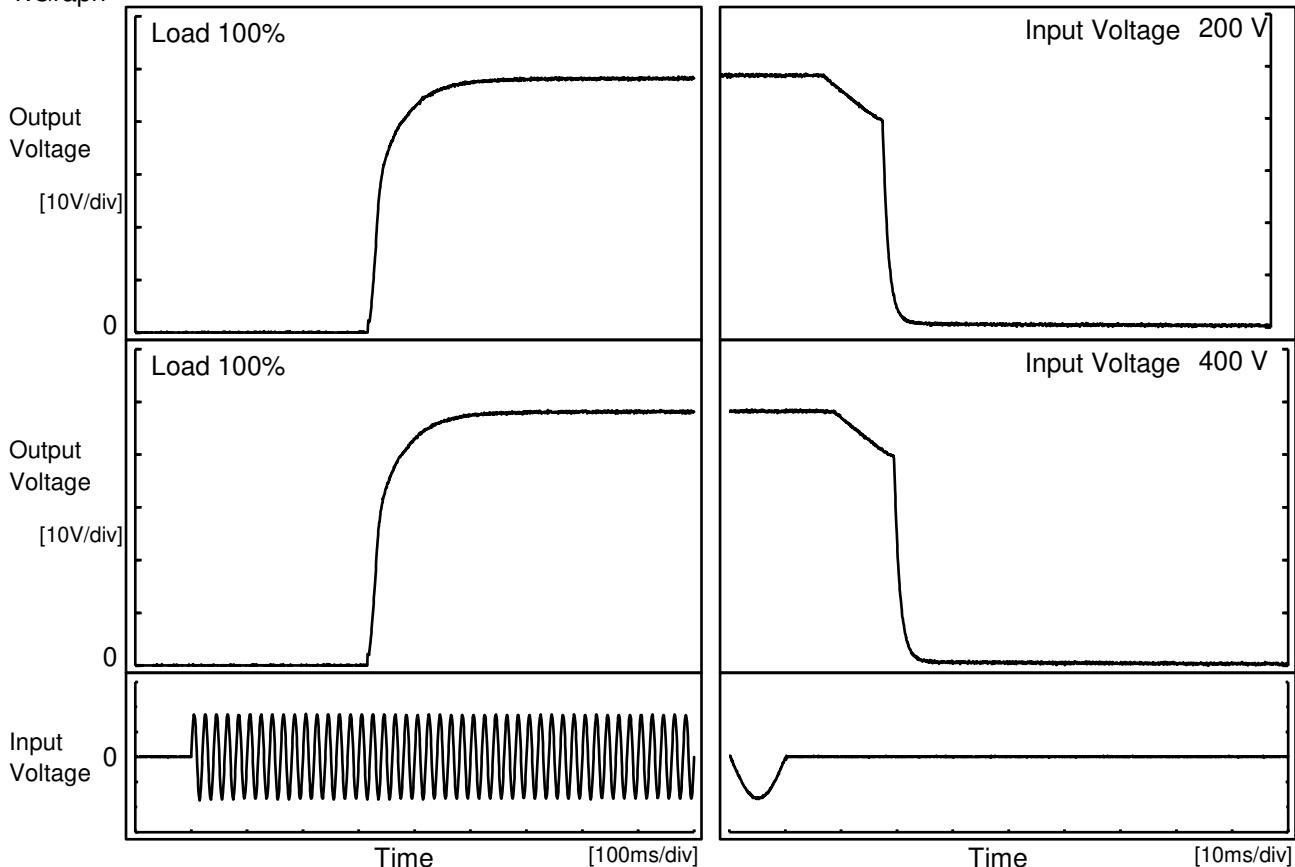
Load 50% ←→
Load 100%



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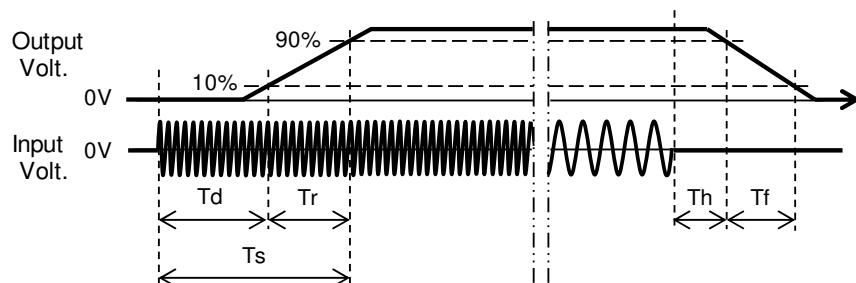
Model	HCA3500TF-48	Ambient Temperature 25°C
Item	Rise and Fall Time	Baseplate Temperature 25°C
Object	+48V73A	Testing Circuitry Figure A

1. Graph



2. Values

Input Voltage	Time	Td	Tr	Ts	Th	Tf	[ms]
200 V		322.0	82.0	404.0	14.3	7.6	
400 V		321.0	81.5	402.5	14.2	7.6	

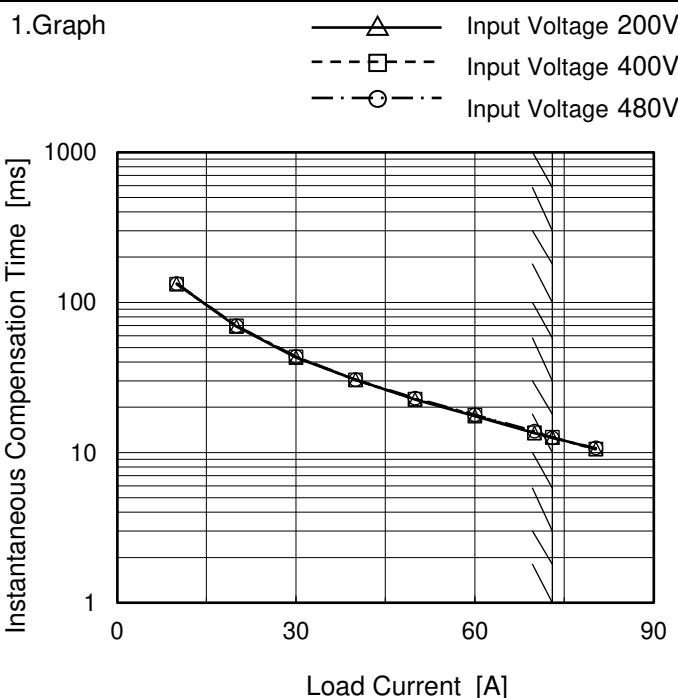


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Model	HCA3500TF-48	Ambient Temperature 25°C Baseplate Temperature 25°C Testing Circuitry Figure A																																
Item	Hold-Up Time																																	
Object	+48V73A																																	
1. Graph		2. Values																																
<p>The graph plots Hold-Up Time [ms] on a logarithmic Y-axis (1 to 1000) against Input Voltage [V] on a linear X-axis (150 to 600). Two data series are shown: Load 50% (dashed line with square markers) and Load 100% (solid line with triangle markers). Both series show a relatively constant hold-up time around 30-40 ms across the input voltage range, except for a slight increase at the lowest and highest voltages. A horizontal dashed line is drawn at approximately 35 ms. A hatched rectangular area highlights the input voltage range between approximately 180V and 528V.</p>		<table border="1"> <thead> <tr> <th rowspan="2">Input Voltage [V]</th> <th colspan="2">Hold-Up Time [ms]</th> </tr> <tr> <th>Load 50%</th> <th>Load 100%</th> </tr> </thead> <tbody> <tr> <td>180</td><td>34</td><td>13</td> </tr> <tr> <td>200</td><td>33</td><td>13</td> </tr> <tr> <td>264</td><td>33</td><td>13</td> </tr> <tr> <td>400</td><td>34</td><td>13</td> </tr> <tr> <td>480</td><td>34</td><td>13</td> </tr> <tr> <td>528</td><td>34</td><td>13</td> </tr> <tr> <td>-</td><td>-</td><td>-</td> </tr> <tr> <td>-</td><td>-</td><td>-</td> </tr> <tr> <td>-</td><td>-</td><td>-</td> </tr> </tbody> </table>	Input Voltage [V]	Hold-Up Time [ms]		Load 50%	Load 100%	180	34	13	200	33	13	264	33	13	400	34	13	480	34	13	528	34	13	-	-	-	-	-	-	-	-	-
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<p>This duration covers from Shut-off of input voltage to the moment when output voltage drops to 95% of the rated voltage.</p> <p>Note: Hatched line shows the range of the rated input voltage.</p>																																		

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Model	HCA3500TF-48
Item	Instantaneous Interruption Compensation
Object	+48V73A

 Ambient Temperature 25°C
 Baseplate Temperature 25°C
 Testing Circuitry Figure A


2.Values

Load Current [A]	Time [ms]		
	Input Voltage 200[V]	Input Voltage 400[V]	Input Voltage 480[V]
0.0	-	-	-
10.0	133	132	134
20.0	69	70	70
30.0	43	43	44
40.0	31	30	31
50.0	23	23	23
60.0	17	18	18
70.0	13	14	14
73.0	13	13	13
80.3	11	11	11
-	-	-	-

Note: Hatched line shows the range of the rated input voltage.

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Model	HCA3500TF-48	Ambient Temperature 25°C Baseplate Temperature 25°C Testing Circuitry Figure A																																																																							
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Note: Hatched line shows the range of the rated load current.



Model	HCA3500TF-48	
Item	Ambient Temperature Drift	Testing Circuitry Figure A
Object	+48V73A	

1.Values

Load 100%

Temperature[°C]		Output Voltage [V]		
Ambient	Baseplate	Input Voltage 200V	Input Voltage 400V	Input Voltage 480V
-10	0	48.192	48.225	48.223
25	25	48.338	48.357	48.354
70	55	48.396	48.395	48.389

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+48V73A	

1.Values

Temperature[°C]		Input Voltage [V]	
Ambient	Baseplate	Load 50%	Load 100%
-10	0	171	171
25	25	171	171
70	55	171	171

Item	Overvoltage Protection	Testing Circuitry Figure A
Object	+48V73A	

1.Values

Load 0%

Temperature[°C]		Operating Point [V]	
Ambient	Baseplate	Input Voltage 200V	Input Voltage 400V
-10	0	59.91	59.92
25	25	60.03	60.02
70	55	60.86	60.88

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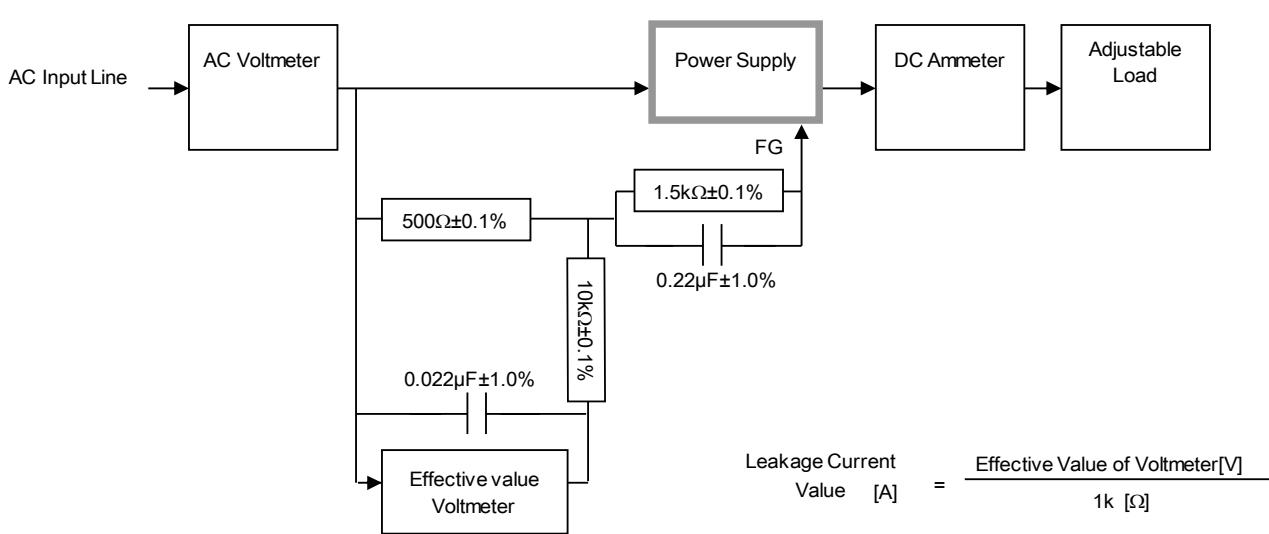
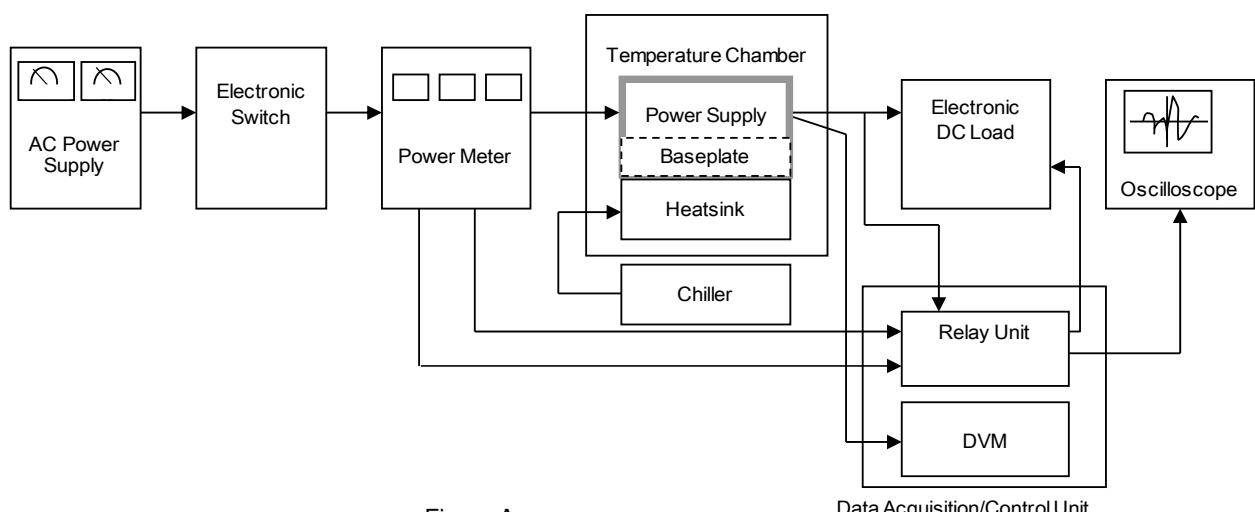


Figure B-1 (IEC62368-1)

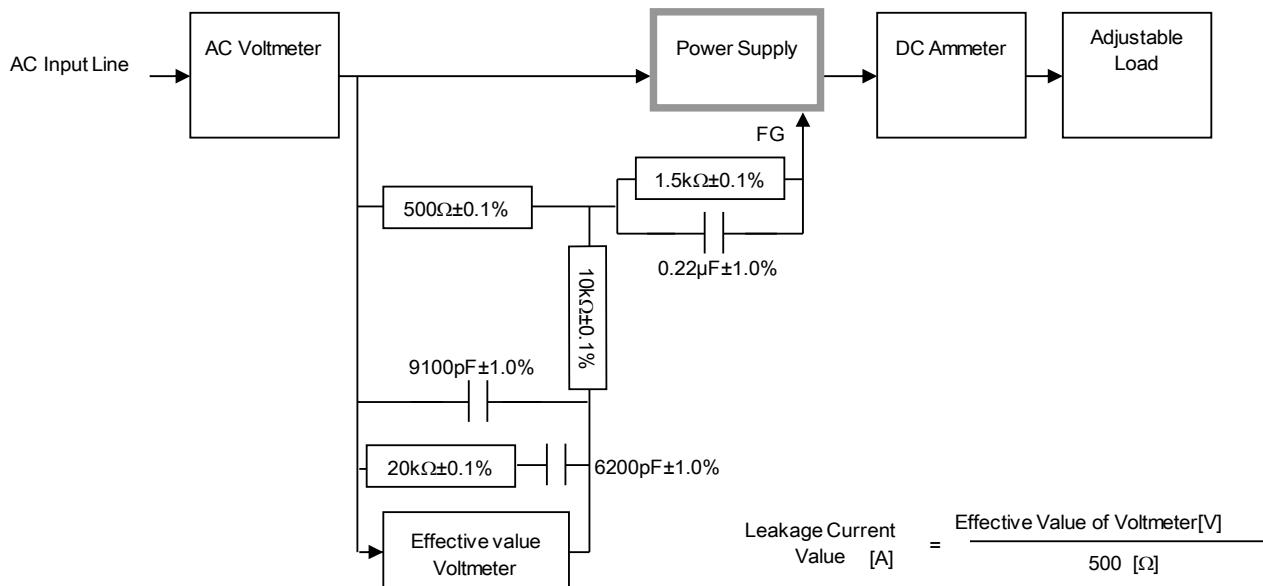


Figure B-2 (IEC62368-1)

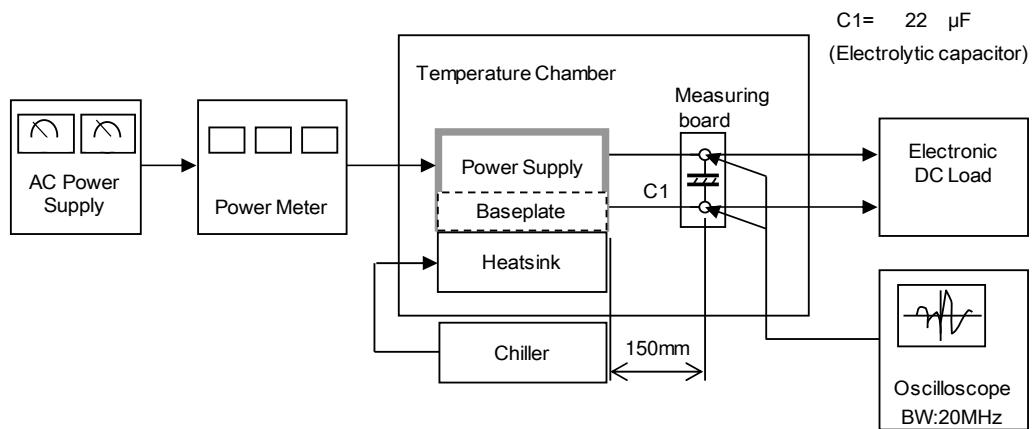
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Figure C