

**COSEL**

TEST DATA OF CDS4004828  
(48V INPUT)

Regulated DC Power Supply

Oct. 18, 2000

Approved by : K. Shimano  
Design Manager

Prepared by : T. Nakayama  
Design Engineer

コーセル株式会社  
**COSEL CO., LTD.**



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Note: Slanted line shows the range of the rated input voltage.

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Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

**COSSEL**

Model	CDS4004828																																																	
Item	Load Regulation 静的負荷変動	Temperature Testing Circuitry	25°C Figure A																																															
Object	+28.0V 18A																																																	
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(注)斜線は定格負荷電流範囲を示す。

**COSEL**

Model	CDS4004828	Temperature Testing Circuitry 25°C Figure A																																							
Item	Ripple Voltage(by Load Current) リップル電圧(負荷特性)																																								
Object	+28V 18A																																								
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**COSSEL**

Model	CDS4004828	Temperature Testing Circuitry 25°C Figure A																																						
Item	Ripple-Noise リップルノイズ																																							
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**COSSEL**

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Note: Slanted line shows the range of the rated load current.

Intermittent operation occurs when the output voltage is from 17.5V to 0V.

(注) 斜線は定格負荷電流範囲を示す。

17.5V～0V間は、間欠モードとなる。

**COSEL**

Model	CDS4004828	Testing Circuitry Figure A																																																								
Item	Overvoltage Protection 過電圧保護																																																									
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Note:	<p>Slanted line shows the range of the rated ambient temperature.</p> <p>(注)斜線は定格周囲温度範囲を示す。</p>																																																									

COSEL

Model	CDS4004828	Temperature	25°C
Item	Dynamic Load Response 動的負荷變動	Testing Circuitry	Figure A
Object	+28V18A		

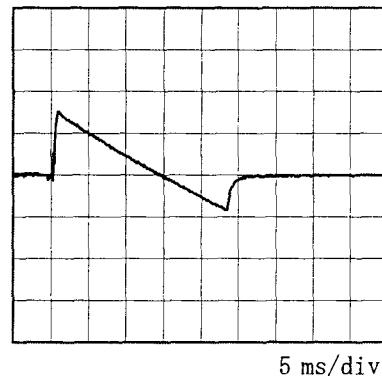
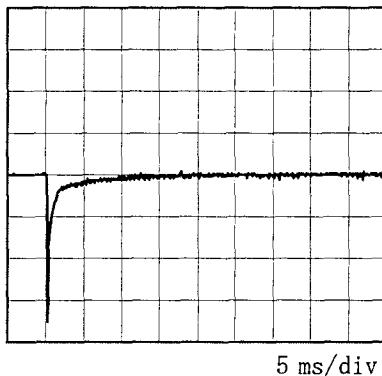
Input Volt. 48 V

Cycle 1000 ms



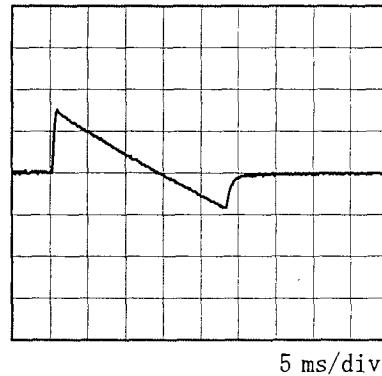
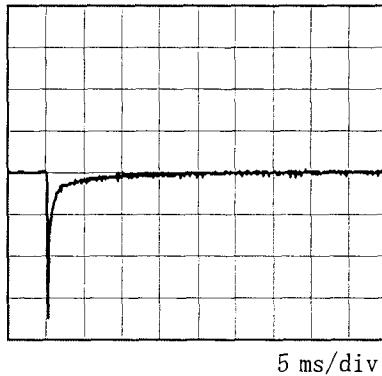
Min. Load (0A) ↔

Load 100% (18A)



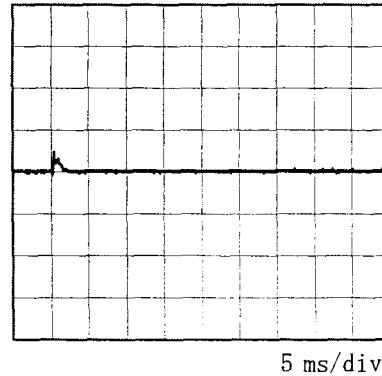
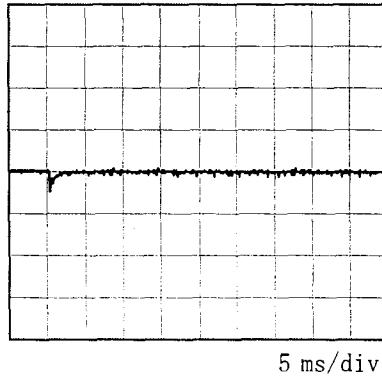
Min. Load (0A) ↔

Load 50% (9A)



Load 10% (1.8A) ↔

Load 100% (18A)

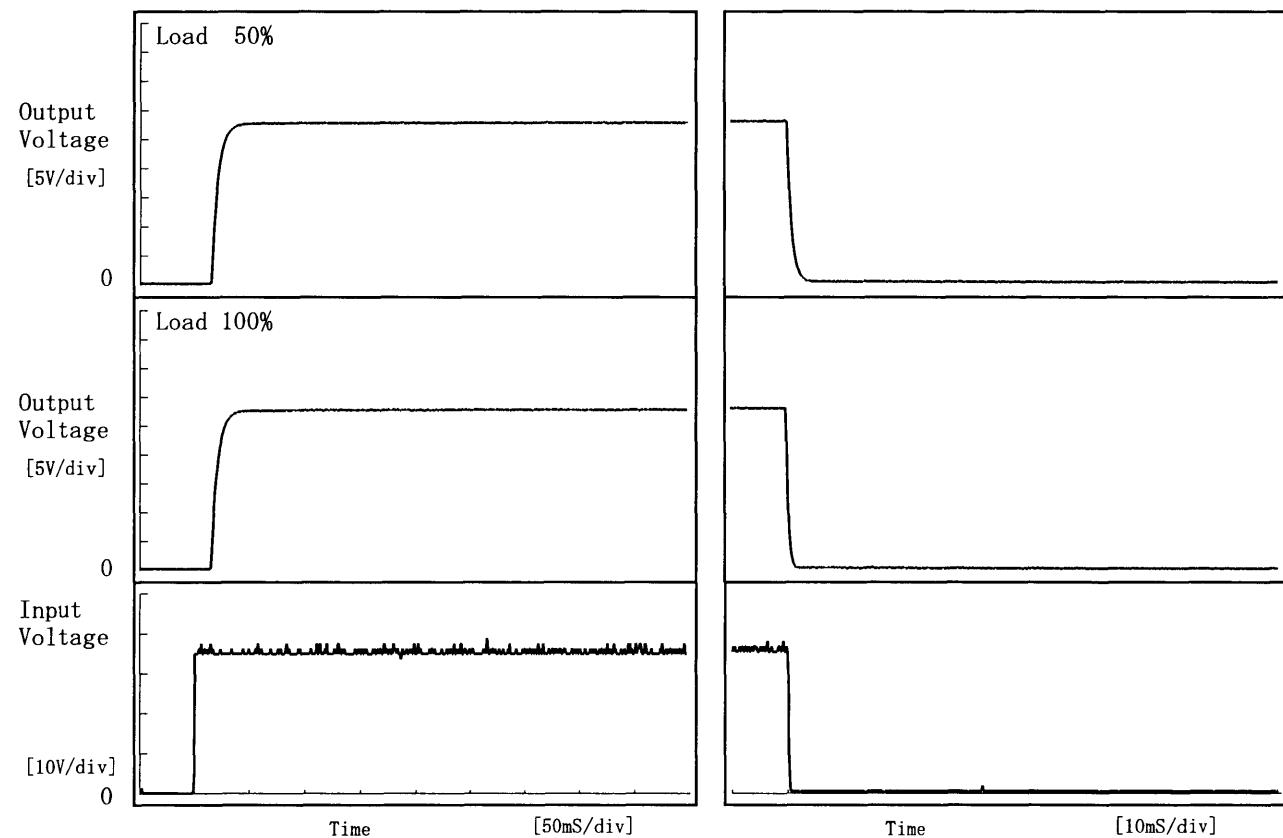


COSEL

Model	CDS4004828
Item	Rise and Fall Time 立ち上り、立下り時間
Object	+28.0V 18A

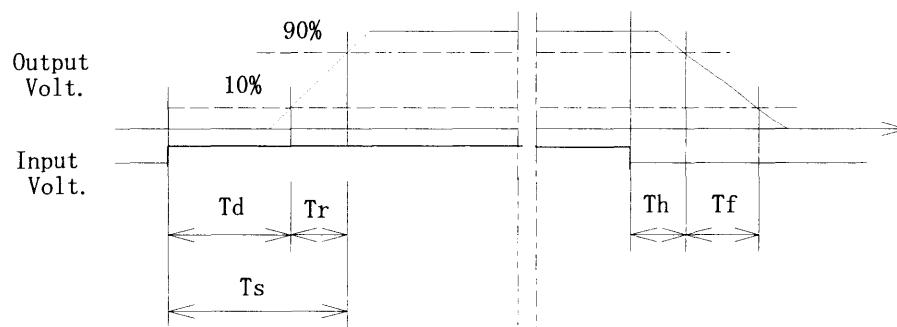
Temperature 25°C  
Testing Circuitry Figure A

## 1. Graph



## 2. Values

Load	Time	T d	T r	T s	T h	T f	[mS]
50 %		15.50	13.50	29.00	0.15	2.10	
100 %		15.50	13.50	29.00	0.05	1.05	

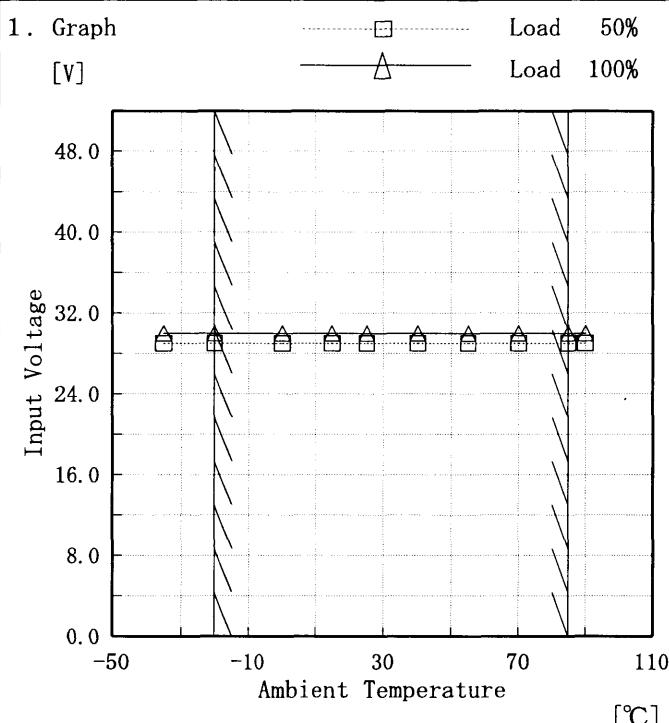


**COSSEL**

Model	CDS4004828																																																					
Item	Ambient Temperature Drift 周囲温度変動																																																					
Object	+28.0V 18A																																																					
1. Graph	<p>[V]</p> <p>Output Voltage</p> <p>Ambient Temperature [°C]</p> <p>Load 100%</p>	Input Volt. 36V	Input Volt. 48V	Input Volt. 76V																																																		
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**COSEL**

Model	CDS4004828
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+28.0V 18A



Testing Circuitry Figure A

## 2. Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-35	29.0	30.0
-20	29.0	30.0
0	29.0	30.0
15	29.0	30.0
25	29.0	30.0
40	29.0	30.0
55	29.0	30.0
70	29.0	30.0
85	29.0	30.0
90	29.0	30.0
—	—	—

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

**COSSEL**

Model CDS4004828

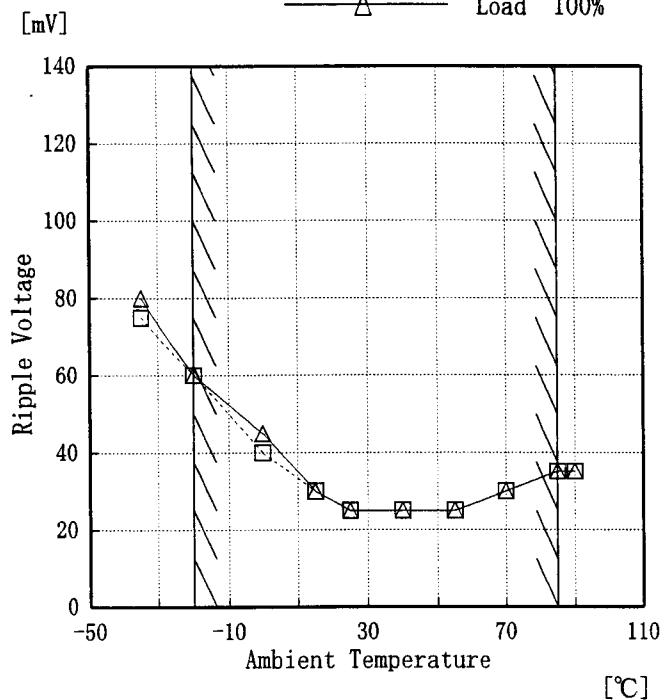
Item Ripple Voltage (by Ambient Temp.)  
リップル電圧 (周囲温度特性)

Object +28V18A

## 1. Graph

Load 50%

Load 100%



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

Testing Circuitry Figure A

## 2. Values

Ambient Temp. [°C]	Ripple Voltage [mV]	
	Load 50%	Load 100%
-35	75	80
-20	60	60
0	40	45
15	30	30
25	25	25
40	25	25
55	25	25
70	30	30
85	35	35
90	35	35
—	—	—

**COSEL**

Model	CDS4004828	Temperature	25°C																						
Item	Time Lapse Drift 経時ドリフト	Testing Circuitry	Figure A																						
Object	+28.0V 18A																								
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<p>[V]</p> <table border="1"> <caption>Data points from Figure A graph</caption> <thead> <tr> <th>Time [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>28.030</td></tr> <tr><td>0.5</td><td>28.022</td></tr> <tr><td>1.0</td><td>28.023</td></tr> <tr><td>2.0</td><td>28.023</td></tr> <tr><td>3.0</td><td>28.024</td></tr> <tr><td>4.0</td><td>28.024</td></tr> <tr><td>5.0</td><td>28.025</td></tr> <tr><td>6.0</td><td>28.025</td></tr> <tr><td>7.0</td><td>28.025</td></tr> <tr><td>8.0</td><td>28.025</td></tr> </tbody> </table> <p>Input Volt. 48V Load 100%</p>				Time [H]	Output Voltage [V]	0.0	28.030	0.5	28.022	1.0	28.023	2.0	28.023	3.0	28.024	4.0	28.024	5.0	28.025	6.0	28.025	7.0	28.025	8.0	28.025
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Model	CDS4004828	Testing Circuitry Figure A
Item	Output Voltage Accuracy 定電圧精度	
Object	+28.0V 18A	

### 1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -20~85 °C

Input Voltage : 36~ 76 V

Load Current : 0~18 A

\* Output Voltage Accuracy = ±(Maximum of Output Voltage - Minimum of Output Voltage) / 2

$$* \text{Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

### 1. 定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 -20~85 °C

入力電圧 36~ 76 V

負荷電流 0~18 A

\* 定電圧精度(変動値) = ±(出力電圧の最高値-出力電圧の最低値) / 2

$$* \text{定電圧精度(変動率)} = \frac{\text{変動値}}{\text{定格出力電圧}} \times 100$$

### 2. Values

Item	Temperature [°C]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy(Ration) [%]
Maximum Voltage	-20	36	0	27.974		
Minimum Voltage	85	76	18	27.750	±112	±0.4



Model	CDS4004828		
Item	Condensation 結露特性	Testing Circuitry	Figure A
Object	+28V18A		

### 1. Condensation test

Testing procedure is as follows.

- ① Keeping and cooling the unit in a tank at -10°C for an hour with the input off.
- ② Taking it out of the tank and dewing itself in a room where the temperature is 25°C and the humidity is 40%RH.
- ③ Testing electrical characteristics of the unit to confirm there be no fault.

### 1. 結露特性試験

入力を切った状態で、恒温槽で-10°Cに冷却しておき、約1時間後に恒温槽から取り出し、室温25°C、湿度40%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

### 2. Values

Item	Data	Testing Conditions
Output Voltage [V]	28.08	Input Volt.: 48V, Load Current:18A
Line Regulation [mV]	12	Input Volt.: 36~76V, Load Current:18A
Load Regulation [mV]	26	Input Volt.: 48V, Load Current:0~18A



Model	CDS4004828	Temperature	25°C
Item	Line Noise Tolerance 入力雑音耐量	Testing Circuitry	Figure B
Object	+28.0V 18A		

## 1. Results

Pulse Width [ nS ]	MODE	No protection failure should occur 保護回路の誤動作がない		DC-like Regulation of Output Voltage 出力電圧の直流的変動
		POLARITY		
50	COMMON	+	OK	no fluctuation
		-	OK	no fluctuation
	NORMAL	+	OK	no fluctuation
		-	OK	no fluctuation
1000	COMMON	+	OK	no fluctuation
		-	OK	no fluctuation
	NORMAL	+	OK	no fluctuation
		-	OK	no fluctuation

## 2. Conditions

Input Voltage : 48 V  
 Pulse Voltage : 2000 V  
 Pulse Cycle : 10 mS  
 Pulse Input Duration : 1 min. or more  
 Load : 100 %

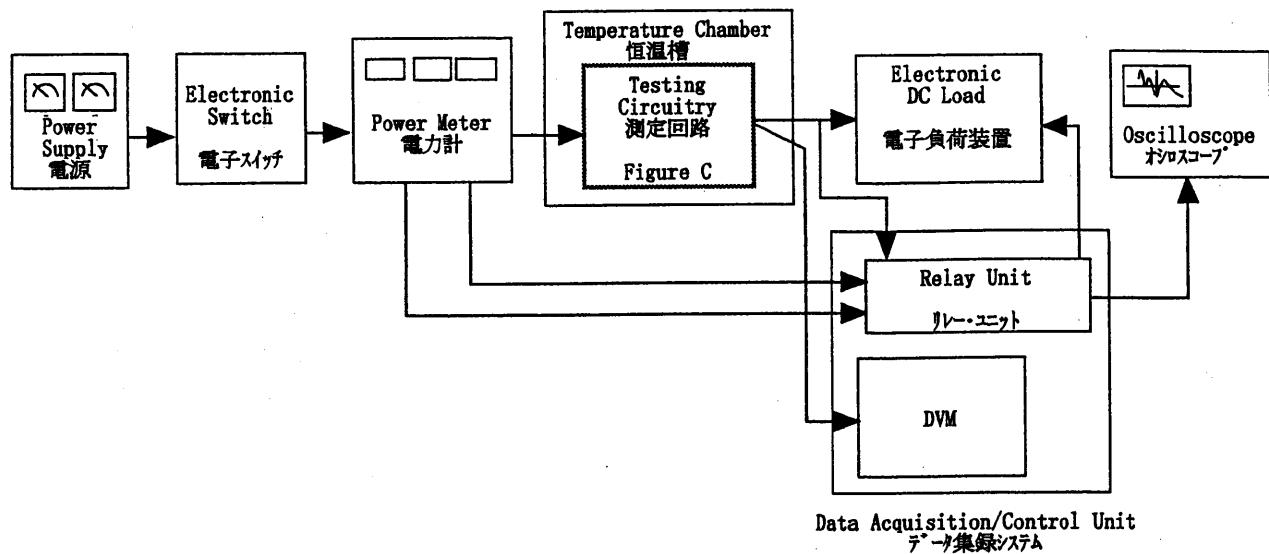


Figure A

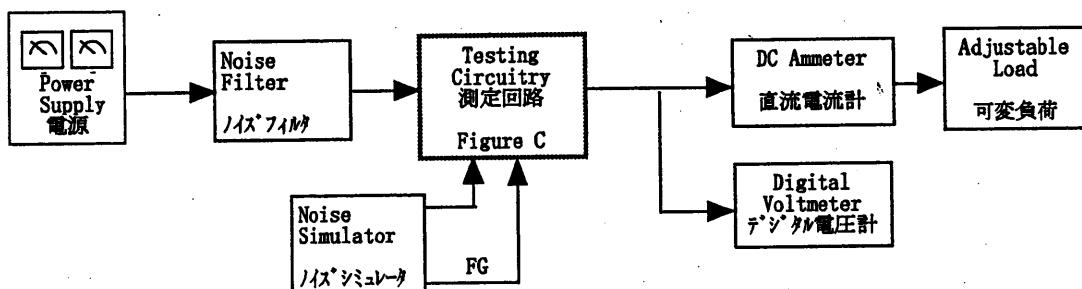
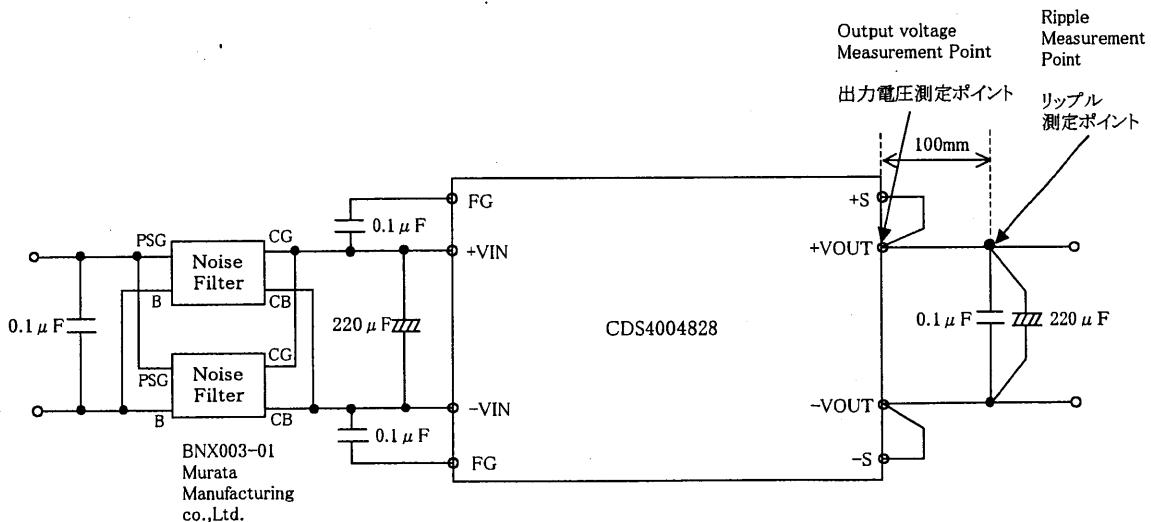


Figure B

Figure C (General Electric Characteristic)  
一般電気特性