



TEST DATA OF MODULE L

(RB series)

Regulated DC Power Supply
November 5, 2018

Approved by : Jun Uchida
Jun Uchida Design Manager

Prepared by : Hideaki Douguchi
Hideaki Douguchi Design Engineer

COSEL CO.,LTD.



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COSEL																																			
Model	MODULE L	Temperature	25°C																																
Item	Line Regulation	Testing Circuitry	Figure A																																
Object	+24V1.3A																																		
<p>1.Graph</p> <p style="text-align: right;"> ---□--- Load 50% —△— Load 100% </p>		<p>2.Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Input Voltage [V]</th> <th colspan="2">Output Voltage [V]</th> </tr> <tr> <th>Load 50%</th> <th>Load 100%</th> </tr> </thead> <tbody> <tr><td>85</td><td>24.324</td><td>24.321</td></tr> <tr><td>90</td><td>24.324</td><td>24.321</td></tr> <tr><td>100</td><td>24.324</td><td>24.321</td></tr> <tr><td>120</td><td>24.324</td><td>24.321</td></tr> <tr><td>200</td><td>24.325</td><td>24.321</td></tr> <tr><td>230</td><td>24.324</td><td>24.321</td></tr> <tr><td>264</td><td>24.324</td><td>24.321</td></tr> <tr><td>280</td><td>24.324</td><td>24.321</td></tr> <tr><td>--</td><td>-</td><td>-</td></tr> </tbody> </table>		Input Voltage [V]	Output Voltage [V]		Load 50%	Load 100%	85	24.324	24.321	90	24.324	24.321	100	24.324	24.321	120	24.324	24.321	200	24.325	24.321	230	24.324	24.321	264	24.324	24.321	280	24.324	24.321	--	-	-
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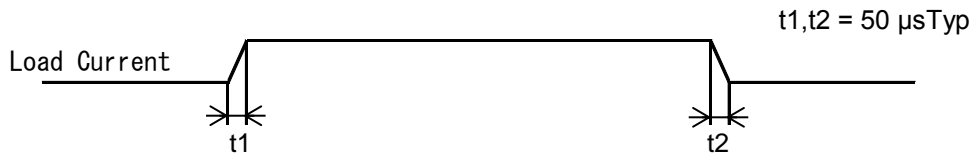


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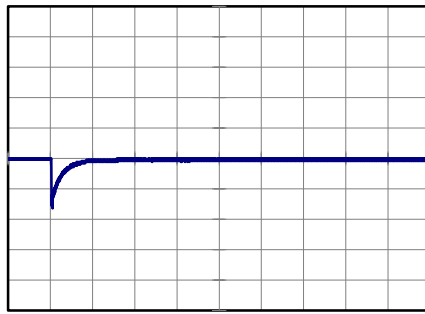
Model	MODULE L	Temperature 25°C Testing Circuitry Figure A
Item	Dynamic Load Response	
Object	+24V1.3A	

Input Volt. 100 V
Cycle 1000 ms

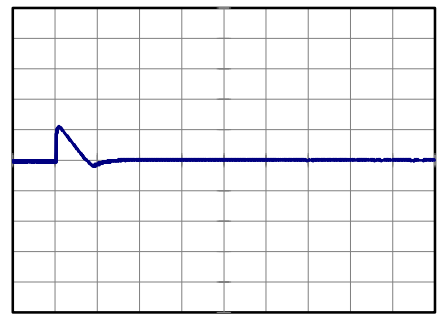


Min. Load (0A) ←→
Load 100% (1.3A)

200 mV/div



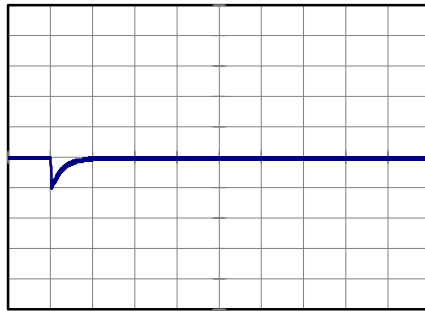
10 ms/div



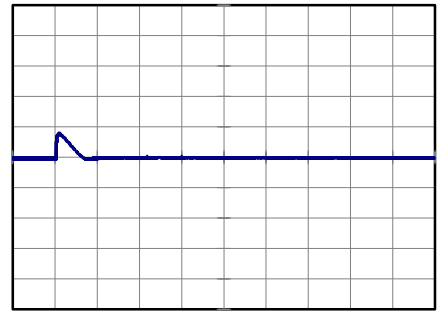
10 ms/div

Min. Load (0A) ←→
Load 50% (0.65A)

200 mV/div



10 ms/div



10 ms/div



COSEL																																									
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Item	Ripple Voltage (by Load Current)	Testing Circuitry	Figure B																																						
Object	+24V1.3A																																								
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<p>Note: Slanted line shows the range of the rated ambient temperature.</p>																																																						



COSEL		Testing Circuitry Figure A
Model	MODULE L	
Item	Output Voltage Accuracy	
Object	+24V1.3A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -20 - 50°C

Input Voltage : 85 - 264V

Load Current : 0 - 1.3A

* Output Voltage Accuracy = $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

* Output Voltage Accuracy (Ratio) = $\frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$

2. Values

Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ratio [%]
Maximum Voltage	50	85	0	24.329	±22	±0.1
Minimum Voltage	-20	85	1.3	24.285		



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Item	Time Lapse Drift	Testing Circuitry	Figure A																						
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<p>1.Graph</p> <div style="display: flex; align-items: flex-start;"> <div style="margin-right: 20px;"> <p>—△ Input Volt. 100V</p> <p>—□ Input Volt. 200V</p> <p>—○ Input Volt. 230V</p> </div> </div> <p style="text-align: center;">Load Current [A]</p> <p>Note: Slanted line shows the range of the rated load current.</p> <p>Intermittent operation occurs when overcurrent protection is activated.</p>		<p>2.Values</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th rowspan="2">Output Voltage [V]</th> <th colspan="3">Load Current [A]</th> </tr> <tr> <th>Input Volt. 100[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 230[V]</th> </tr> </thead> <tbody> <tr> <td>24</td> <td>1.56</td> <td>1.56</td> <td>1.56</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> <td>-</td> </tr> </tbody> </table>	Output Voltage [V]	Load Current [A]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	24	1.56	1.56	1.56	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-	--	-	-	-
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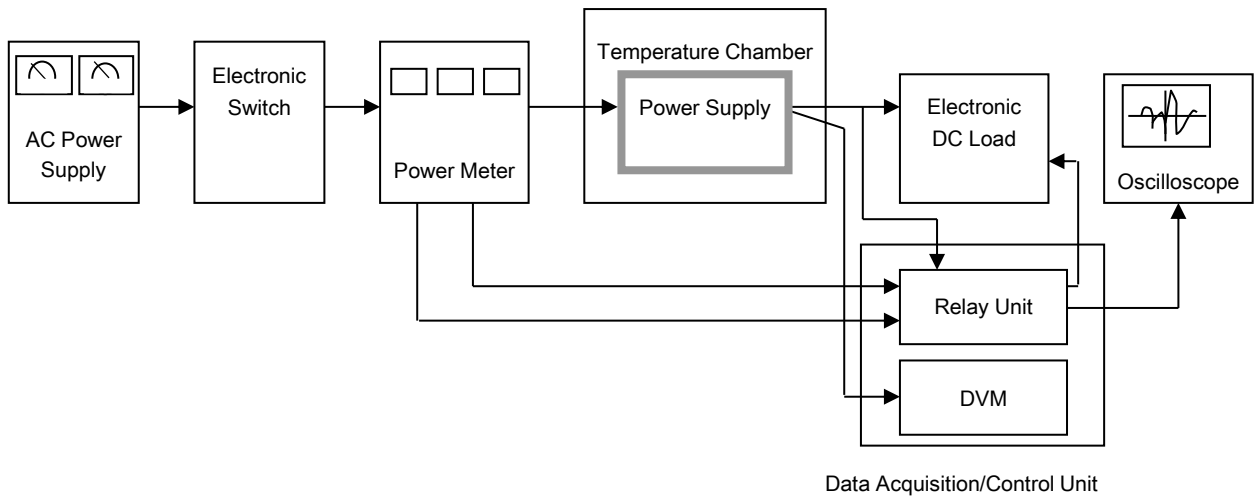
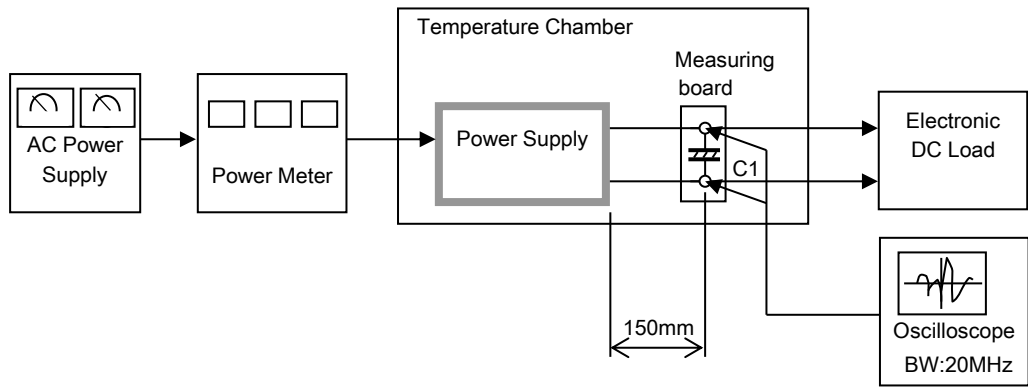


Figure A



C1= 22 μ F
(Electrolytic capacitor)

Figure B