参考資料

COSEL

TEST DATA OF PAA75F-15
(100V INPUT)

Regulated DC Power Supply

Date : Feb. 17. 1997

Approved by : M. Tanikawa
Design Manager

Prepared by : T. Amei
Design Engineer

コーセル株式会社

COSEL CO., LTD.

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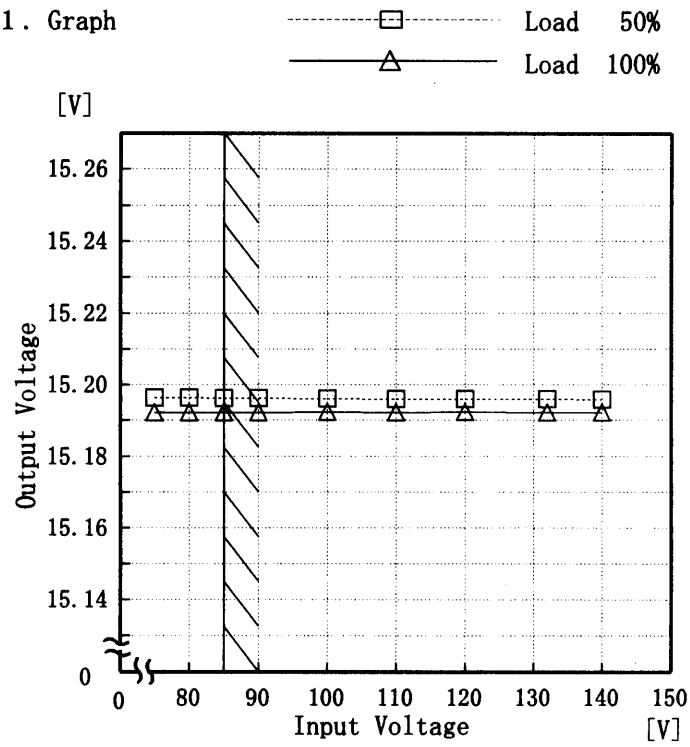
Model	PAA75F-15
Item	Line Regulation 静的入力変動
Object	+15V5.0A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Load 50% Output Volt. [V]	Load 100% Output Volt. [V]
75	15.196	15.192
80	15.196	15.192
85	15.196	15.192
90	15.196	15.192
100	15.196	15.192
110	15.196	15.192
120	15.196	15.192
132	15.196	15.192
140	15.196	15.192

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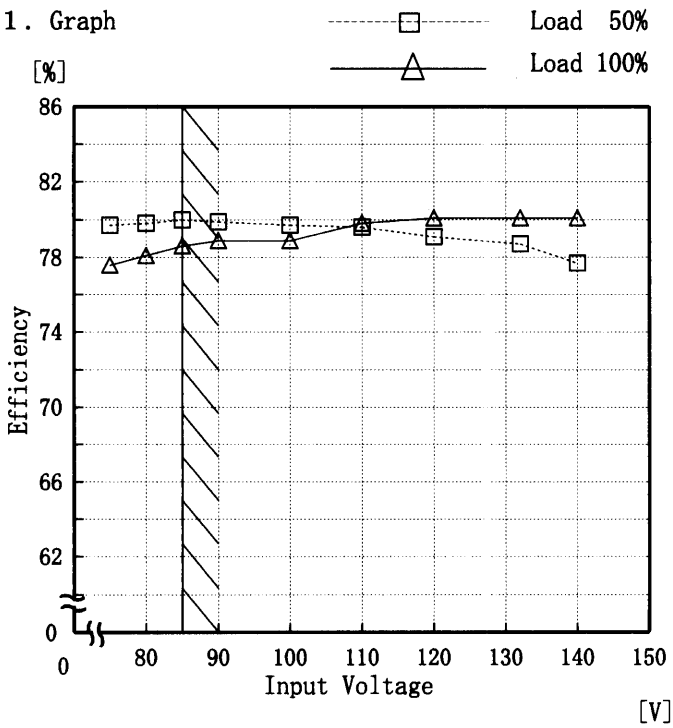
Model	PAA75F-15
Item	Efficiency 効率
Object	+15V5.0A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Load 50% Efficiency [%]	Load 100% Efficiency [%]
75	79.7	77.6
80	79.8	78.1
85	80.0	78.6
90	79.9	78.9
100	79.7	78.9
110	79.6	79.8
120	79.1	80.1
132	78.7	80.1
140	77.7	80.1

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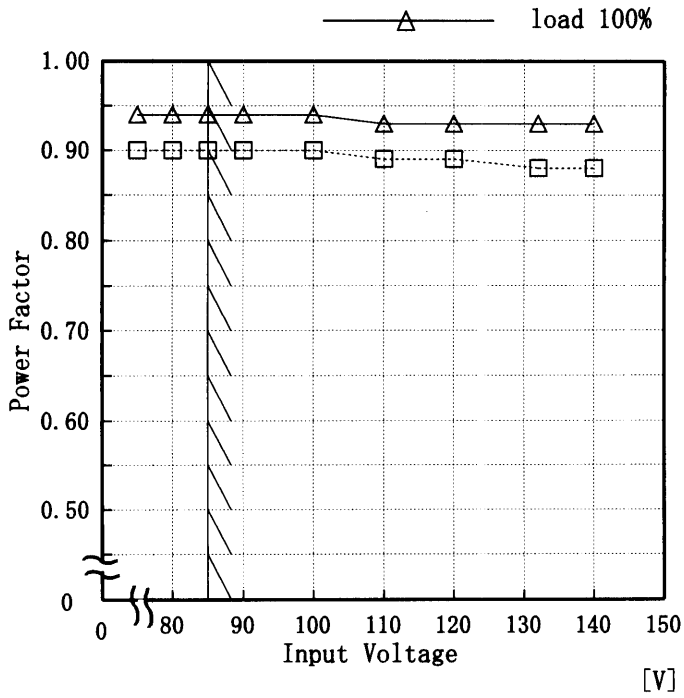
Model	PAA75F-15
Item	Power Factor 力率
Object	+15V5.0A

Temperature 3℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	load 50% Power Factor	load 100% Power Factor
75	0.90	0.94
80	0.90	0.94
85	0.90	0.94
90	0.90	0.94
100	0.90	0.94
110	0.89	0.93
120	0.89	0.93
132	0.88	0.93
140	0.88	0.93

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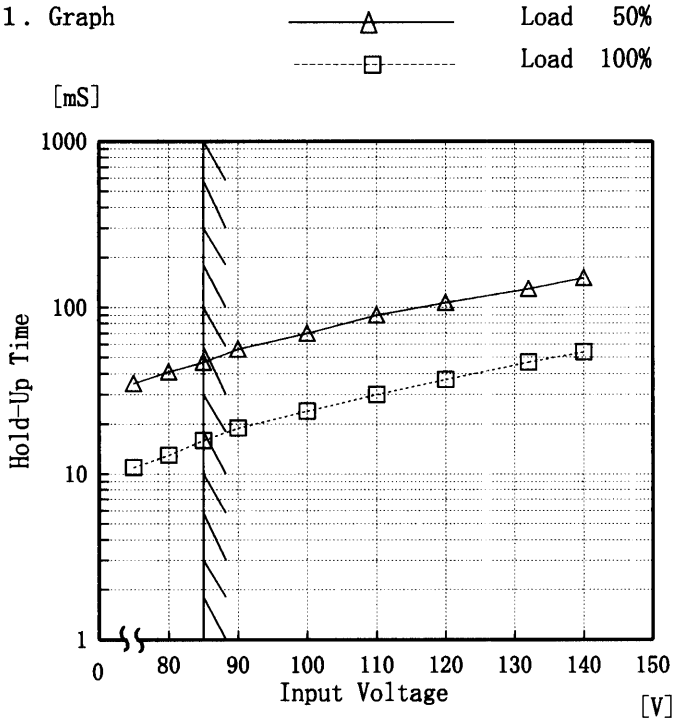
Model	PAA75F-15
Item	Hold-Up Time　出力保持時間
Object	+15V5.0A

Temperature　25℃
Testing Circuitry　Figure A

1. Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated input voltage.

出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Load 50% Hold-Up Time [mS]	Load 100% Hold-Up Time [mS]
75	35	11
80	41	13
85	47	16
90	56	19
100	70	24
110	90	30
120	107	37
132	129	47
140	151	54

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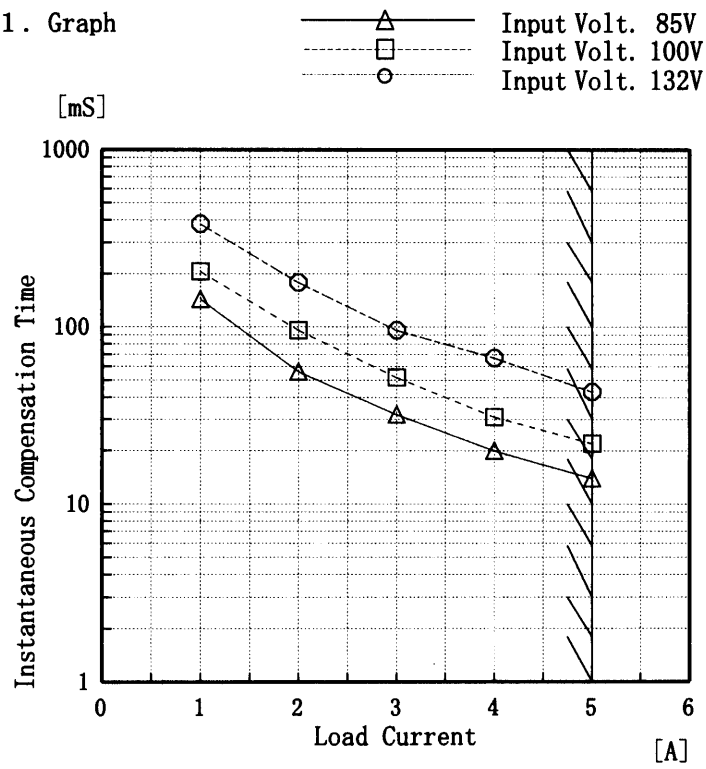
Model	PAA75F-15
Item	Instantaneous Interruption Compensation 瞬時停電保障
Object	+15V5.0A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



This duration counts between Shut-off and on of input voltage automatically.

Note: Slanted line shows the range of the rated load current.

瞬時停電保障時間とは、出力電圧が定格値の９５％になる時の瞬時停電時間をいう。

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
	Time [mS]		
0.0	—	—	—
1.0	144	206	381
2.0	56	96	179
3.0	32	52	96
4.0	20	31	67
5.0	14	22	43
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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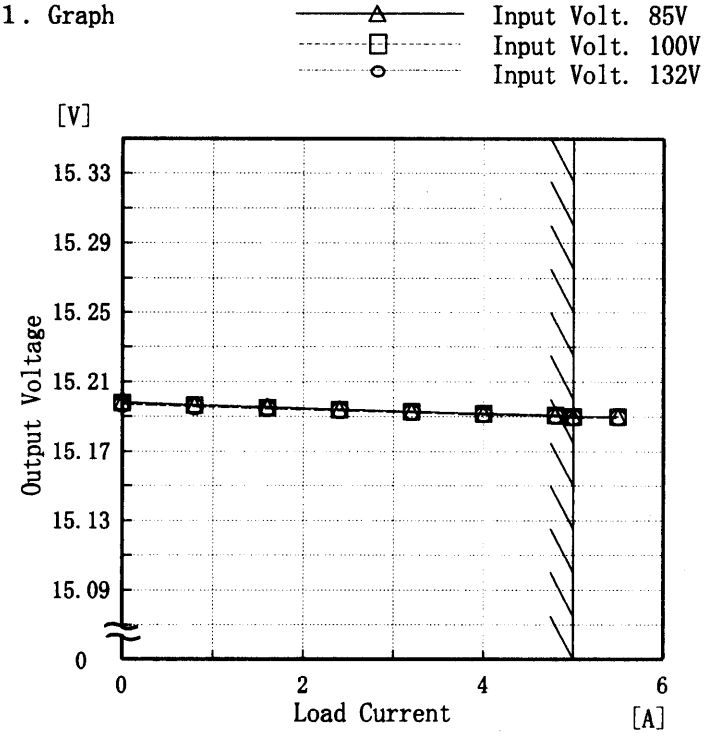
Model	PAA75F-15
Item	Load Regulation 静的負荷変動
Object	+15V5.0A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Input Volt. 85[V] Output Volt. [V]	Input Volt. 100[V] Output Volt. [V]	Input Volt. 132[V] Output Volt. [V]
0.0	15.198	15.198	15.197
0.8	15.197	15.196	15.196
1.6	15.196	15.195	15.195
2.4	15.194	15.194	15.194
3.2	15.193	15.193	15.193
4.0	15.192	15.192	15.191
4.8	15.191	15.191	15.191
5.0	15.190	15.190	15.190
5.5	15.190	15.190	15.190
—	—	—	—

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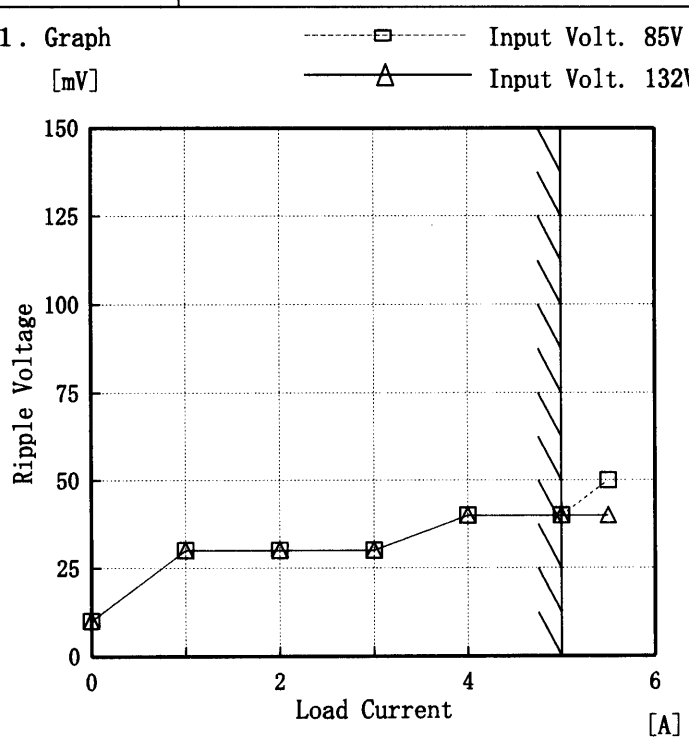
Model	PAA75F-15
Item	Ripple Voltage(by Load Current) リップル電圧(負荷電流特性)
Object	+15V5.0A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Ripple Voltage is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

リップル電圧は、下図ｐ－ｐ値で示される。

(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
ｽｲｯﾁﾝｸﾞ周期

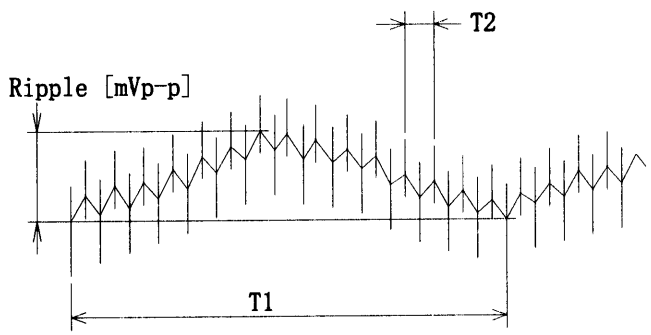


Fig. Complex Ripple Wave Form
図 リップル波形詳細図

2. Values

Load Current [A]	Input Volt. 85 [V] Ripple Output Volt. [mV]	Input Volt. 132 [V] Ripple Output Volt. [mV]
0.0	10	10
1.0	30	30
2.0	30	30
3.0	30	30
4.0	40	40
5.0	40	40
5.5	50	40
—	—	—
—	—	—
—	—	—
—	—	—

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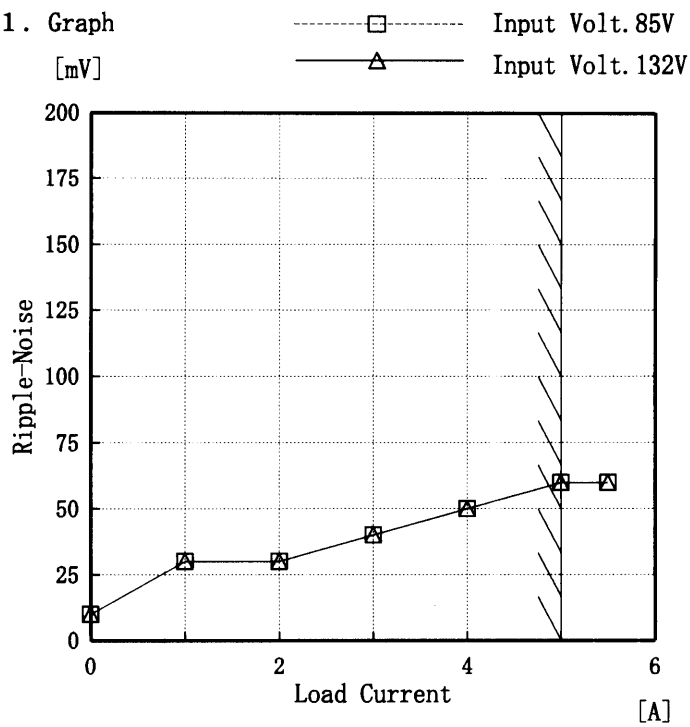
Model	PAA75F-15
Item	Ripple-Noise リップルノイズ
Object	+15V5.0A

Temperature 1℃
Testing Circuitry Figure A

1. Graph



Ripple-Noise is shown as p-p in the figure below.
Note: Slanted line shows the range of the rated load current.

リップルノイズは、下図ｐ－ｐ値で示される。
(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
ｽｲｯﾁﾝｸﾞ周期

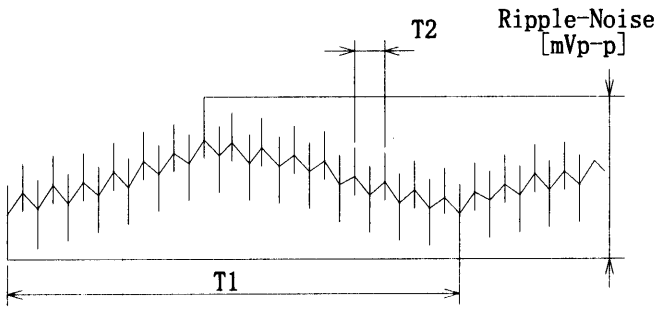


Fig. Complex Ripple Wave Form
図 リップル波形詳細図

2. Values

Load current [A]	Input Volt. 85 [V] Ripple-Noise [mV]	Input Volt. 132 [V] Ripple-Noise [mV]
0.0	10	10
1.0	30	30
2.0	30	30
3.0	40	40
4.0	50	50
5.0	60	60
5.5	60	60
—	—	—
—	—	—
—	—	—
—	—	—

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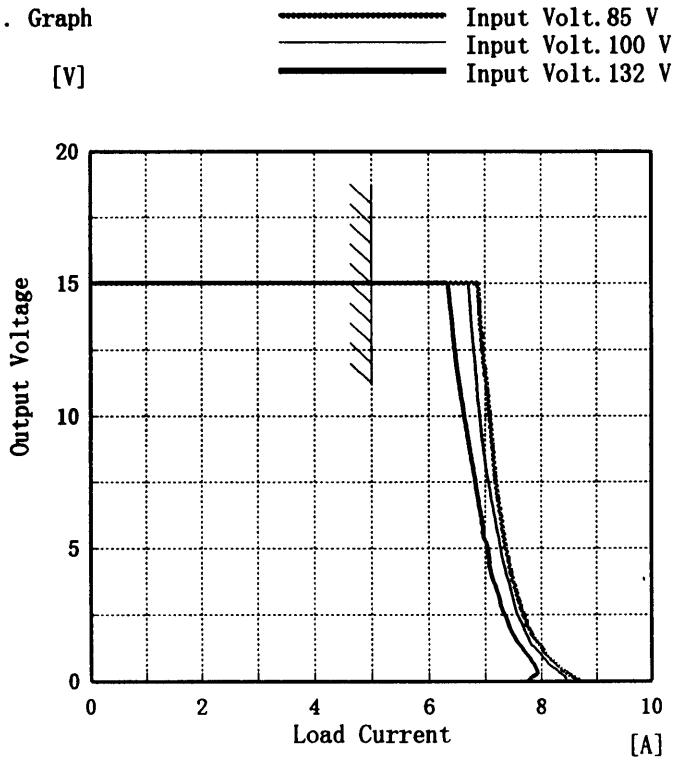
Model	PAA75F-15
Item	Overcurrent Protection 過電流保護
Object	+15V5.0A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Output Voltage [V]	Input Volt. 85[V] Load Current [A]	Input Volt. 100[V] Load Current [A]	Input Volt. 132[V] Load Current [A]
15.00	6.87	6.70	6.35
14.25	6.89	6.73	6.38
13.50	6.91	6.75	6.42
12.00	6.98	6.82	6.50
10.50	7.06	6.88	6.60
9.00	7.13	6.95	6.72
7.50	7.19	7.06	6.84
6.00	7.30	7.19	6.96
4.50	7.43	7.33	7.07
3.00	7.61	7.50	7.27
1.50	7.92	7.79	7.58
0.00	8.58	8.41	7.84

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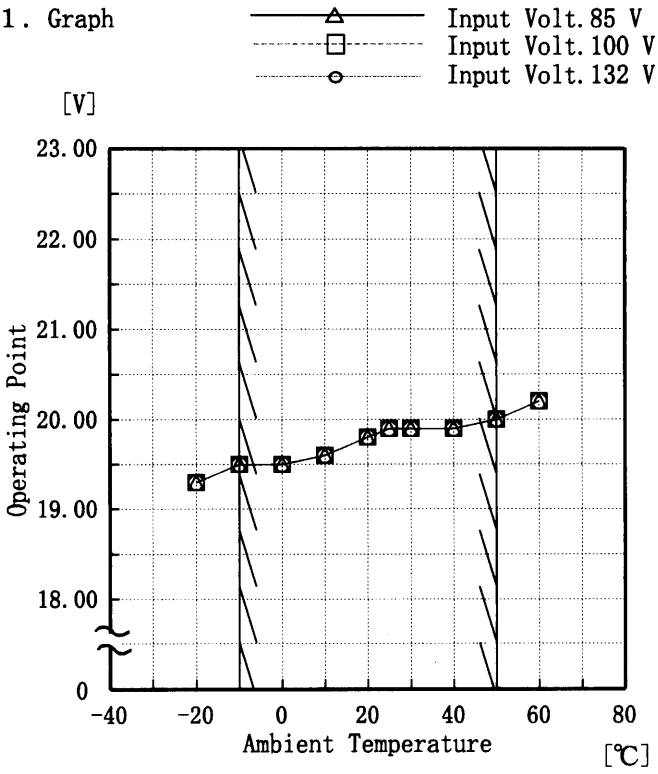
Model	PAA75F-15
Item	Overvoltage Protection 過電圧保護
Object	+15V5.0A

Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

Ambient Temp. [℃]	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
	Operating Point [V]		
-20	19.30	19.30	19.30
-10	19.50	19.50	19.50
0	19.50	19.50	19.50
10	19.60	19.60	19.60
20	19.80	19.80	19.80
25	19.90	19.90	19.90
30	19.90	19.90	19.90
40	19.90	19.90	19.90
50	20.00	20.00	20.00
60	20.20	20.20	20.20
—	—	—	—

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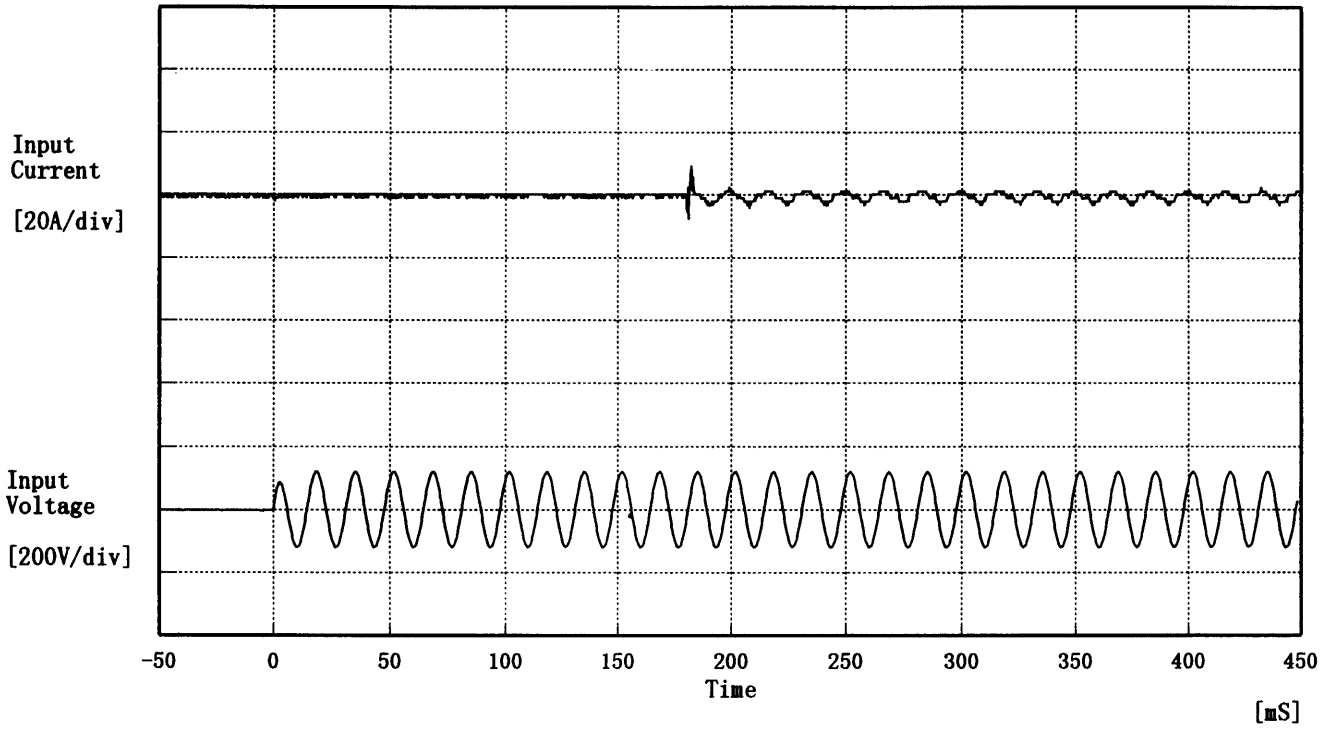
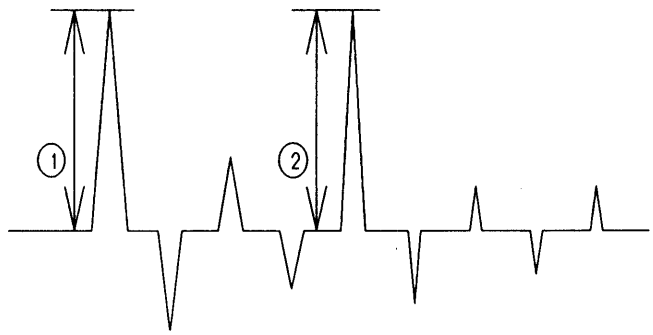
Model	PAA75F-15
Item	Inrush Current 突入電流
Object	+15V5.0A

Temperature 25℃
Testing Circuitry Figure A



Input Voltage 100 V
Frequency 60 Hz
Load 100 %
Inrush Current
① 2.05 [A]
② 9.17 [A]



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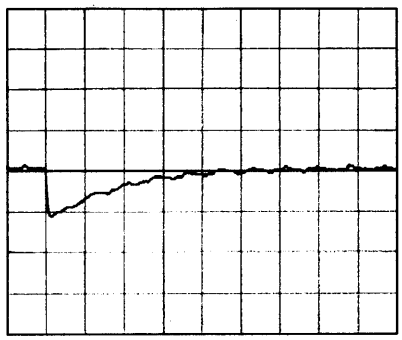
Model	PAA75F-15		
Item	Dynamic Load Responce 動的負荷変動	Temperature Testing Circuitry	25℃ Figure A
Object	+15V5.0A		

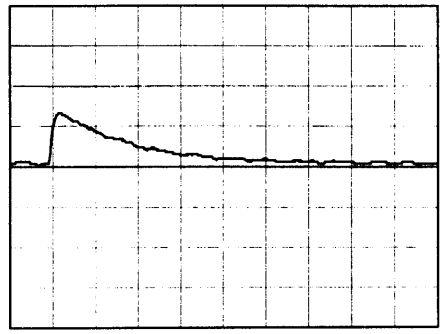
Input Volt. 100 V
Cycle 200 mS

Load Current

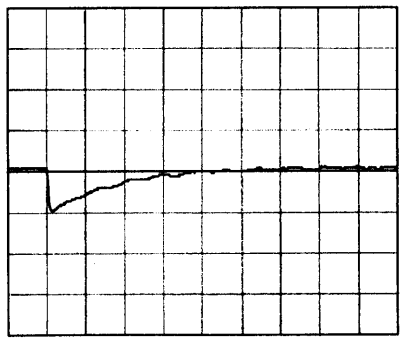
t 10 mS/div
Vo 100 mV/div

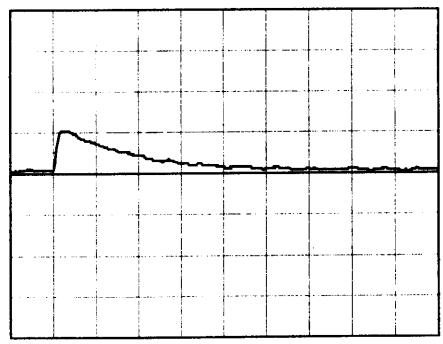
Min. Load ←→
Load 100 %





Min. Load ←→
Load 50 %





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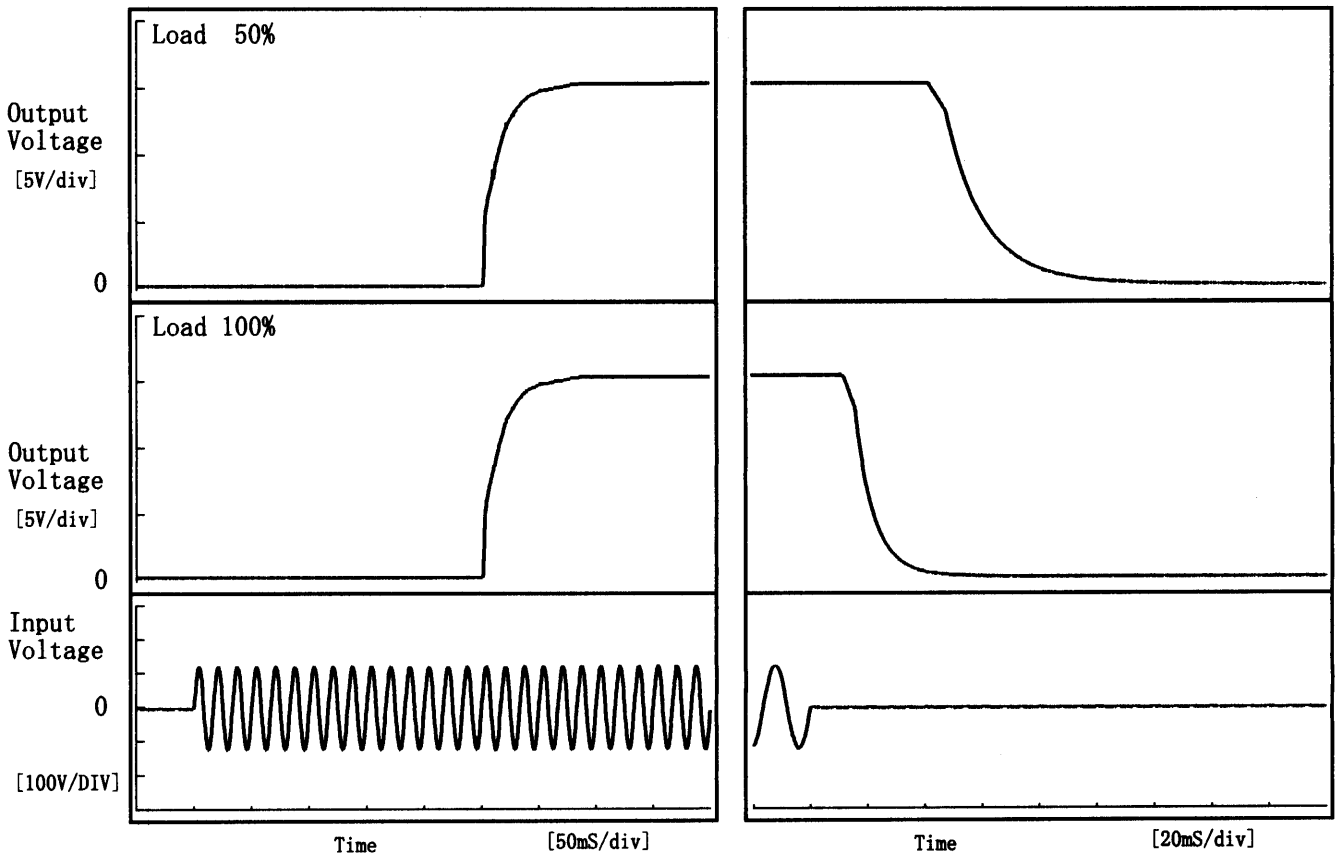
Model	PAA75F-15		
Item	Rise and Fall Time 立上り、立下り時間	Temperature	25℃
		Testing Circuitry	Figure A
Object	+15V5.0A		

1. Graph

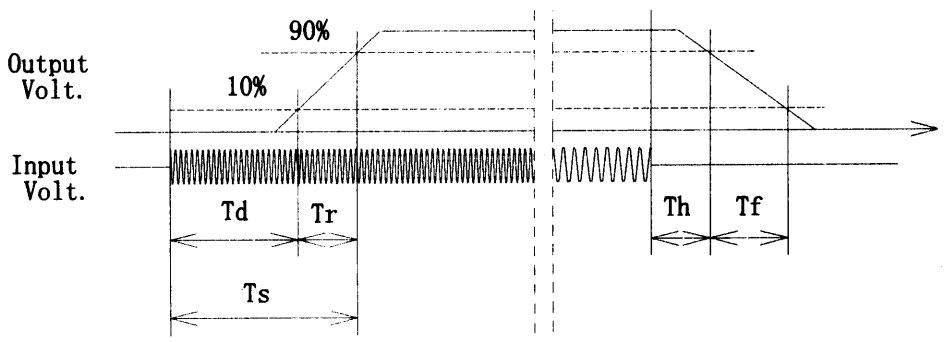
Input Volt. 85 V



2. Values

[mS]

Load \ Time	T d	T r	T s	T h	T f
50 %	251.8	32.0	283.8	46.4	32.0
100 %	251.8	32.5	284.3	14.5	16.2



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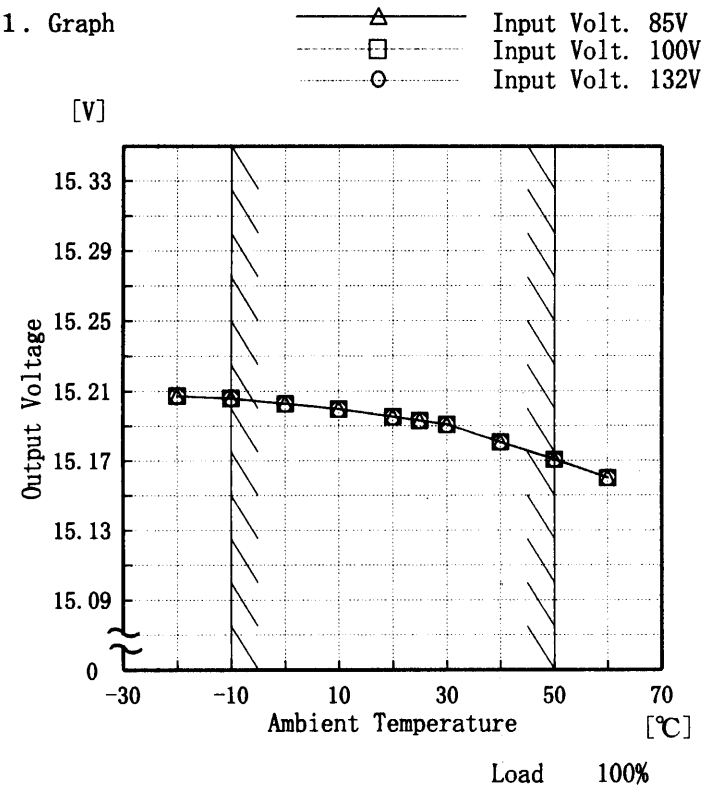
Model	PAA75F-15
Item	Ambient Temperature Drift 周囲温度変動
Object	+15V5.0A

Testing Circuitry Figure A

1. Graph



Load 100%

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

Temperature [℃]	Input Volt. 85[V] Output Volt. [V]	Input Volt. 100[V] Output Volt. [V]	Input Volt. 132[V] Output Volt. [V]
-20	15.207	15.207	15.207
-10	15.206	15.206	15.206
0	15.203	15.203	15.203
10	15.200	15.200	15.200
20	15.195	15.195	15.195
25	15.193	15.193	15.193
30	15.191	15.191	15.191
40	15.181	15.181	15.181
50	15.171	15.171	15.171
60	15.160	15.160	15.160
—	—	—	—

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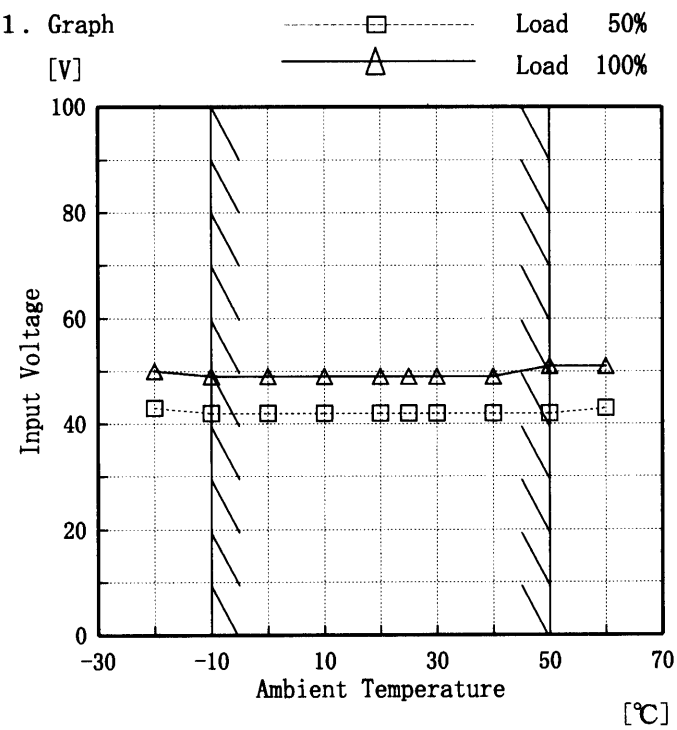
Model	PAA75F-15
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+15V5.0A

Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

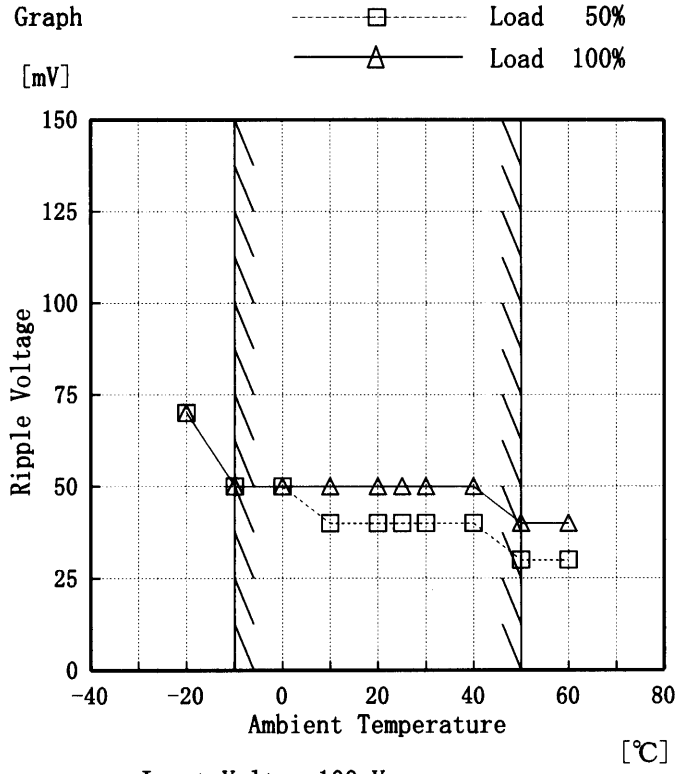
Ambient Temp. [℃]	Load 50% Input Volt. [V]	Load 100% Input Volt. [V]
-20	43	50
-10	42	49
0	42	49
10	42	49
20	42	49
25	42	49
30	42	49
40	42	49
50	42	51
60	43	51
—	—	—

COSEL

Model	PAA75F-15
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)
Object	+15V5.0A

Testing Circuitry Figure A

1. Graph



Input Volt. 100 V

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

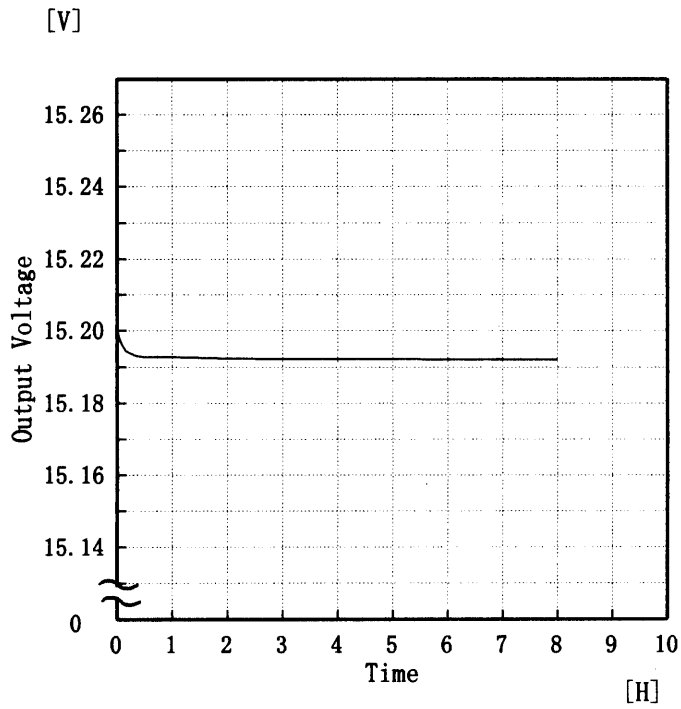
Ambient Temp. [℃]	Load 50% Ripple Output Volt. [mV]	Load 100% Ripple Output Volt. [mV]
-20	70	70
-10	50	50
0	50	50
10	40	50
20	40	50
25	40	50
30	40	50
40	40	50
50	30	40
60	30	40
—	—	—

COSEL

Model	PAA75F-15
Item	Time Lapse Drift 経時ドリフト
Object	+15V5.0A

Temperature 25 ℃
Testing Circuitry Figure A

1. Graph



Input Volt. 100V
Load 100%

2. Values

Time since start [H]	Output Voltage [V]
0.0	15.202
0.5	15.193
1.0	15.193
2.0	15.192
3.0	15.192
4.0	15.192
5.0	15.192
6.0	15.192
7.0	15.192
8.0	15.192

COSEL

Model	PAA75F-15	
Item	Output Voltage Accuracy　定電圧精度	Testing Circuitry　Figure A
Object	+15V5.0A	

Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature　: -10～50 ℃
Input Voltage : 85～132 V
Load Current　: 0.0～5.0 A

* Output Voltage Accuracy = ±(Maximum of Output Voltage　− Minimum of Output Voltage)／2

$$* \text{ Output Voltage Accuracy (Ration)} = \frac{\text{Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

定電圧精度

周囲温度、入力電圧、負荷を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度　-10～50 ℃
入力電圧　85～132 V
負荷電流　0.0～5.0 A

* 定電圧精度(変動値) ＝ ±(出力電圧の最高値−出力電圧の最低値)／2

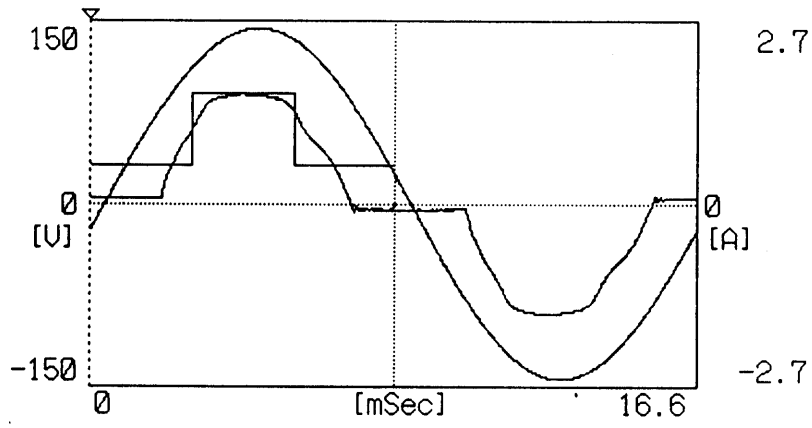
$$* \text{ 定電圧精度(変動率)} = \frac{\text{変動値}}{\text{定格出力電圧}} \times 100$$

Item	Temperature [℃]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy (Ration) [%]
Maximum Voltage	-10	85	0.0	15.211	±22	±0.2
Minimum Voltage	50	132	5.0	15.169		

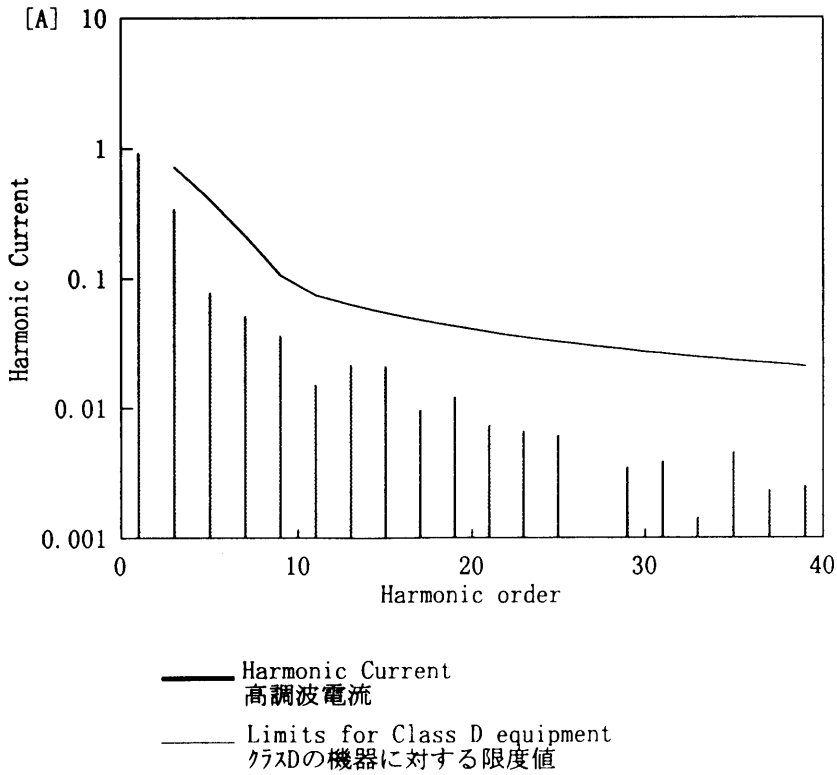
COSEL

Model	PAA75F-15		
Item	Harmonic Current 高調波電流	Temperature Testing Circuitry	25℃ Figure E
Object	+15V5.0A		

Conditions	Values
Input Voltage [V]	100
Input Current [A]	1
Active Power [W]	91.9
Apparent Power[VA]	99.4
Frequency [Hz]	60
Power Factor	0.925
Output Power [W]	75



2. Harmonic Current



Harmonics order 高調波次数	Limits 限度値 [A]	Values 測定値 [A]
1	—	0.930
2	—	0.000
3	0.719	0.341
4	—	0.000
5	0.402	0.078
6	—	0.000
7	0.211	0.052
8	—	0.000
9	0.106	0.037
10	—	0.000
11	0.074	0.015
12	—	0.000
13	0.063	0.022
14	—	0.000
15	0.054	0.021
16	—	0.000
17	0.048	0.010
18	—	0.000
19	0.043	0.012
20	—	0.000
21	0.039	0.007
22	—	0.000
23	0.035	0.007
24	—	0.000
25	0.033	0.006
26	—	0.000
27	0.030	0.001
28	—	0.000
29	0.028	0.004
30	—	0.000
31	0.026	0.004
32	—	0.000
33	0.025	0.001
34	—	0.000
35	0.023	0.005
36	—	0.000
37	0.022	0.002
38	—	0.000
39	0.021	0.003
40	—	0.000

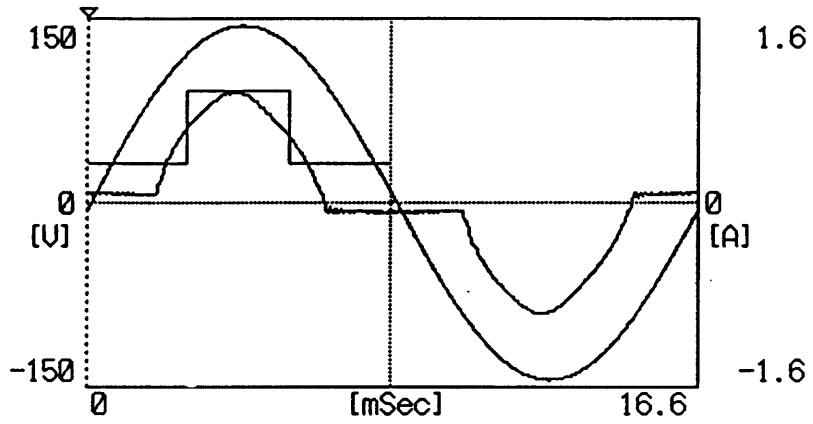
BC－0537

COSEL

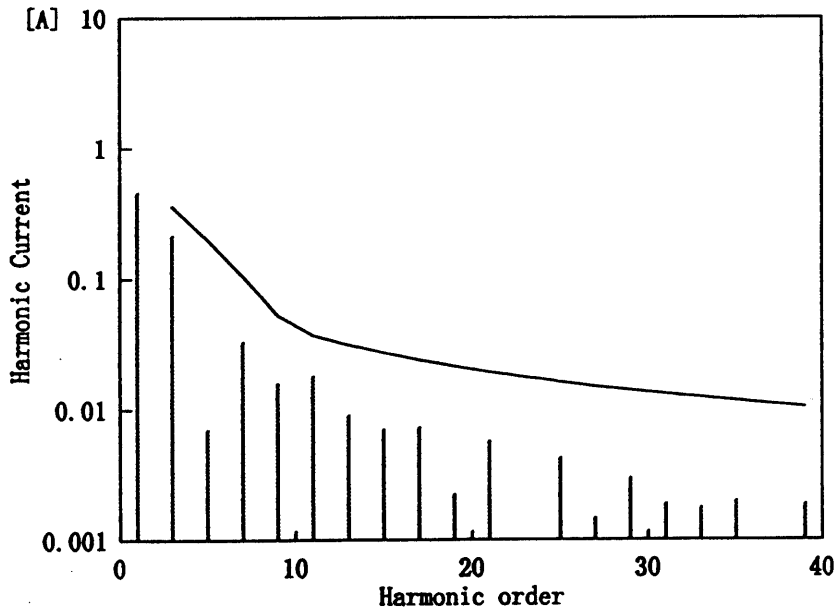
Model	PAA75F-15
Item	Harmonic Current 高調波電流
Object	+15V5.0A

Temperature 25℃
Testing Circuitry Figure E

Conditions	Values
Input Voltage [V]	100
Input Current [A]	0.52
Active Power [W]	45.8
Apparent Power[VA]	51.6
Frequency [Hz]	60
Power Factor	0.888
Output Power [W]	37.5



2. Harmonic Current



Harmonic Current
高調波電流

Limits for Class D equipment
ｸﾗｽDの機器に対する限度値

Harmonics order 高調波次数	Limits 限度値 [A]	Values 測定値 [A]
1	—	0.464
2	—	0.000
3	0.358	0.218
4	—	0.000
5	0.200	0.007
6	—	0.000
7	0.105	0.033
8	—	0.000
9	0.053	0.016
10	—	0.000
11	0.037	0.018
12	—	0.000
13	0.031	0.009
14	—	0.000
15	0.027	0.007
16	—	0.000
17	0.024	0.007
18	—	0.000
19	0.021	0.002
20	—	0.000
21	0.019	0.006
22	—	0.000
23	0.018	0.001
24	—	0.000
25	0.016	0.004
26	—	0.000
27	0.015	0.001
28	—	0.000
29	0.014	0.003
30	—	0.000
31	0.013	0.002
32	—	0.000
33	0.012	0.002
34	—	0.000
35	0.012	0.002
36	—	0.000
37	0.011	0.001
38	—	0.000
39	0.010	0.002
40	—	0.000

COSEL

Model	PAA75F-15
Item	Condensation 結露特性
Object	+15V5.0A

Testing Circuitry Figure A

1. Condensation test

Testing procedure is as follows.

① Keeping and cooling the unit in a tank at -10℃ for an hour with the input off.

② Taking it out of the tank and dewing itself in a room where the temperature is 25℃ and the humidity is 40%RH.

③ Testing electrical characteristics(Output Voltage, Ripple Voltage, Ripple noise) of the unit to confirm there be no fault.

④ Repeating ①, ② and ③ three times.

1．結露特性試験

入力を切った状態で、恒温槽で－１０℃に冷却しておき、約１時間後に恒温槽から取り出し、室温２５℃、湿度４０%RHの状態におき結露させ、その電気的特性（出力電圧、ﾘｯﾌﾟﾙ、ﾘｯﾌﾟﾙﾉｲｽﾞ）の測定を３度行い、異常のないことを確認する。

2．Values

	Times	Output Voltage [V]	Ripple Voltage [mV]	Ripple Noise [mV]
Load 50 %	1	15.166	40	50
	2	15.166	40	50
	3	15.165	40	50
Load 100 %	1	15.160	40	50
	2	15.161	40	50
	3	15.161	40	50

Input Volt. 100 V

COSEL

Model	PAA75F-15
Item	Leakage Current 漏洩電流
Object	+15V5.0A

Testing Circuitry Figure A

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132[V]
(A) DENTORI	0.18	0.18	0.20
(B) UL	0.11	0.14	0.18
(C) CSA	0.11	0.14	0.18

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 220 [V]	Input Volt. 264 [V]
(D) VDE	-	-	-

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力の両相について測定し、その大きい方を漏洩電流測定値とする。

Load 100 %

(A) Input Resistance :1KΩ

(B) Input Resistance :1.5KΩ
Input Capacitance :0.15μF

(C) Input Resistance :1.5KΩ
Input Capacitance :0.15μF

(D) Input Resistance :2KΩ
Input Capacitance :0.1μF

COSEL

Model	PAA75F-15
Item	Line Noise Tolerance 入力雑音耐量
Object	+15V5.0A

Testing Circuitry Figure C

1. Results

Pulse Width [nS]	MODE	Operating Point of Overvoltage Protection [V] 過電圧保護動作値	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	19.20	no regulation
	NORMAL	10.20	no regulation
1000	COMMON	19.30	no regulation
	NORMAL	19.30	no regulation

Conditions

Input Voltage :100 V
Pulse Voltage :2000 V
Pulse Cycle :10 mS
Pulse Input Duration :1 min. or more
Load :100 %

COSEL

Model	PAA75F-15
Item	Conducted Emission 雑音端子電圧
Object	+15V5.0A

Testing Circuitry Figure D

1. Graph

Remarks

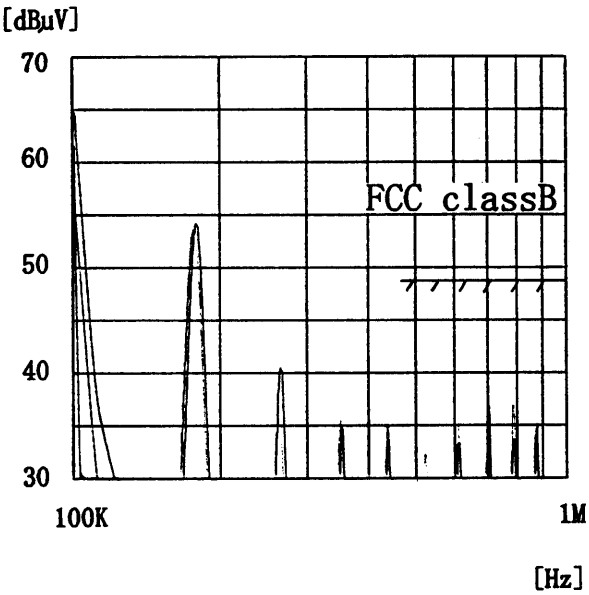
Input Volt. 120 V

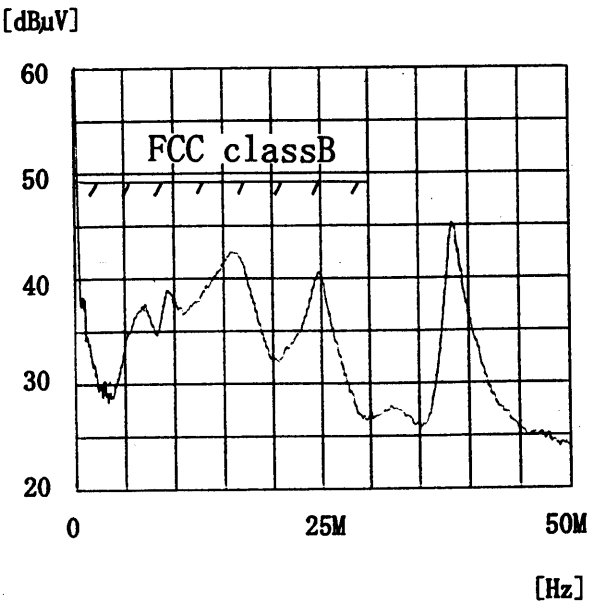
Load 100 %

Note: Slanted line shows the range of Tolerance.

(注)斜線は許容値を示す。

NO	Standards	Standards Complied	Frequency [MHz]	Tolerance [dB/μV]
1	FCC class A		0.45～1.6	60
			1.6～30	69.5
2	FCC class B	○	0.45～30	48
3	VCCI -1		0.15～0.5	79
			0.5～30	73
4	VCCI -2	○	0.15～0.5	66-56
			0.5～5	56
			5～30	60
5	CISPR22-A		0.01～0.15	91-69.5
			0.15～0.5	66
			0.5～30	60
6	CISPR22-B		0.01～0.05	110
			0.05～0.15	90-80
			0.15～0.5	66-56
			0.5～5	56
			5～30	60





COSEL

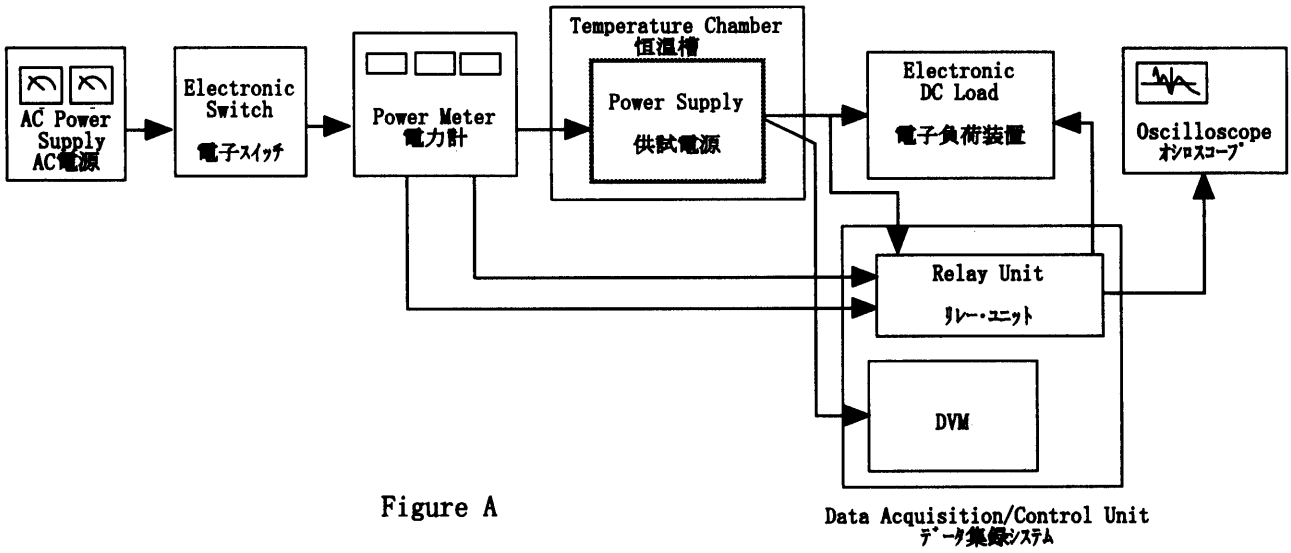


Figure A

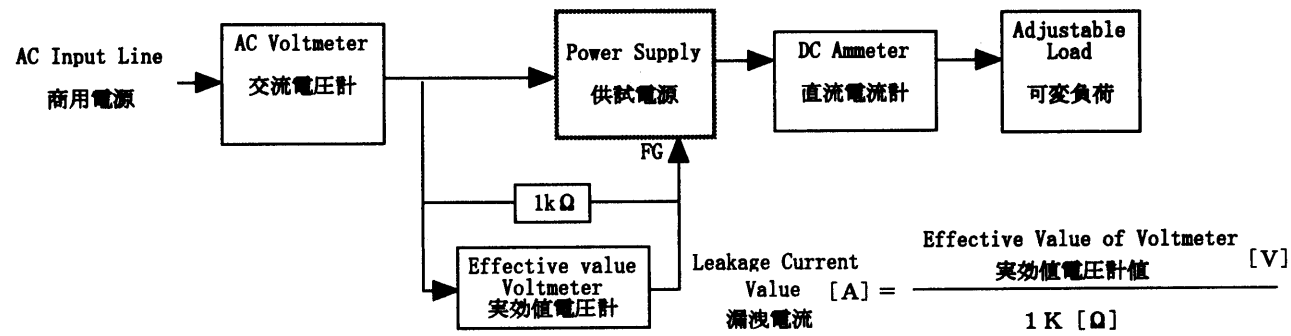


Figure B(DENTORI)

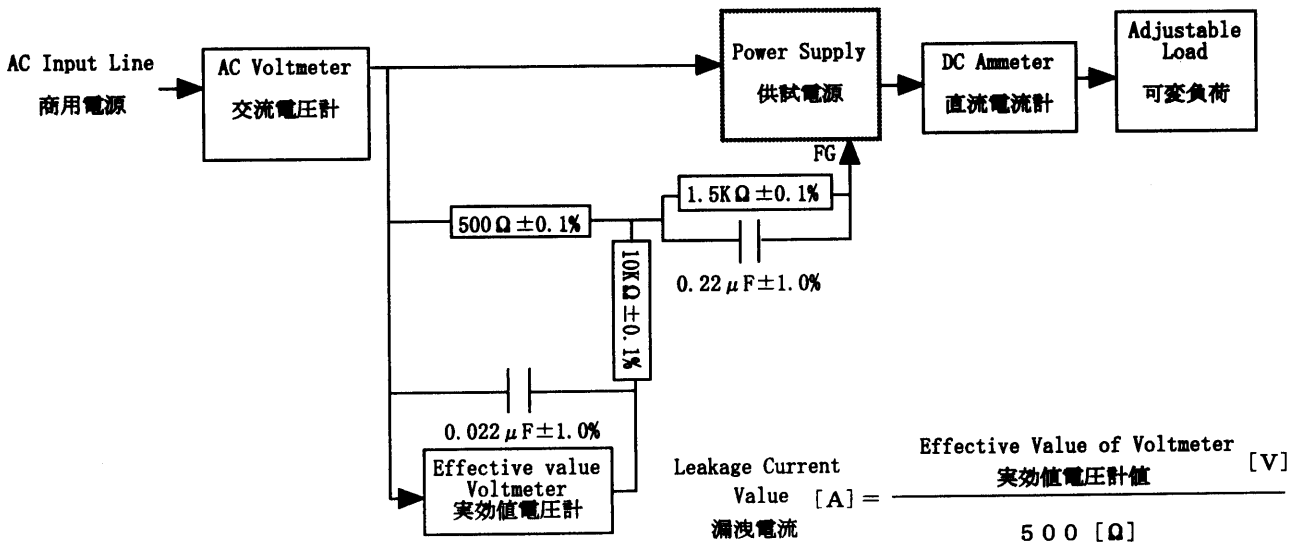


Figure B (UL, CSA, VDE)

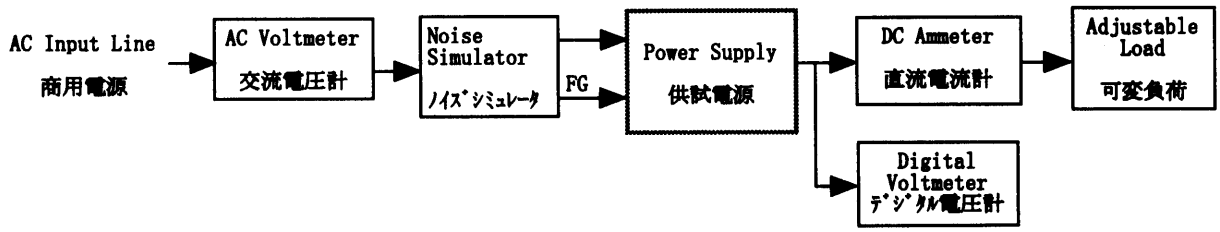


Figure C

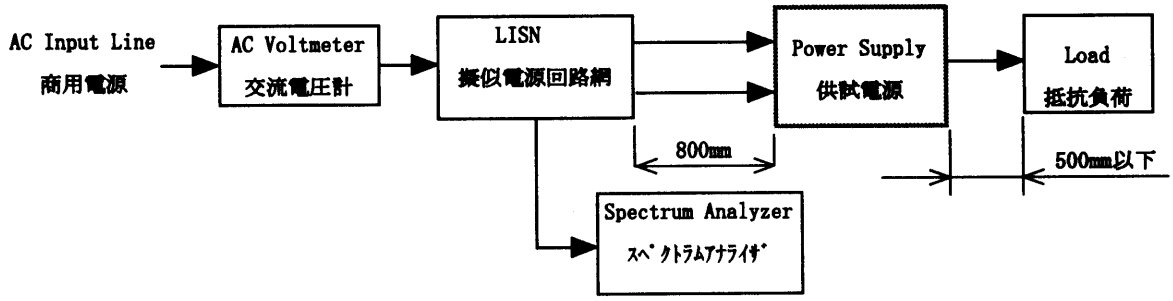


Figure D

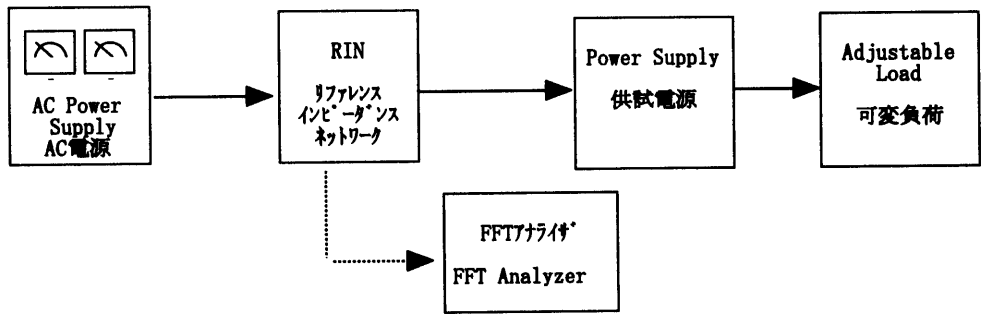


Figure E