



TEST DATA OF LDA75F-9
(200V INPUT)

Regulated DC Power Supply

May 22, 2002

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Design Manager

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Design Engineer

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COSEL CO., LTD.

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<p>Model LDA75F-9</p> <p>Item Line Regulation 静の入力変動</p> <p>Object +9V8.5A</p>		<p>Temperature 25°C</p> <p>Testing Circuitry Figure A</p>																																
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Item		Hold-Up Time 出力保持時間																																	
Object		+9V8.5A																																	
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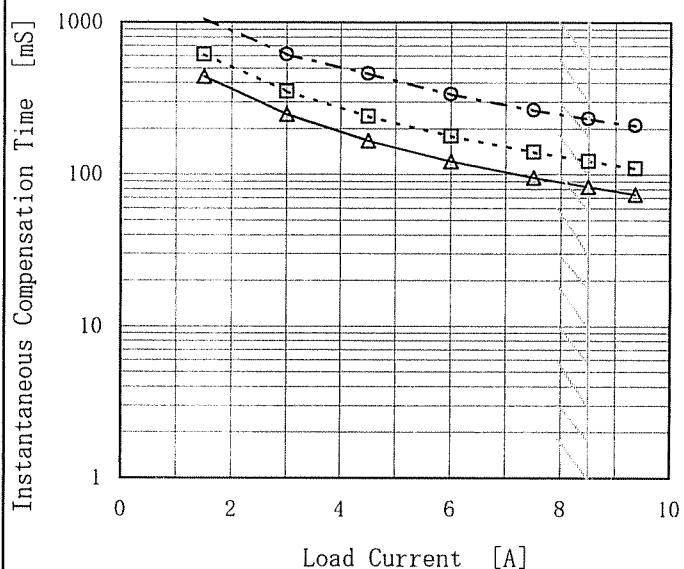


Model	LDA75F-9
Item	Instantaneous Interruption Compensation 瞬時停電保障
Object	+9V8.5A

Temperature 25°C
Testing Circuitry Figure A

1. Graph

- △— Input Volt. 170V
- -□- - - Input Volt. 200V
- -○- - - Input Volt. 264V



2. Values

Load Current [A]	Time [mS]		
	Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]
0.00	—	—	—
1.50	440	615	1062
3.00	248	352	614
4.50	166	241	461
6.00	122	179	336
7.50	95	141	266
8.50	83	123	233
9.35	74	110	210
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Note: Slanted line shows the range of the rated load current.

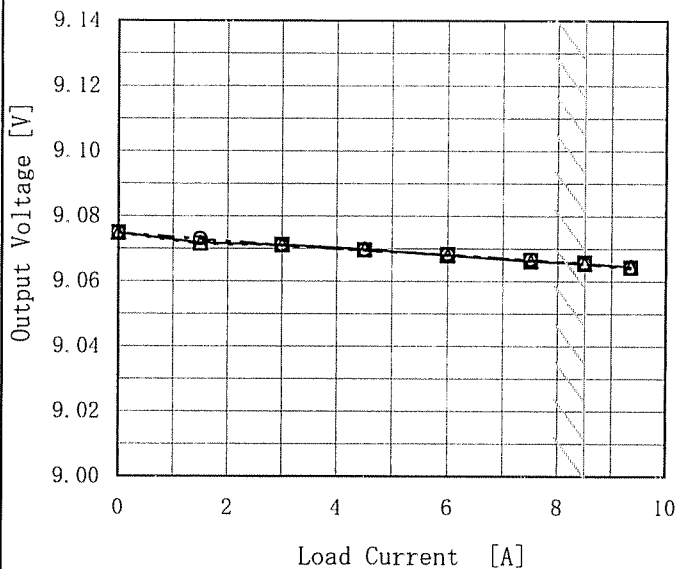
(注) 斜線は定格負荷電流範囲を示す。



Model	LDA75F-9
Item	Load Regulation 静的負荷変動
Object	+9V8.5A

Temperature 25°C
Testing Circuitry Figure A

1. Graph
- △— Input Volt. 170V
 - Input Volt. 200V
 - Input Volt. 264V



2. Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]
0.00	9.075	9.075	9.075
1.50	9.072	9.072	9.073
3.00	9.071	9.071	9.071
4.50	9.070	9.070	9.070
6.00	9.068	9.068	9.068
7.50	9.066	9.067	9.067
8.50	9.066	9.066	9.065
9.35	9.065	9.064	9.064
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Note: Slanted line shows the range of the rated load current.

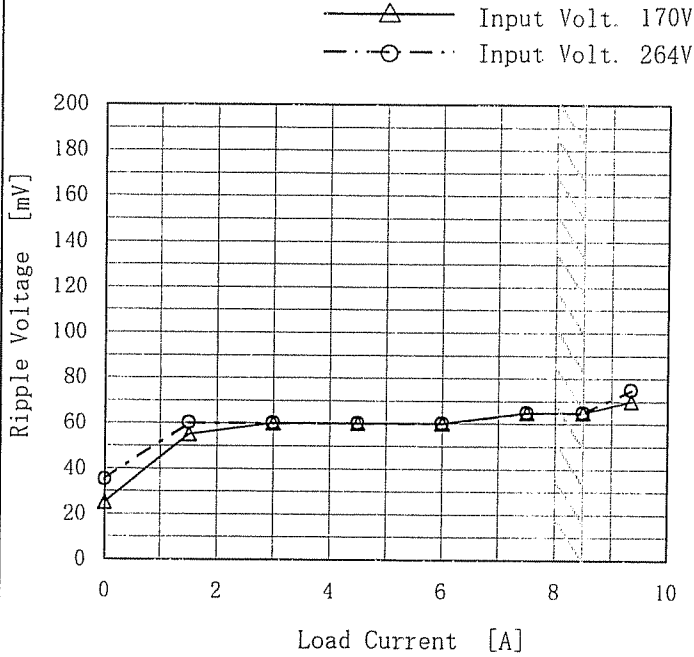
(注) 斜線は定格負荷電流範囲を示す。



Model	LDA75F-9
Item	Ripple Voltage (by Load Current) リップル電圧 (負荷特性)
Object	+9V8.5A

Temperature 25°C
Testing Circuitry Figure A

1. Graph



2. Values

Load Current [A]	Ripple Voltage [mV]	
	Input Volt. 170 [V]	Input Volt. 264 [V]
0.00	25	35
1.50	55	60
3.00	60	60
4.50	60	60
6.00	60	60
7.50	65	65
8.50	65	65
9.35	70	75
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Ripple Voltage is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

リップル電圧は、下図 p-p 値で示される。
(注) 斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
スイッチング周期

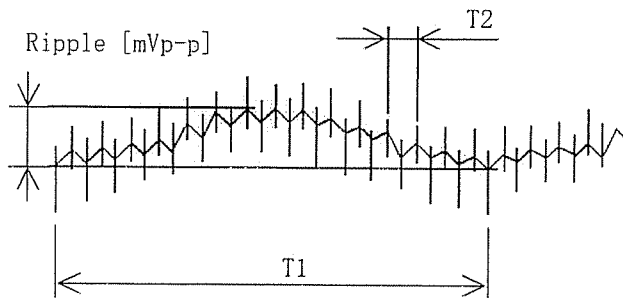


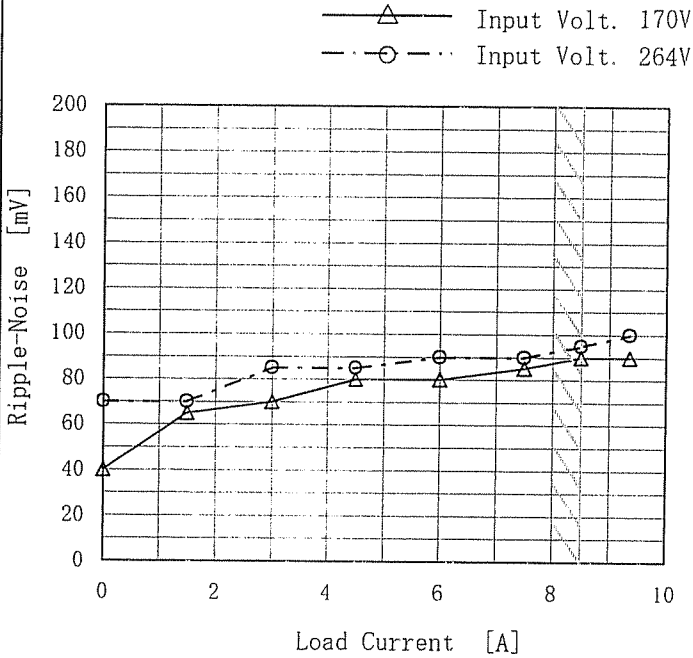
Fig. Complex Ripple Wave Form
図 リップル波形詳細図



Model	LDA75F-9
Item	Ripple-Noise リップルノイズ
Object	+9V8.5A

Temperature 25°C
Testing Circuitry Figure A

1. Graph



2. Values

Load Current [A]	Ripple-Noise [mV]	
	Input Volt. 170 [V]	Input Volt. 264 [V]
0.00	40	70
1.50	65	70
3.00	70	85
4.50	80	85
6.00	80	90
7.50	85	90
8.50	90	95
9.35	90	100
--	--	--
--	--	--
--	--	--

Ripple-Noise is shown as p-p in the figure below.
Note: Slanted line shows the range of the rated load current.

リップルノイズは、下図 p-p 値で示される。
(注) 斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
スイッチング周期

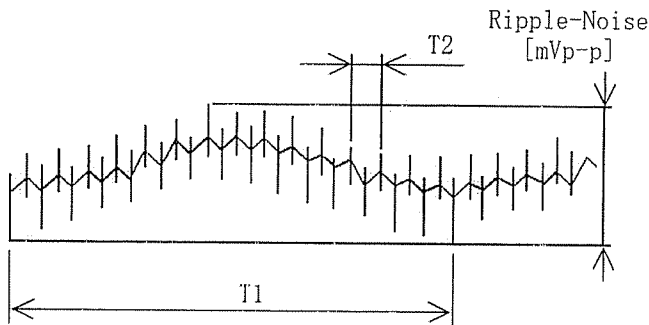


Fig. Complex Ripple Wave Form
図 リップル波形詳細図



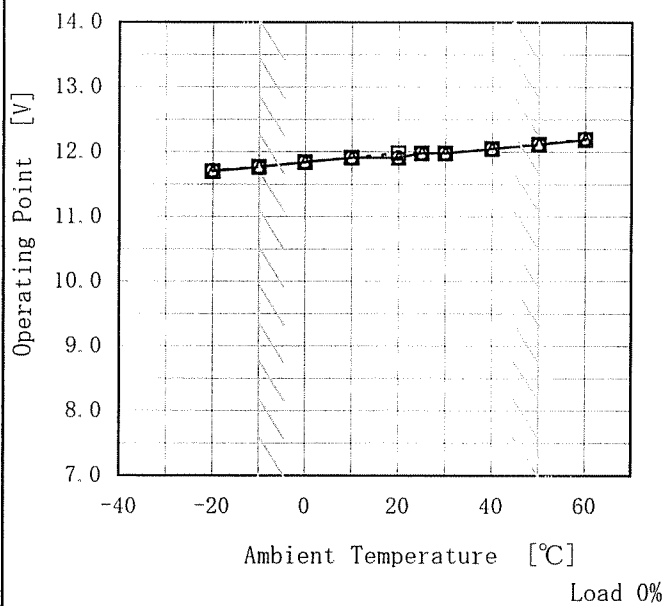
<p>Model LDA75F-9</p> <p>Item Overcurrent Protection 過電流保護</p> <p>Object +9V8.5A</p>		<p>Temperature 25°C</p> <p>Testing Circuitry Figure A</p>																																																							
<p>1. Graph</p> <p> Input Volt. 170V Input Volt. 200V Input Volt. 264V </p> <p style="text-align: center;">Load Current [A]</p> <p>Note: Slanted line shows the range of the rated load current. (注) 斜線は定格負荷電流範囲を示す。</p>		<p>2. Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Output Voltage [V]</th> <th colspan="3">Load Current [A]</th> </tr> <tr> <th>Input Volt. 170[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 264[V]</th> </tr> </thead> <tbody> <tr><td>9.000</td><td>10.20</td><td>10.34</td><td>10.63</td></tr> <tr><td>8.550</td><td>10.26</td><td>10.39</td><td>10.70</td></tr> <tr><td>8.100</td><td>10.32</td><td>10.50</td><td>10.75</td></tr> <tr><td>7.200</td><td>10.49</td><td>10.57</td><td>10.87</td></tr> <tr><td>6.300</td><td>10.57</td><td>10.69</td><td>11.00</td></tr> <tr><td>5.400</td><td>10.68</td><td>10.79</td><td>11.13</td></tr> <tr><td>4.500</td><td>10.78</td><td>10.90</td><td>11.25</td></tr> <tr><td>3.600</td><td>10.87</td><td>10.99</td><td>11.34</td></tr> <tr><td>2.700</td><td>10.94</td><td>11.04</td><td>11.35</td></tr> <tr><td>1.800</td><td>10.92</td><td>10.96</td><td>11.08</td></tr> <tr><td>0.900</td><td>10.55</td><td>10.41</td><td>10.17</td></tr> <tr><td>0.000</td><td>9.69</td><td>9.57</td><td>9.53</td></tr> </tbody> </table>	Output Voltage [V]	Load Current [A]			Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]	9.000	10.20	10.34	10.63	8.550	10.26	10.39	10.70	8.100	10.32	10.50	10.75	7.200	10.49	10.57	10.87	6.300	10.57	10.69	11.00	5.400	10.68	10.79	11.13	4.500	10.78	10.90	11.25	3.600	10.87	10.99	11.34	2.700	10.94	11.04	11.35	1.800	10.92	10.96	11.08	0.900	10.55	10.41	10.17	0.000	9.69	9.57	9.53
Output Voltage [V]	Load Current [A]																																																								
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Model	LDA75F-9
Item	Overvoltage Protection 過電圧保護
Object	+9V8.5A

Testing Circuitry Figure A

1. Graph
- △— Input Volt. 170V
 - Input Volt. 200V
 - Input Volt. 264V



2. Values

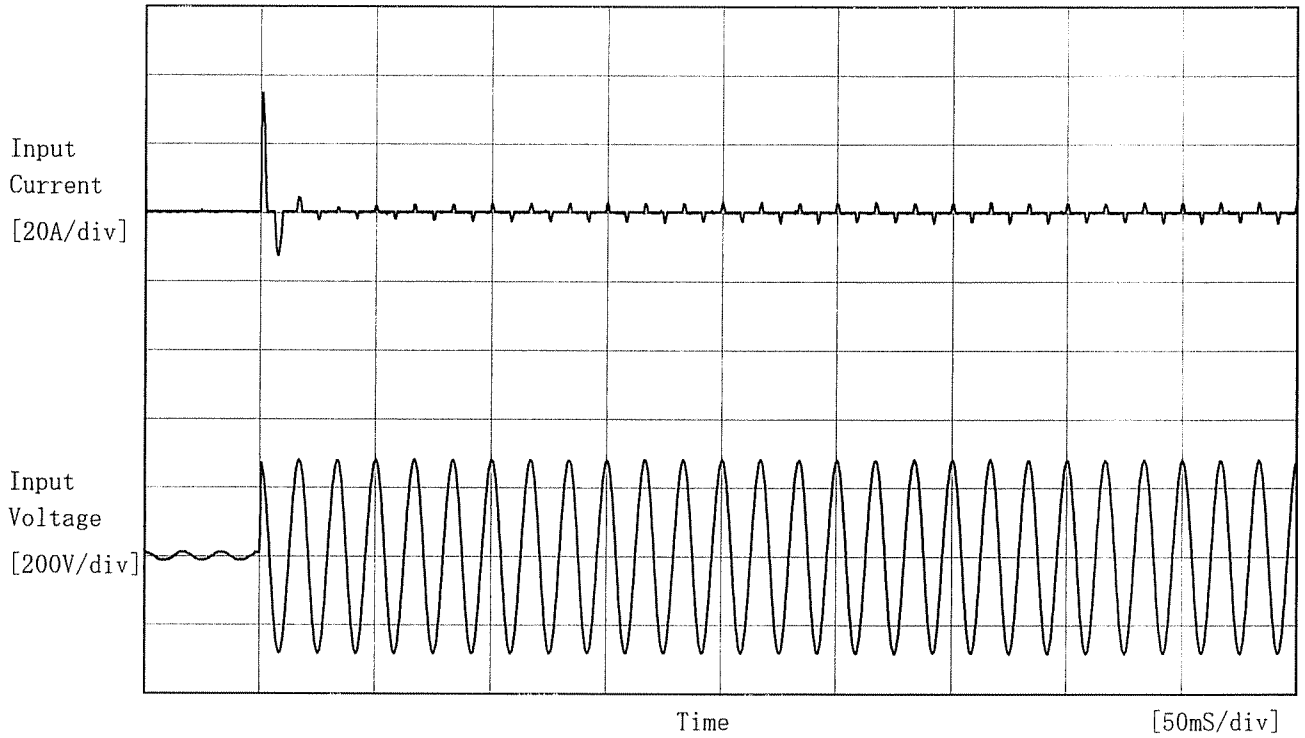
Ambient Temperature [°C]	Operating Point [V]		
	Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]
-20	11.70	11.70	11.71
-10	11.77	11.77	11.77
0	11.84	11.84	11.84
10	11.91	11.91	11.91
20	11.91	11.98	11.91
25	11.98	11.98	11.98
30	11.98	11.98	11.98
40	12.05	12.05	12.05
50	12.12	12.12	12.12
60	12.19	12.19	12.20
--	--	--	--

Note: Slanted line shows the range of the rated ambient temperature.

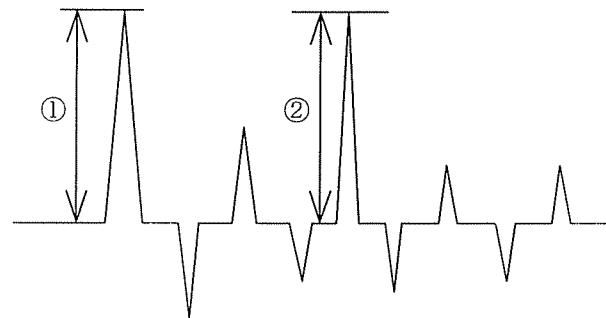
(注) 斜線は定格周囲温度範囲を示す。



Model	LDA75F-9	Temperature	25°C
Item	Inrush Current 突入電流	Testing Circuitry	Figure A
Object	_____		



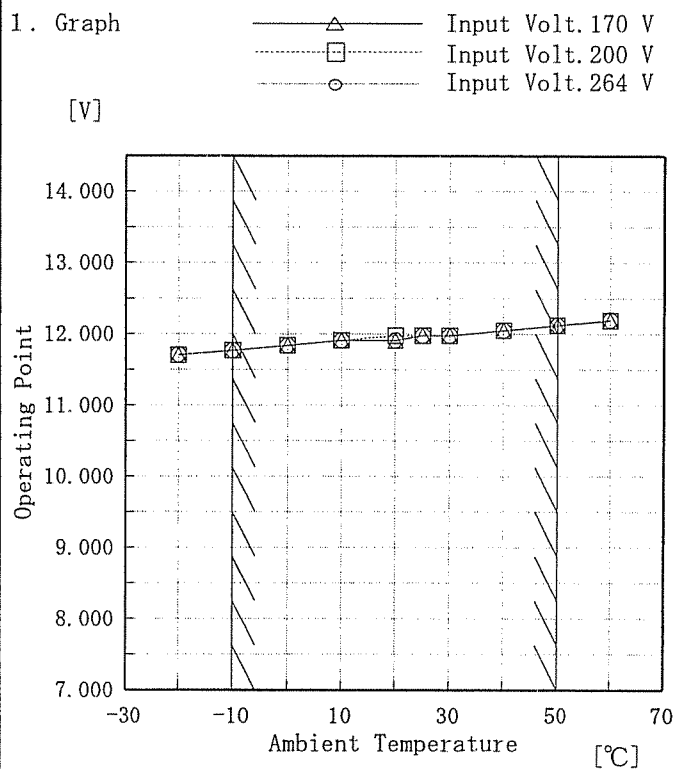
Input Voltage 200 V
 Frequency 60 Hz
 Load 100 %
 Inrush Current
 ① 34.8 [A]
 ② 3.2 [A]





Model	LDA75F-9
Item	Overvoltage Protection 過電圧保護
Object	+9.0V8.5A

Testing Circuitry Figure A



2. Values

Ambient Temperature [°C]	Operating Point [V]		
	Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]
-20	11.70	11.70	11.71
-10	11.77	11.77	11.77
0	11.84	11.84	11.84
10	11.91	11.91	11.91
20	11.91	11.98	11.91
25	11.98	11.98	11.98
30	11.98	11.98	11.98
40	12.05	12.05	12.05
50	12.12	12.12	12.12
60	12.19	12.19	12.20
—	—	—	—

Load 0%

Note: Slanted line shows the range of the rated ambient temperature.

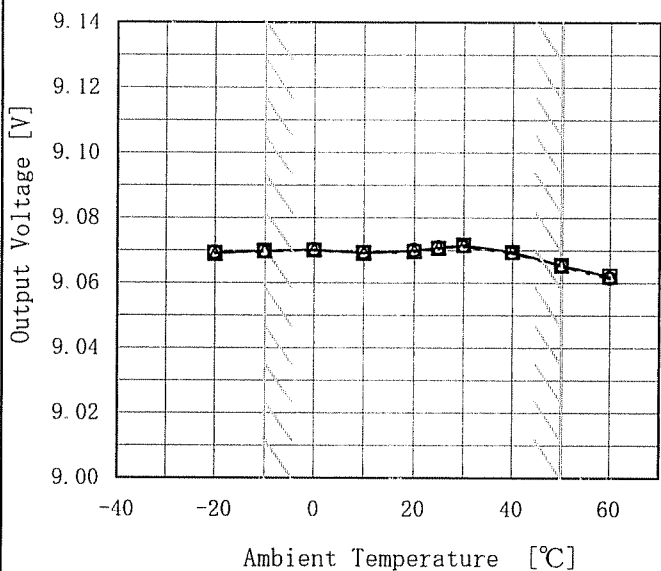
(注) 斜線は定格周囲温度範囲を示す。



Model	LDA75F-9
Item	Ambient Temperature Drift 周囲温度変動
Object	+9V8.5A

Testing Circuitry Figure A

1. Graph
- △— Input Volt. 170V
 - Input Volt. 200V
 - Input Volt. 264V



Load 100%

Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。

2. Values

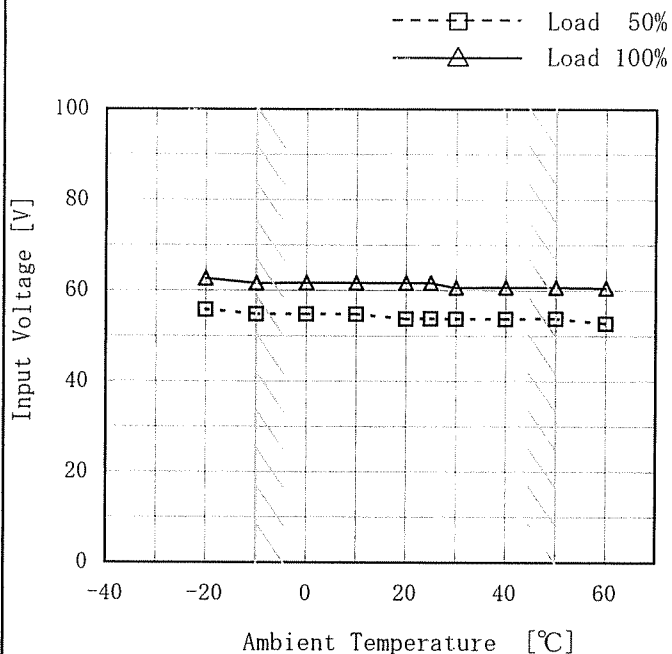
Ambient Temperature [°C]	Output Voltage [V]		
	Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]
-20	9.069	9.069	9.069
-10	9.070	9.070	9.070
0	9.070	9.070	9.070
10	9.069	9.069	9.069
20	9.070	9.070	9.070
25	9.071	9.071	9.071
30	9.072	9.072	9.071
40	9.070	9.069	9.069
50	9.066	9.066	9.066
60	9.062	9.062	9.062
--	—	—	—



Model	LDA75F-9
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+9V8.5A

Testing Circuitry Figure A

1. Graph



2. Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	56	63
-10	55	62
0	55	62
10	55	62
20	54	62
25	54	62
30	54	61
40	54	61
50	54	61
60	53	61
--	--	--

Note: Slanted line shows the range of the rated ambient temperature.

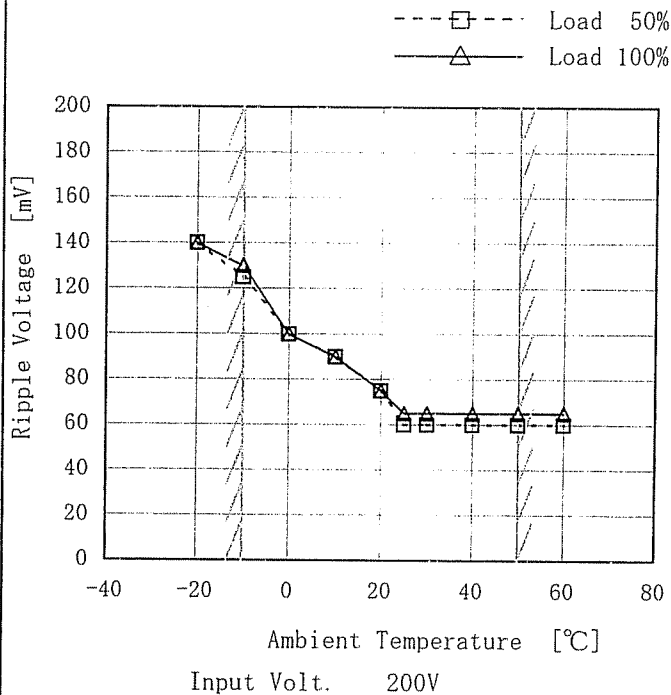
(注) 斜線は定格周囲温度範囲を示す。



Model	LDA75F-9
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)
Object	+9V8.5A

Testing Circuitry Figure A

1. Graph



2. Values

Ambient Temperature [°C]	Ripple Voltage [mV]	
	Load 50%	Load 100%
-20	140	140
-10	125	130
0	100	100
10	90	90
20	75	75
25	60	65
30	60	65
40	60	65
50	60	65
60	60	65
--	--	--

Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。



COSEL		
Model	LDA75F-9	
Item	Output Voltage Accuracy 定電圧精度	Testing Circuitry Figure A
Object	+9V8.5A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10 ~ 50°C

Input Voltage : 170 ~ 264V

Load Current : 0 ~ 8.5A

* Output Voltage Accuracy = $\pm (\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

* Output Voltage Accuracy (Ration) = $\frac{\text{Output Voltage}}{\text{Rated Output Voltage}} \times 100$

1. 定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 : -10 ~ 50°C

入力電圧 : 170 ~ 264V

負荷電流 : 0 ~ 8.5A

* 定電圧精度(変動値) = $\pm (\text{出力電圧の最高値} - \text{出力電圧の最低値}) / 2$

* 定電圧精度(変動率) = $\frac{\text{変動値}}{\text{定格出力電圧}} \times 100$

2. Values

Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ration [%]
Maximum Voltage	25	264	0	9.080	±8	±0.1
Minimum Voltage	50	264	8.5	9.065		



Model		LDA75F-9	Temperature 25°C Testing Circuitry Figure B
Item		Leakage Current 漏洩電流	
Object		_____	

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DEN-AN	—	—	—
(B) IEC60950	—	—	—

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B) IEC60950	0.34	0.41	0.45

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力 of 両相について測定し、その大きい方を漏洩電流測定値とする。



Model		LDA75F-9	Temperature		25°C
Item		Line Noise Tolerance 入力雑音耐量	Testing Circuitry		Figure C
Object		+9V8.5A			

1. Conditions

- Input Voltage : 200 V
- Pulse Voltage : 2000 V
- Pulse Cycle : 10 mS
- Pulse Input Duration : 1 min. or more
- Load : 100 %

2. Results

Pulse Width [nS]	MODE		No protection failure should occur	DC-like Regulation of Output Voltage
	POLARITY		保護回路の誤動作がない	出力電圧の直流的変動
50	COMMON	+	OK	no fluctuation
		-	OK	no fluctuation
	NORMAL	+	OK	no fluctuation
		-	OK	no fluctuation
1000	COMMON	+	OK	no fluctuation
		-	OK	no fluctuation
	NORMAL	+	OK	no fluctuation
		-	OK	no fluctuation



Model		LDA75F-9	Temperature		25°C
Item		Conducted Emission 雑音端子電圧	Testing Circuitry		Figure D
Object					

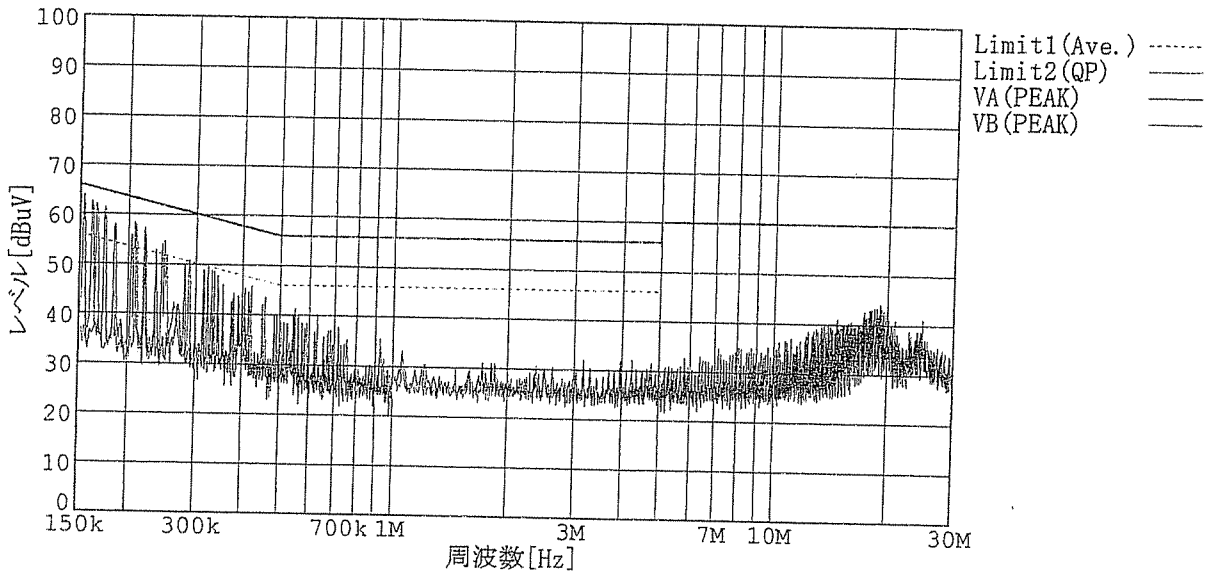
1. Graph

Remarks

Input Volt. 230V (CISPR Pub22 Class B)

Load 100%

規格 1 : [CISPR Pub22] Class B (平均値)
規格 2 : [CISPR Pub22] Class B (QP)



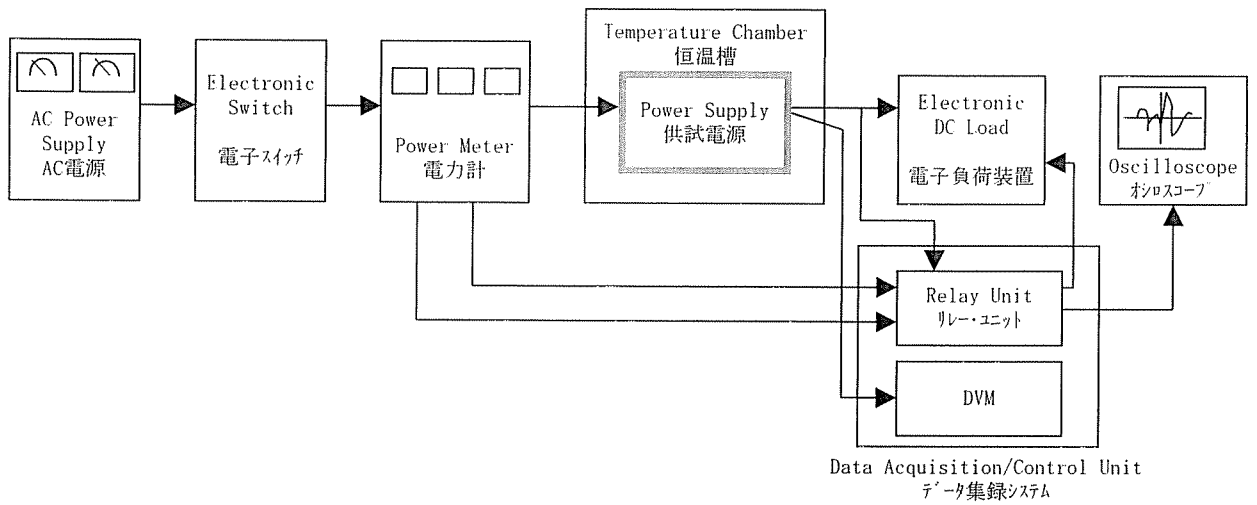


Figure A

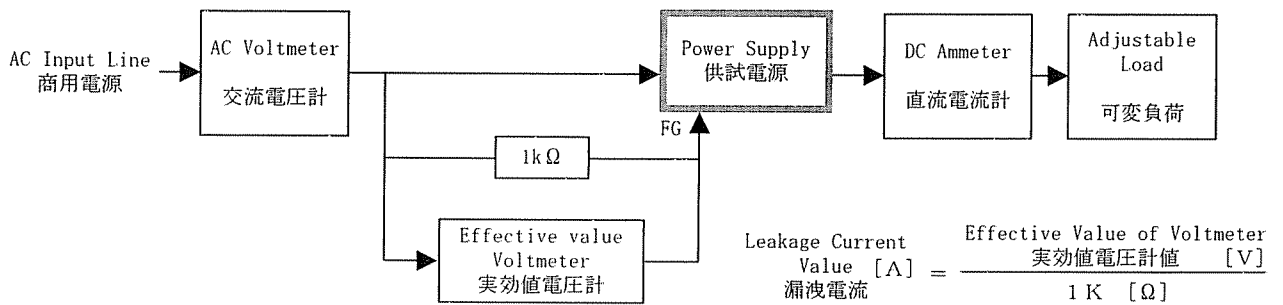


Figure B (DEN-AN)

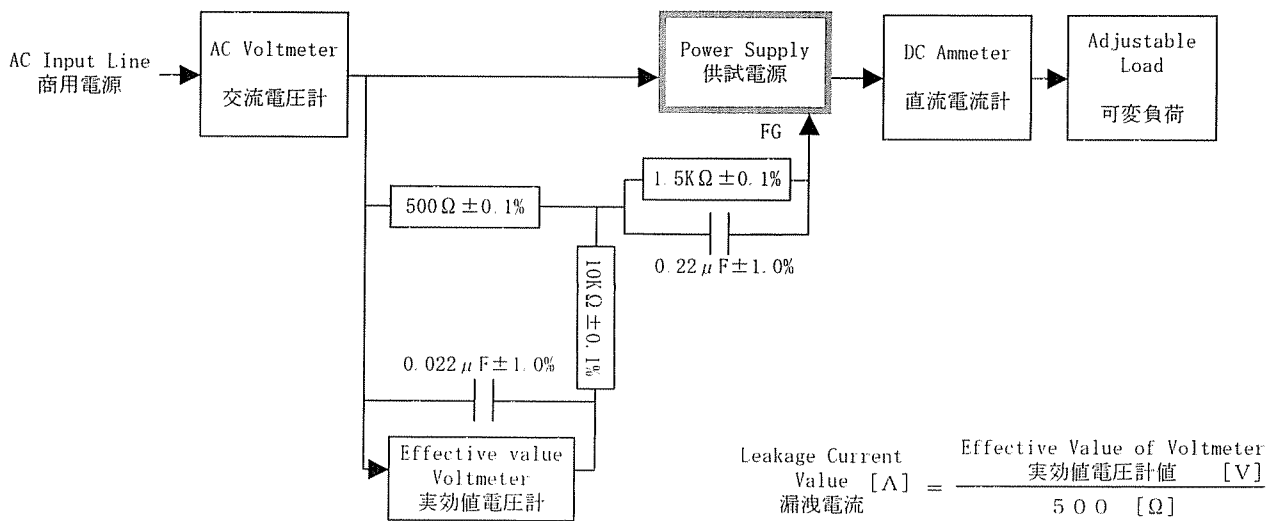


Figure B (IEC60950)

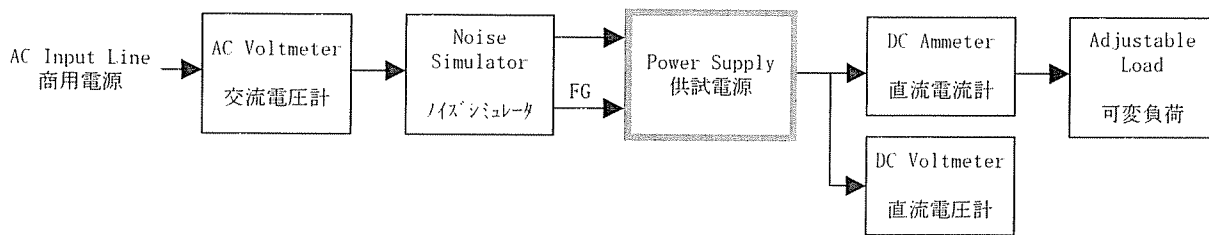


Figure C

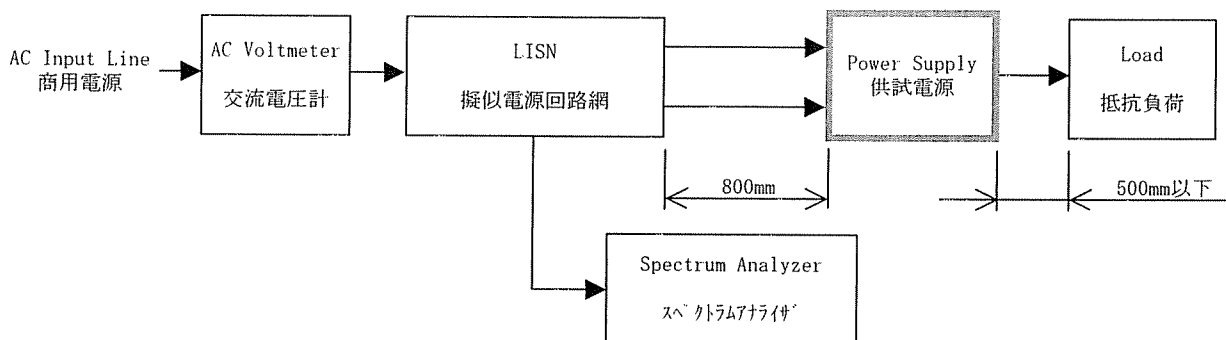


Figure D

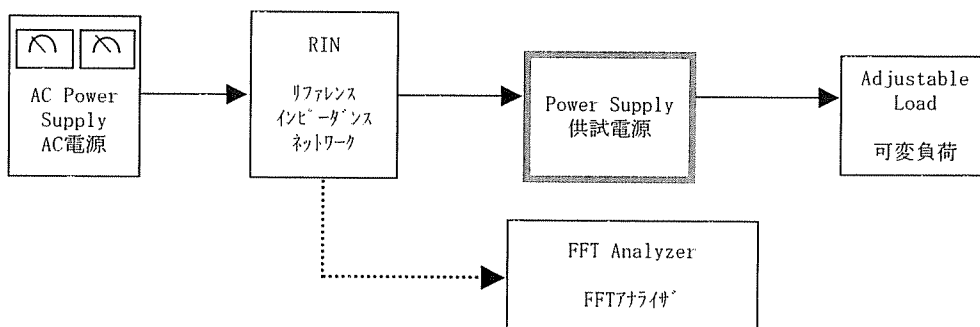


Figure E