



TEST DATA OF LDA10F-24  
(100V INPUT)

Regulated DC Power Supply

Date : June 18. 1999

Approved by : *H. Yamaguchi*  
Design Manager

Prepared by : *J. Ashihara*  
Design Engineer

**コーセル株式会社**

**COSEL CO., LTD.**

CONTENTS

1. Line Regulation . . . . .	1
静的入力変動	
2. Input Current (by Load Current) . . . . .	2
入力電流 (負荷特性)	
3. Input Power (by Load Current) . . . . .	3
入力電力 (負荷特性)	
4. Efficiency (by Input Voltage) . . . . .	4
効率 (入力電圧特性)	
5. Efficiency (by Load Current) . . . . .	5
効率 (負荷特性)	
6. Hold-Up Time . . . . .	6
出力保持時間	
7. Instantaneous Interruption Compensation . . . . .	7
瞬時停電保障	
8. Load Regulation . . . . .	8
静的負荷変動	
9. Ripple Voltage (by Load Current) . . . . .	9
リップル電圧 (負荷特性)	
10. Ripple-Noise . . . . .	10
リップルノイズ	
11. Overcurrent Protection . . . . .	11
過電流保護	
12. Inrush Current . . . . .	12
突入電流	
13. Dynamic Load Responce . . . . .	13
動的負荷変動	
14. Rise and Fall Time . . . . .	14
立上り、立下がり時間	
15. Ambient Temperature Drift . . . . .	15
周囲温度変動	
16. Minimum Input Voltage for Regulated Output Voltage . . . . .	16
最低レギュレーション電圧	
17. Ripple Voltage (by Ambient Temperature) . . . . .	17
リップル電圧 (周囲温度特性)	
18. Time Lapse Drift . . . . .	18
経時ドリフト	
19. Output Voltage Accuracy . . . . .	19
定電圧精度	
20. Condensation . . . . .	20
結露特性	
21. Leakage Current . . . . .	21
漏洩電流	
22. Line Noise Tolerance . . . . .	22
入力雑音耐量	
23. Conducted Emission . . . . .	23
雑音端子電圧	
24. Figure of Testing Circuitry . . . . .	24
測定回路図	

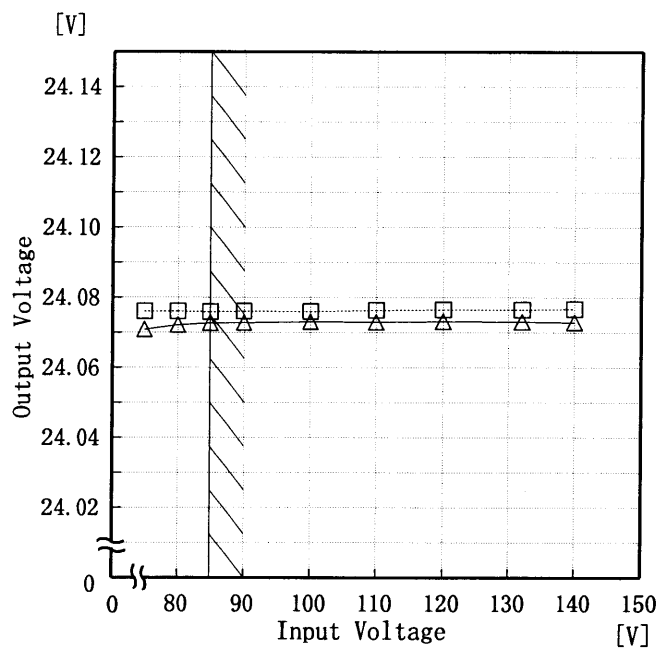
(Final Page 25 )



Model	LDA10F-24
Item	Line Regulation 静的入力変動
Object	+24.0V0.5A

Temperature 25°C  
Testing Circuitry Figure A

1. Graph □ Load 50%  
△ Load 100%



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
75	24.076	24.071
80	24.076	24.072
85	24.076	24.073
90	24.076	24.073
100	24.076	24.073
110	24.076	24.073
120	24.077	24.073
132	24.077	24.073
140	24.077	24.073

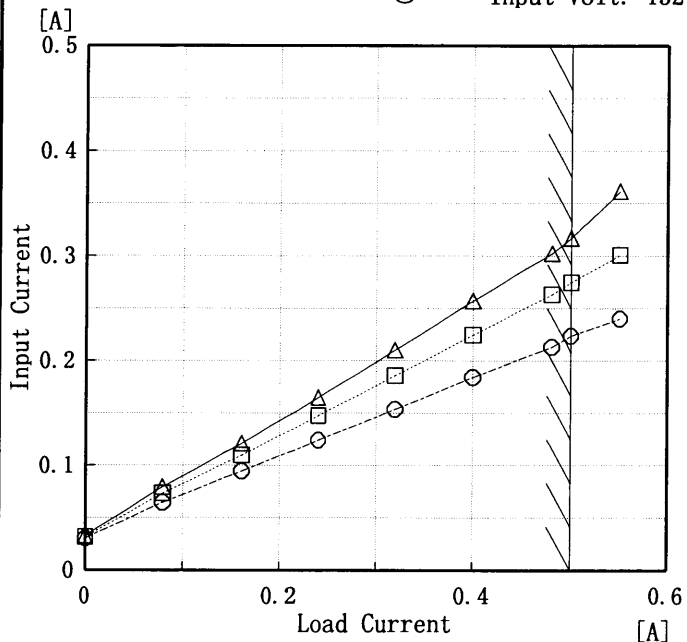


Model	LDA10F-24
Item	Input Current (by Load Current) 入力電流 (負荷特性)
Output	—————

Temperature 25°C  
Testing Circuitry Figure A

1. Graph

—△— Input Volt. 85V  
 - -□- - Input Volt. 100V  
 - -○- - Input Volt. 132V



Note: Slanted line shows the range of the rated load current

(注) 斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Input Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	0.033	0.032	0.030
0.08	0.080	0.074	0.065
0.16	0.121	0.110	0.095
0.24	0.165	0.148	0.124
0.32	0.210	0.186	0.154
0.40	0.257	0.225	0.184
0.48	0.303	0.263	0.213
0.50	0.317	0.275	0.224
0.55	0.362	0.301	0.240
—	—	—	—
—	—	—	—
—	—	—	—

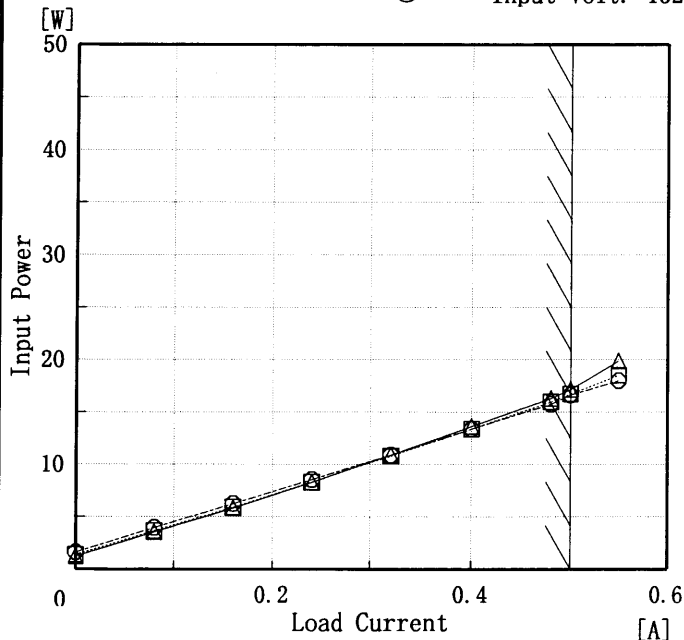


Model	LDA10F-24
Item	Input Power (by Load Current) 入力電力 (負荷特性)
Output	_____

Temperature 25°C  
Testing Circuitry Figure A

1. Graph

—△— Input Volt. 85V  
 - - -□- - - Input Volt. 100V  
 - - -○- - - Input Volt. 132V



Note: Slanted line shows the range of the rated load current

(注) 斜線は定格負荷電流範囲を示す。

2. Values

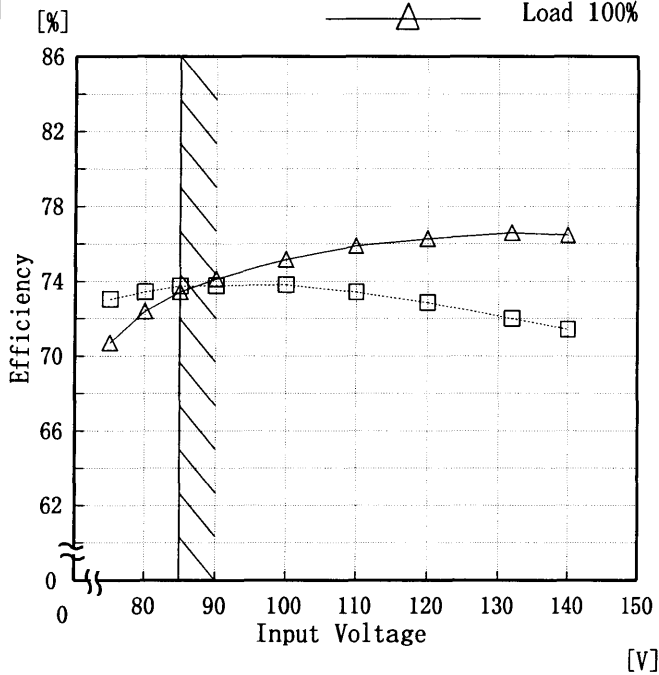
Load Current [A]	Input Power [W]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
0.00	1.22	1.34	1.60
0.08	3.54	3.68	3.97
0.16	5.82	5.91	6.24
0.24	8.29	8.31	8.54
0.32	10.89	10.80	10.90
0.40	13.64	13.39	13.39
0.48	16.32	15.97	15.76
0.50	17.16	16.72	16.65
0.55	19.90	18.52	17.99
—	—	—	—
—	—	—	—
—	—	—	—



Model	LDA10F-24
Item	Efficiency 効率
Object	_____

Temperature 25°C  
Testing Circuitry Figure A

1. Graph □ Load 50%  
△ Load 100%



Note: Slanted line shows the range of the rated input voltage.

(注) 斜線は定格入力電圧範囲を示す。

2. Values

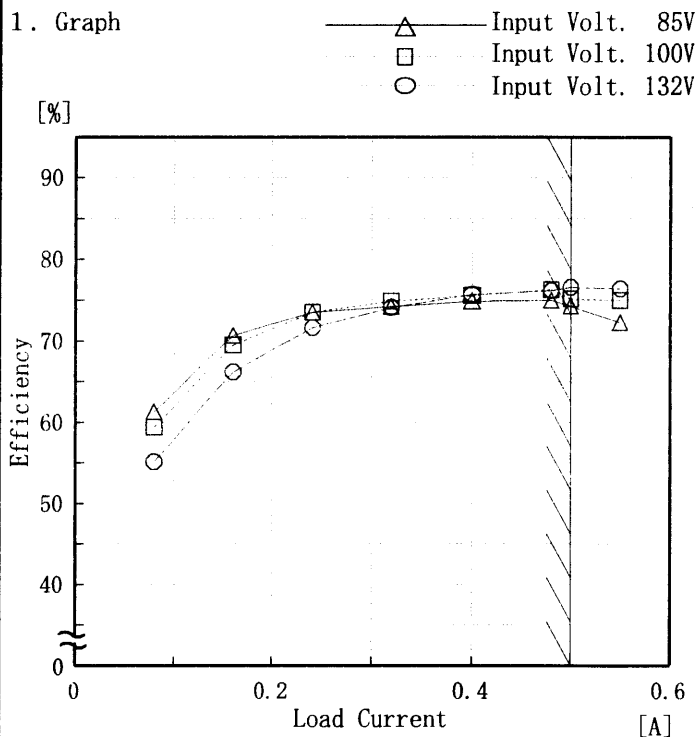
Input Voltage [V]	Efficiency [%]	
	Load 50%	Load 100%
75	73.0	70.7
80	73.4	72.4
85	73.8	73.4
90	73.8	74.1
100	73.8	75.2
110	73.4	75.9
120	72.9	76.3
132	72.0	76.6
140	71.4	76.5



Model	LDA10F-24
Item	Efficiency (by Load Current) 効率 (負荷特性)
Output	—————

Temperature 25°C  
Testing Circuitry Figure A

1. Graph



2. Values

Load Current [A]	Efficiency [%]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.08	61.3	59.4	55.1
0.16	70.7	69.5	66.2
0.24	73.5	73.5	71.6
0.32	74.2	74.9	74.1
0.40	74.9	75.6	75.7
0.48	75.0	76.3	76.2
0.50	74.3	75.2	76.6
0.55	72.3	75.0	76.4
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

Note: Slanted line shows the range of the rated load current

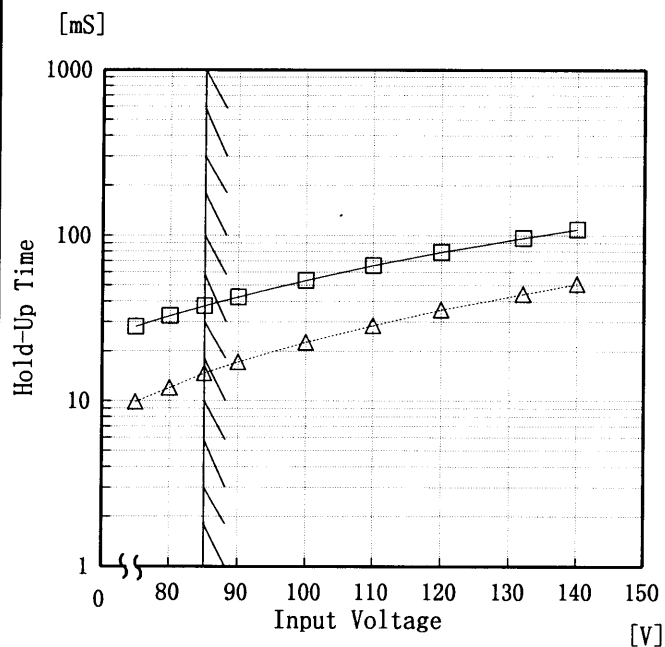
(注) 斜線は定格負荷電流範囲を示す。



Model	LDA10F-24
Item	Hold-Up Time 出力保持時間
Object	+24.0V0.5A

Temperature 25°C  
Testing Circuitry Figure A

1. Graph □ Load 50%  
△ Load 100%



2. Values

Input Voltage [V]	Hold-Up Time [mS]	
	Load 50%	Load 100%
75	28	10
80	33	12
85	38	15
90	43	17
100	54	23
110	66	29
120	79	36
132	97	44
140	109	51

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated input voltage.

出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。

(注)斜線は定格入力電圧範囲を示す。





<p>Model LDA10F-24</p>		<p>Temperature 25°C</p>																																																				
<p>Item Instantaneous Interruption Compensation 瞬時停電保障</p>		<p>Testing Circuitry Figure A</p>																																																				
<p>Object +24.0V0.5A</p>																																																						
<p>1. Graph</p> <div style="display: flex; align-items: center;"> <div style="margin-right: 20px;"> <p>—△— Input Volt. 85 V</p> <p>- - -□- - - Input Volt. 100 V</p> <p>- - -○- - - Input Volt. 132 V</p> </div> </div> <p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy. Note: Slanted line shows the range of the rated load current.</p> <p>瞬時停電保障時間とは、出力電圧が定電圧精度の規格範囲を保持している瞬時停電時間をいう。 (注)斜線は定格負荷電流範囲を示す。</p>		<p>2. Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Time [mS]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0.00</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>0.08</td><td>101</td><td>141</td><td>245</td></tr> <tr><td>0.16</td><td>53</td><td>76</td><td>138</td></tr> <tr><td>0.24</td><td>31</td><td>48</td><td>92</td></tr> <tr><td>0.32</td><td>22</td><td>34</td><td>68</td></tr> <tr><td>0.40</td><td>14</td><td>23</td><td>53</td></tr> <tr><td>0.48</td><td>10</td><td>18</td><td>40</td></tr> <tr><td>0.50</td><td>5</td><td>14</td><td>39</td></tr> <tr><td>0.55</td><td>5</td><td>14</td><td>35</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>		Load Current [A]	Time [mS]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0.00	—	—	—	0.08	101	141	245	0.16	53	76	138	0.24	31	48	92	0.32	22	34	68	0.40	14	23	53	0.48	10	18	40	0.50	5	14	39	0.55	5	14	35	—	—	—	—	—	—	—	—
Load Current [A]	Time [mS]																																																					
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]																																																			
0.00	—	—	—																																																			
0.08	101	141	245																																																			
0.16	53	76	138																																																			
0.24	31	48	92																																																			
0.32	22	34	68																																																			
0.40	14	23	53																																																			
0.48	10	18	40																																																			
0.50	5	14	39																																																			
0.55	5	14	35																																																			
—	—	—	—																																																			
—	—	—	—																																																			

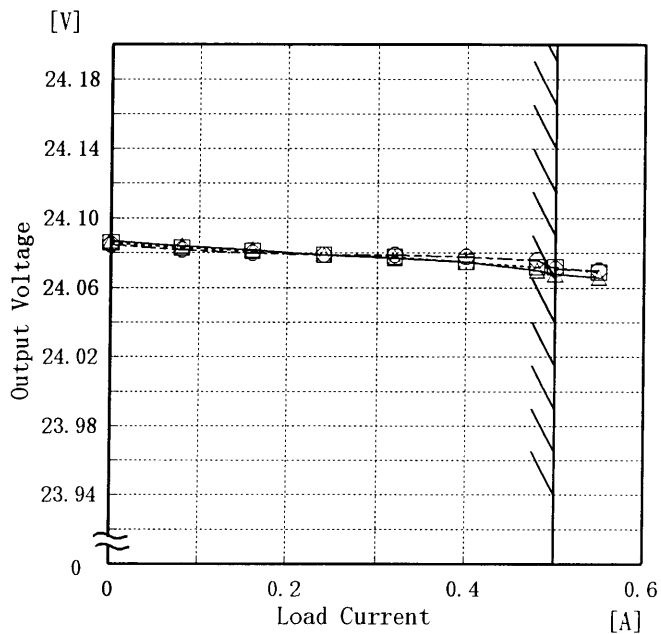


Model	LDA10F-24	Temperature	25°C
Item	Load Regulation 静的負荷変動	Testing Circuitry	Figure A

Object +24.0V0.5A

1. Graph

—△— Input Volt. 85 V  
 - - -□- - - Input Volt. 100 V  
 - - -○- - - Input Volt. 132 V



Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.00	24.087	24.086	24.085
0.08	24.084	24.083	24.082
0.16	24.082	24.081	24.080
0.24	24.079	24.079	24.079
0.32	24.077	24.078	24.079
0.40	24.075	24.075	24.078
0.48	24.070	24.072	24.076
0.50	24.068	24.072	24.071
0.55	24.066	24.069	24.070
—	—	—	—



<p>Model LDA10F-24</p>																																									
Item	Ripple Voltage (by Load Current) リップル電圧 (負荷電流特性)	Temperature	25°C																																						
		Testing Circuitry	Figure A																																						
Object	+24.0V0.5A																																								
<p>1. Graph</p> <p>[mV]</p> <p>-----□----- Input Volt. 85V</p> <p>-----△----- Input Volt. 132V</p> <p>Ripple Voltage is shown as p-p in the figure below.</p> <p>Note: Slanted line shows the range of the rated load current.</p> <p>リップル電圧は、下図 p-p 値で示される。 (注)斜線は定格負荷電流範囲を示す。</p> <p>T1: Due to AC Input Line 入力商用周期</p> <p>T2: Due to Switching スイッチング周期</p> <p>Fig. Complex Ripple Wave Form 図 リップル波形詳細図</p>		<p>2. Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th>Input Volt. 85 [V]</th> <th>Input Volt. 132 [V]</th> </tr> <tr> <th>Ripple Output Volt. [mV]</th> <th>Ripple Output Volt. [mV]</th> </tr> </thead> <tbody> <tr><td>0.00</td><td>10</td><td>10</td></tr> <tr><td>0.10</td><td>10</td><td>10</td></tr> <tr><td>0.20</td><td>10</td><td>10</td></tr> <tr><td>0.25</td><td>10</td><td>10</td></tr> <tr><td>0.30</td><td>15</td><td>10</td></tr> <tr><td>0.40</td><td>20</td><td>10</td></tr> <tr><td>0.50</td><td>30</td><td>15</td></tr> <tr><td>0.55</td><td>40</td><td>15</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>		Load Current [A]	Input Volt. 85 [V]	Input Volt. 132 [V]	Ripple Output Volt. [mV]	Ripple Output Volt. [mV]	0.00	10	10	0.10	10	10	0.20	10	10	0.25	10	10	0.30	15	10	0.40	20	10	0.50	30	15	0.55	40	15	—	—	—	—	—	—	—	—	—
Load Current [A]	Input Volt. 85 [V]	Input Volt. 132 [V]																																							
	Ripple Output Volt. [mV]	Ripple Output Volt. [mV]																																							
0.00	10	10																																							
0.10	10	10																																							
0.20	10	10																																							
0.25	10	10																																							
0.30	15	10																																							
0.40	20	10																																							
0.50	30	15																																							
0.55	40	15																																							
—	—	—																																							
—	—	—																																							
—	—	—																																							

# COSEL

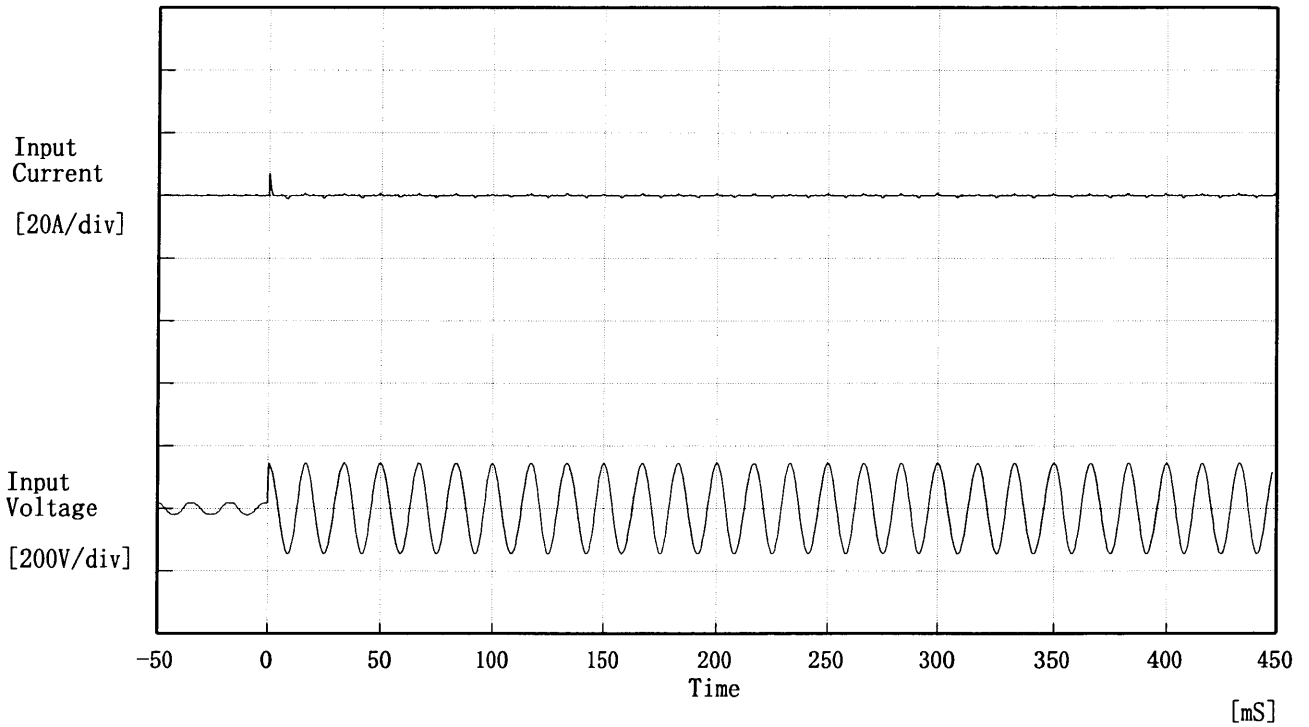
<p>Model LDA10F-24</p>		<p>Temperature 25°C</p>																																							
<p>Item Ripple-Noise リップルノイズ</p>		<p>Testing Circuitry Figure A</p>																																							
<p>Object +24.0V0.5A</p>																																									
<p>1. Graph</p> <p>[mV]</p> <p>□ ----- Input Volt. 85V</p> <p>△ ----- Input Volt. 132V</p> <p>Ripple-Noise</p> <p>Load Current [A]</p>		<p>2. Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Load current [A]</th> <th>Input Volt. 85 [V]</th> <th>Input Volt. 132 [V]</th> </tr> <tr> <th>Ripple-Noise [mV]</th> <th>Ripple-Noise [mV]</th> </tr> </thead> <tbody> <tr><td>0.00</td><td>10</td><td>10</td></tr> <tr><td>0.10</td><td>15</td><td>15</td></tr> <tr><td>0.20</td><td>15</td><td>15</td></tr> <tr><td>0.25</td><td>20</td><td>15</td></tr> <tr><td>0.30</td><td>25</td><td>15</td></tr> <tr><td>0.40</td><td>30</td><td>20</td></tr> <tr><td>0.50</td><td>50</td><td>20</td></tr> <tr><td>0.55</td><td>70</td><td>30</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>		Load current [A]	Input Volt. 85 [V]	Input Volt. 132 [V]	Ripple-Noise [mV]	Ripple-Noise [mV]	0.00	10	10	0.10	15	15	0.20	15	15	0.25	20	15	0.30	25	15	0.40	30	20	0.50	50	20	0.55	70	30	—	—	—	—	—	—	—	—	—
Load current [A]	Input Volt. 85 [V]	Input Volt. 132 [V]																																							
	Ripple-Noise [mV]	Ripple-Noise [mV]																																							
0.00	10	10																																							
0.10	15	15																																							
0.20	15	15																																							
0.25	20	15																																							
0.30	25	15																																							
0.40	30	20																																							
0.50	50	20																																							
0.55	70	30																																							
—	—	—																																							
—	—	—																																							
—	—	—																																							
<p>Ripple-Noise is shown as p-p in the figure below.</p> <p>Note: Slanted line shows the range of the rated load current.</p>																																									
<p>リップルノイズは、下図 p-p 値で示される。</p> <p>(注) 斜線は定格負荷電流範囲を示す。</p>																																									
<p>T1: Due to AC Input Line 入力商用周期</p> <p>T2: Due to Switching スイッチング周期</p> <p>Ripple-Noise [mVp-p]</p> <p>T1</p>																																									
<p>Fig. Complex Ripple Wave Form</p> <p>図 リップル波形詳細図</p>																																									



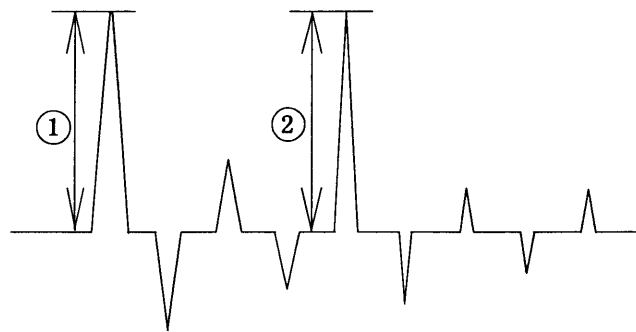
<b>COSEL</b>																																																										
Model	LDA10F-24	Temperature	25°C																																																							
Item	Overcurrent Protection 過電流保護	Testing Circuitry	Figure A																																																							
Object	+24.0V0.5A																																																									
<p>1. Graph</p> <p>[V]</p> <p>Output Voltage</p> <p>40.0</p> <p>30.0</p> <p>20.0</p> <p>10.0</p> <p>0.0</p> <p>0 0.2 0.4 0.6 0.8 1</p> <p>Load Current [A]</p> <p> <span style="border-bottom: 1px solid black; width: 50px; display: inline-block;"></span> Input Volt. 85 V  <span style="border-bottom: 1px solid black; width: 50px; display: inline-block;"></span> Input Volt. 100 V  <span style="border-bottom: 1px solid black; width: 50px; display: inline-block;"></span> Input Volt. 132 V                 </p> <p>Note: Slanted line shows the range of the rated load current.</p> <p>(注)斜線は定格負荷電流範囲を示す。</p>		<p>2. Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Output Voltage [V]</th> <th colspan="3">Load Current [A]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>24.00</td><td>0.60</td><td>0.63</td><td>0.65</td></tr> <tr><td>22.80</td><td>0.62</td><td>0.65</td><td>0.67</td></tr> <tr><td>21.60</td><td>0.64</td><td>0.67</td><td>0.68</td></tr> <tr><td>19.20</td><td>0.69</td><td>0.71</td><td>0.72</td></tr> <tr><td>16.80</td><td>0.74</td><td>0.75</td><td>0.75</td></tr> <tr><td>14.40</td><td>0.79</td><td>0.80</td><td>0.78</td></tr> <tr><td>12.00</td><td>0.84</td><td>0.84</td><td>0.81</td></tr> <tr><td>9.60</td><td>0.89</td><td>0.87</td><td>0.82</td></tr> <tr><td>7.20</td><td>0.93</td><td>0.89</td><td>0.82</td></tr> <tr><td>4.80</td><td>0.92</td><td>0.86</td><td>0.79</td></tr> <tr><td>2.40</td><td>0.82</td><td>0.76</td><td>0.69</td></tr> <tr><td>0.00</td><td>0.56</td><td>0.52</td><td>0.49</td></tr> </tbody> </table>		Output Voltage [V]	Load Current [A]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	24.00	0.60	0.63	0.65	22.80	0.62	0.65	0.67	21.60	0.64	0.67	0.68	19.20	0.69	0.71	0.72	16.80	0.74	0.75	0.75	14.40	0.79	0.80	0.78	12.00	0.84	0.84	0.81	9.60	0.89	0.87	0.82	7.20	0.93	0.89	0.82	4.80	0.92	0.86	0.79	2.40	0.82	0.76	0.69	0.00	0.56	0.52	0.49
Output Voltage [V]	Load Current [A]																																																									
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]																																																							
24.00	0.60	0.63	0.65																																																							
22.80	0.62	0.65	0.67																																																							
21.60	0.64	0.67	0.68																																																							
19.20	0.69	0.71	0.72																																																							
16.80	0.74	0.75	0.75																																																							
14.40	0.79	0.80	0.78																																																							
12.00	0.84	0.84	0.81																																																							
9.60	0.89	0.87	0.82																																																							
7.20	0.93	0.89	0.82																																																							
4.80	0.92	0.86	0.79																																																							
2.40	0.82	0.76	0.69																																																							
0.00	0.56	0.52	0.49																																																							



Model	LDA10F-24	Temperature 25°C Testing Circuitry Figure A
Item	Inrush Current 突入電流	
Object	_____	



Input Voltage 100 V  
 Frequency 60 Hz  
 Load 100 %  
 Inrush Current  
 ① 7.19 [A]  
 ② 0.81 [A]





Model		LDA10F-24	Temperature		25°C
Item		Dynamic Load Responce 動的負荷変動	Testing Circuitry		Figure A
Object		+24.0V0.5A			

Input Volt. 100 V

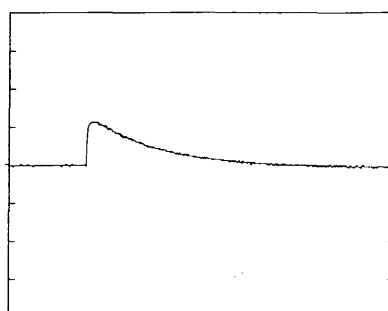
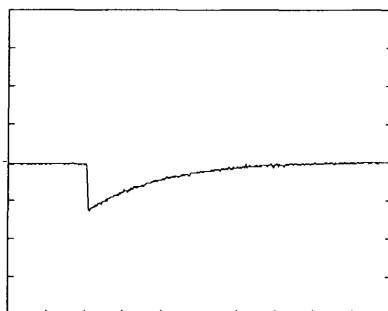
Cycle 1000 mS

Load Current



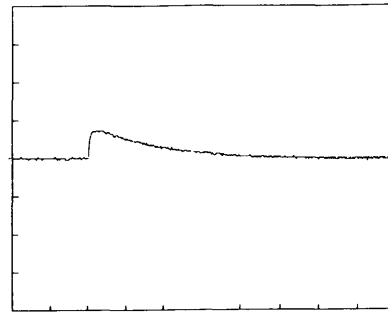
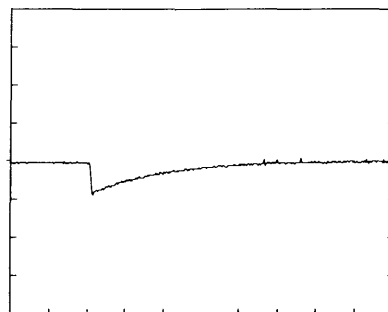
Load 0% ↔

Load 100 %



Load 0% ↔

Load 50 %



200 mV/div

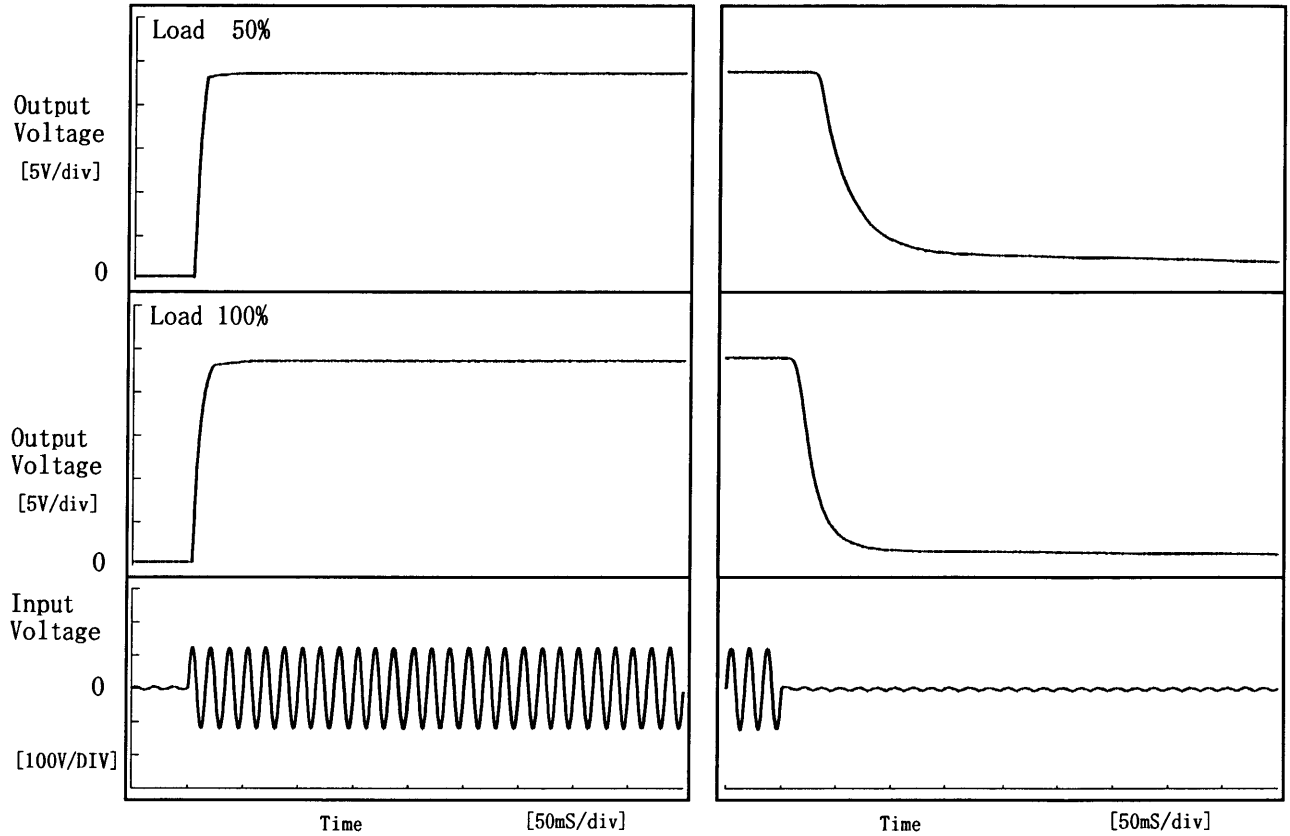
10 mS/div



Model	LDA10F-24	Temperature	25°C
Item	Rise and Fall Time 立上り、立下り時間	Testing Circuitry	Figure A
Object	+24.0V0.5A		

1. Graph

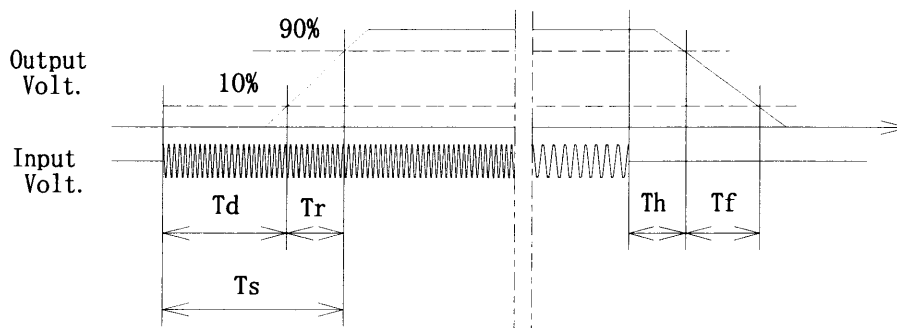
Input Volt. 85 V



2. Values

[mS]

Load \ Time	T <sub>d</sub>	T <sub>r</sub>	T <sub>s</sub>	T <sub>h</sub>	T <sub>f</sub>
50 %	3.8	9.8	13.5	36.3	260.3
100 %	3.8	14.3	18.0	15.0	52.5



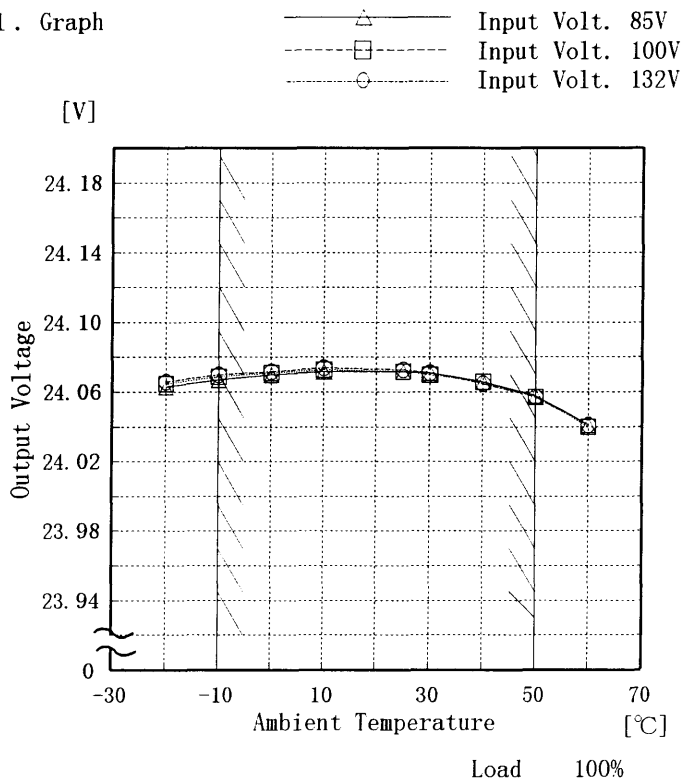




Model	LDA10F-24
Item	Ambient Temperature Drift 周囲温度変動
Object	+24.0V0.5A

Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

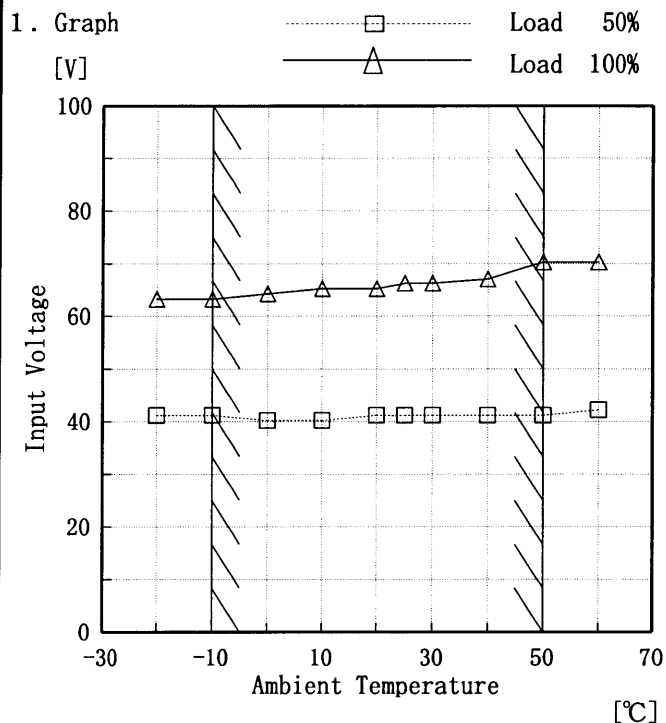
2. Values

Temperature [°C]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
-20	24.063	24.065	24.066
-10	24.067	24.069	24.070
0	24.070	24.071	24.072
10	24.072	24.073	24.074
25	24.072	24.072	24.073
30	24.071	24.070	24.071
40	24.066	24.066	24.065
50	24.058	24.057	24.057
60	24.041	24.040	24.041
—	—	—	—
—	—	—	—



Model	LDA10F-24
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+24.0V0.5A

Testing Circuitry Figure A



2. Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	41	63
-10	41	63
0	40	64
10	40	65
20	41	65
25	41	66
30	41	66
40	41	67
50	41	70
60	42	70
—	—	—

Note: Slanted line shows the range of the rated ambient temperature.

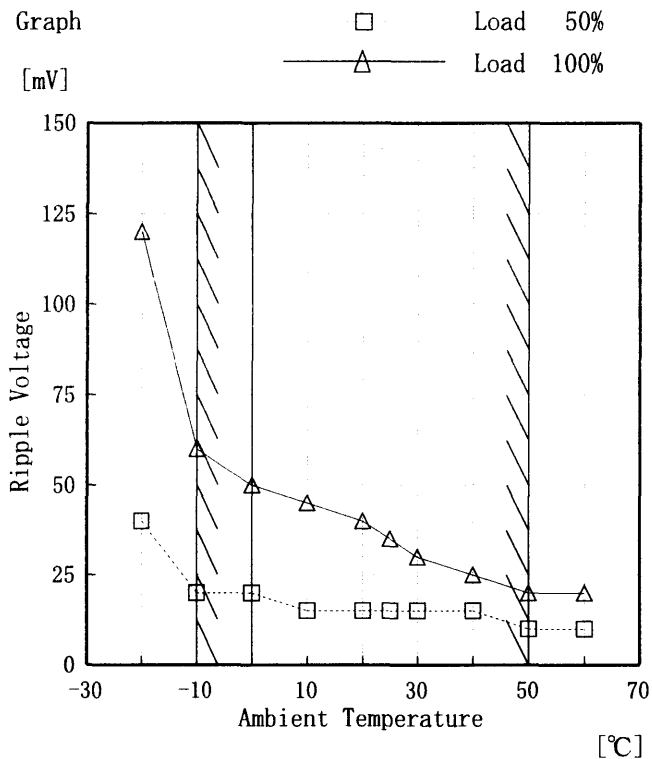
(注)斜線は定格周囲温度範囲を示す。



Model	LDA10F-24
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)
Object	+24.0V 0.5A

Testing Circuitry Figure A

1. Graph



Input Volt. 100 V

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

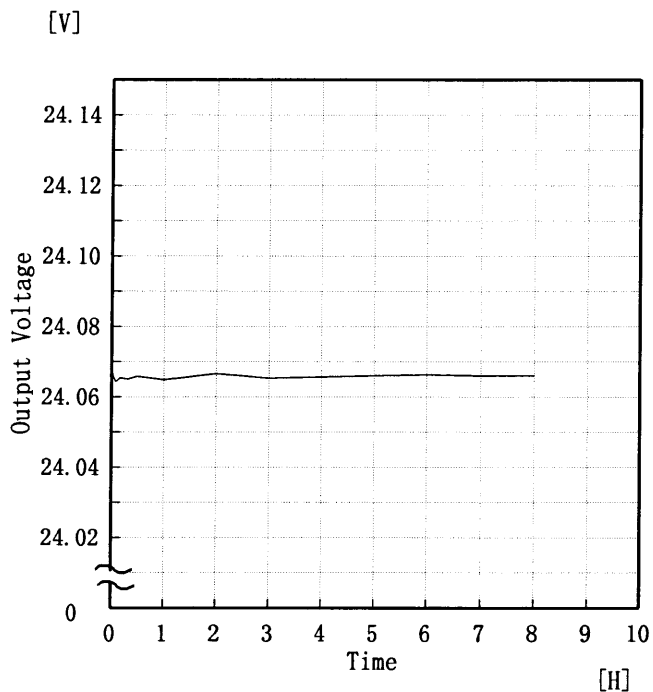
2. Values

Ambient Temp. [°C]	Load 50%	Load 100%
	Ripple Output Volt. [mV]	Ripple Output Volt. [mV]
-20	40	120
-10	20	60
0	20	50
10	15	45
20	15	40
25	15	35
30	15	30
40	15	25
50	10	20
60	10	20
—	—	—



Model	LDA10F-24	Temperature	25°C
Item	Time Lapse Drift 経時ドリフト	Testing Circuitry	Figure A
Object	+24.0V0.5A		

1. Graph



Input Volt. 100V  
Load 100%

2. Values

Time since start [H]	Output Voltage [V]
0.0	24.073
0.5	24.066
1.0	24.065
2.0	24.067
3.0	24.065
4.0	24.066
5.0	24.066
6.0	24.066
7.0	24.066
8.0	24.066



<b>COSEL</b>		
Model	LDA10F-24	
Item	Output Voltage Accuracy 定電圧精度	Testing Circuitry Figure A
Object	+24.0V0.5A	

**Output Voltage Accuracy**

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature -10~50 °C

Input Voltage : 85~132 V

Load Current : 0~0.5 A

\* Output Voltage Accuracy =  $\pm (\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

\* Output Voltage Accuracy (Ration) =  $\frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$

**定電圧精度**

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 -10~50 °C

入力電圧 85~132 V

負荷電流 0~0.5 A

\* 定電圧精度(変動値) =  $\pm (\text{出力電圧の最高値} - \text{出力電圧の最低値}) / 2$

\* 定電圧精度(変動率) =  $\frac{\text{変動値}}{\text{定格出力電圧}} \times 100$

Item	Temperature [°C]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy(Ration) [%]
Maximum Voltage	25	132	0.0	24.085	±22	±0.6
Minimum Voltage	50	85	0.5	24.042		

# COSEL

<b>COSEL</b>		
Model	LDA10F-24	
Item	Condensation 結露特性	Testing Circuitry Figure A
Object	+24.0V0.5A	

1. Condensation test

Testing procedure is as follows.

- ① Keeping and cooling the unit in a tank at -10°C for an hour with the input off.
- ② Taking it out of the tank and dewing itself in a room where the temperature is 25°C and the humidity is 40%RH.
- ③ Testing electrical characteristics of the unit to confirm there be no fault.

1. 結露特性試験

入力を切った状態で、恒温槽で-10°Cに冷却しておき、約1時間後に恒温槽から取り出し、室温25°C、湿度40%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

2. Values

Item	Data	Testing Conditions
Output Voltage [V]	24.081	Input Volt.: 100V, Load Current:0.5A
Line Regulation [mV]	7	Input Volt.: 85~132V, Load Current:0.5A
Load Regulation [mV]	21	Input Volt.: 100V, Load Current:0~0.5A



Model		LDA10F-24	Temperature		25°C
Item		Leakage Current 漏洩電流	Testing Circuitry		Figure B
Object		_____			

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DENTORI	0.11	0.14	0.17
(B) IEC60950	0.12	0.15	0.18

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B) IEC60950	—	—	—

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力 of 両相について測定し、その大きい方を漏洩電流測定値とする。



Model		LDA10F-24	Temperature 25°C Testing Circuitry Figure C
Item		Line Noise Tolerance 入力雑音耐量	
Object		+24.0V0.5A	

1. Results

Pulse Width [ nS ]	MODE	No protection failure should occur 保護回路の誤動作がない	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation
1000	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation

2. Conditions

Input Voltage : 100 V  
 Pulse Voltage : 2000 V  
 Pulse Cycle : 10 mS  
 Pulse Input Duration : 1 min. or more  
 Load : 100 %





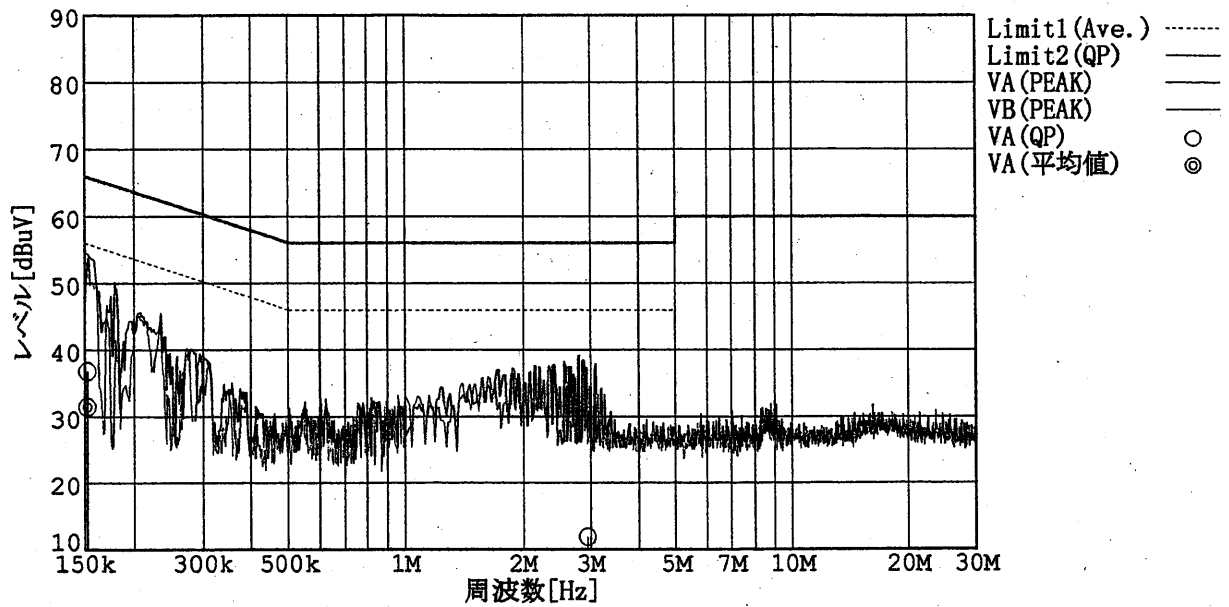
Model	LDA10F-24	Temperature	25°C
Item	Conducted Emission 雑音端子電圧	Testing Circuitry	Figure D
Object			

1. Graph

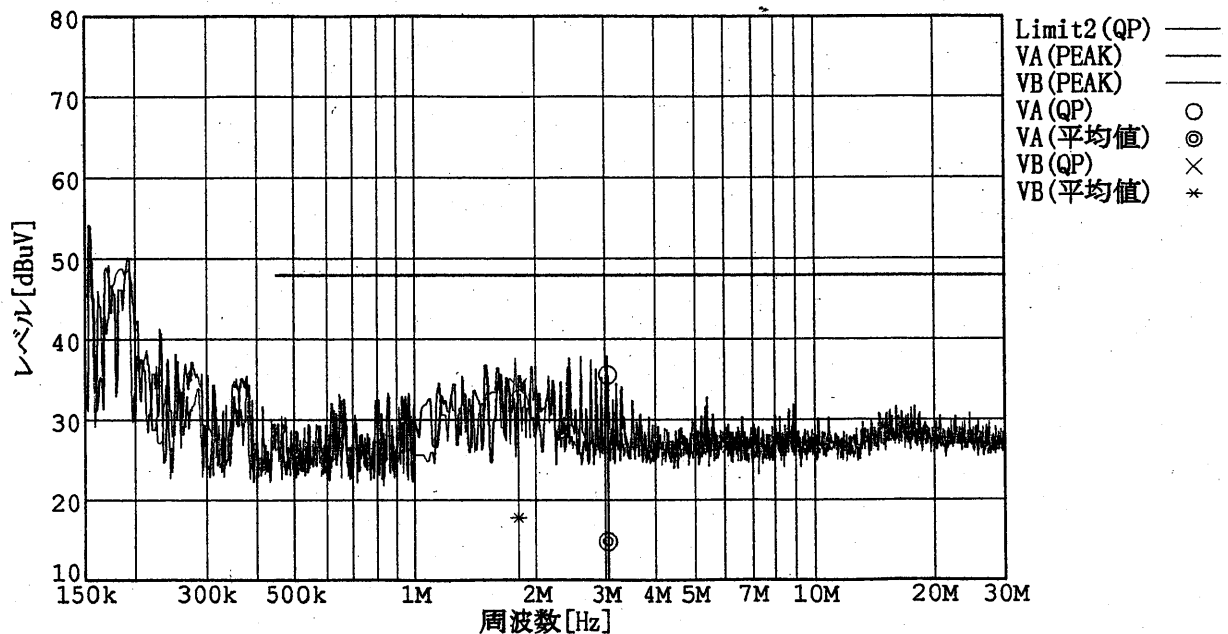
Remarks

Input Volt. 100 V (VCCI Class B)  
120 V (FCC Class B)  
Load 100 %

規格 1: [VCCI] Class B(平均值)  
規格 2: [VCCI] Class B(QP)



規格 2: [FCC Part15] Class B



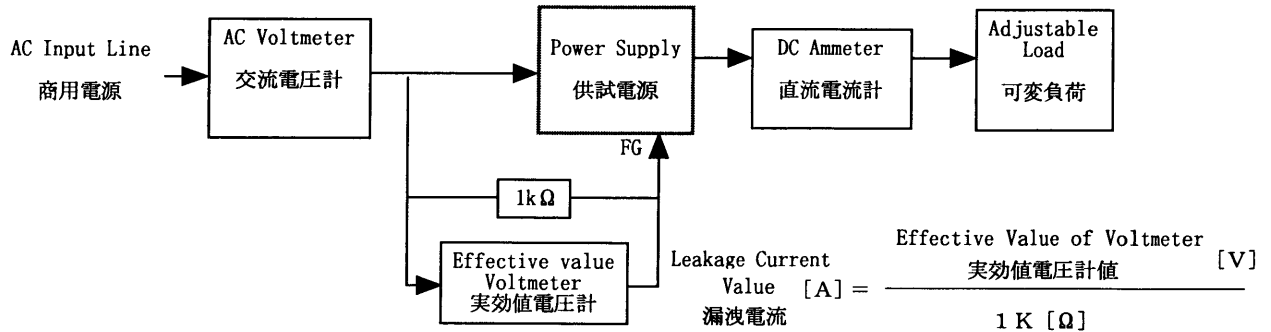
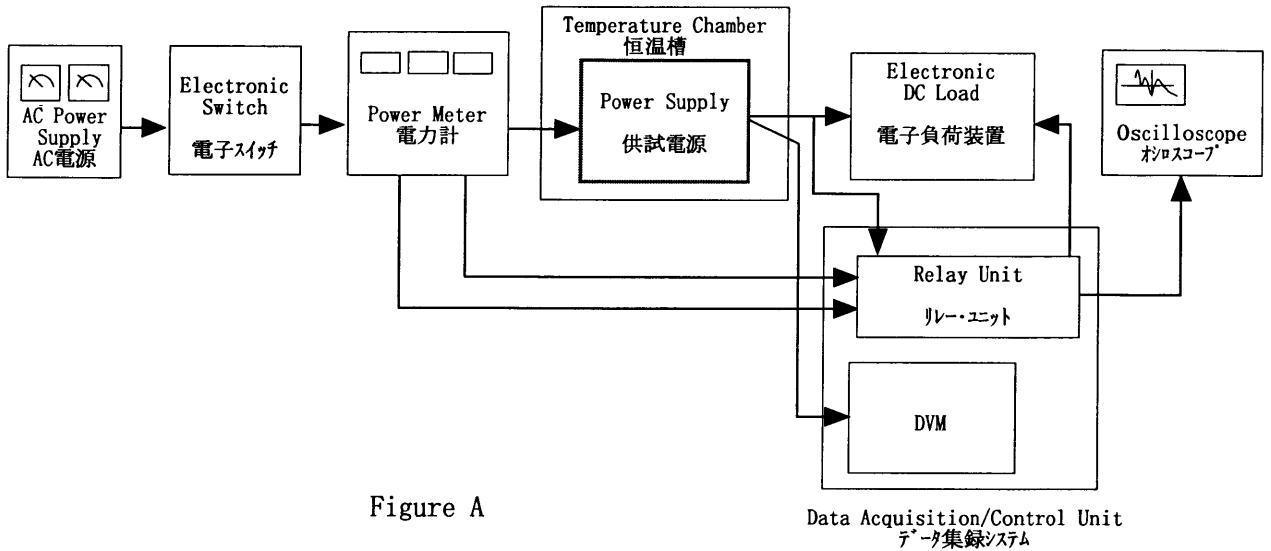


Figure B (DENTORI)

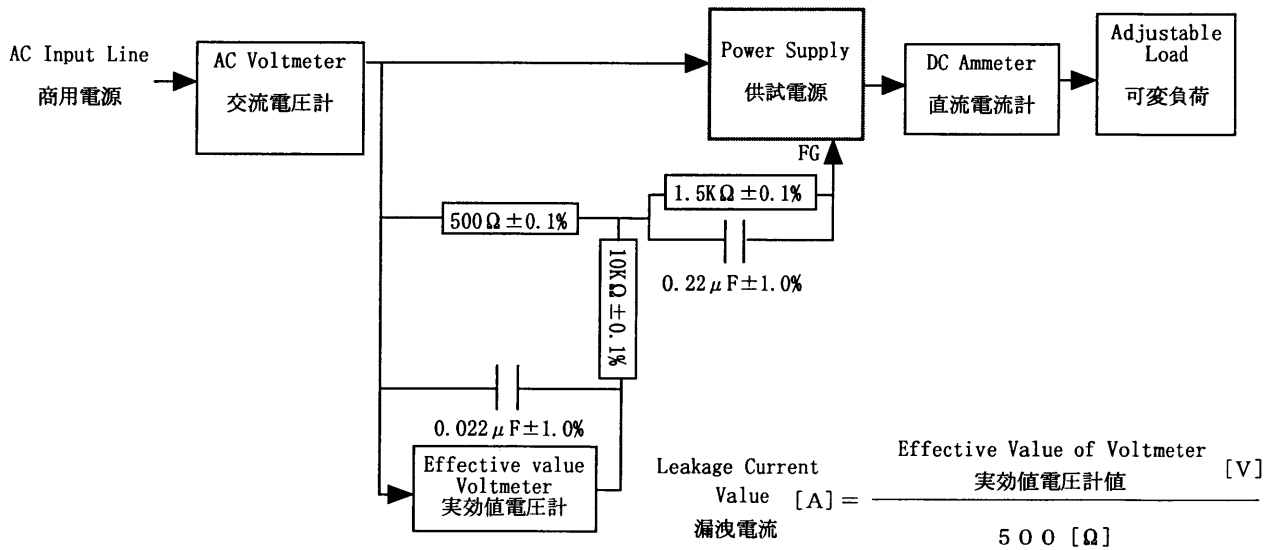


Figure B (IEC 60950)

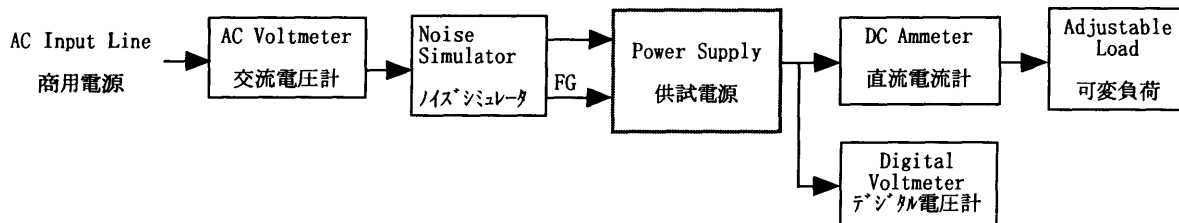


Figure C

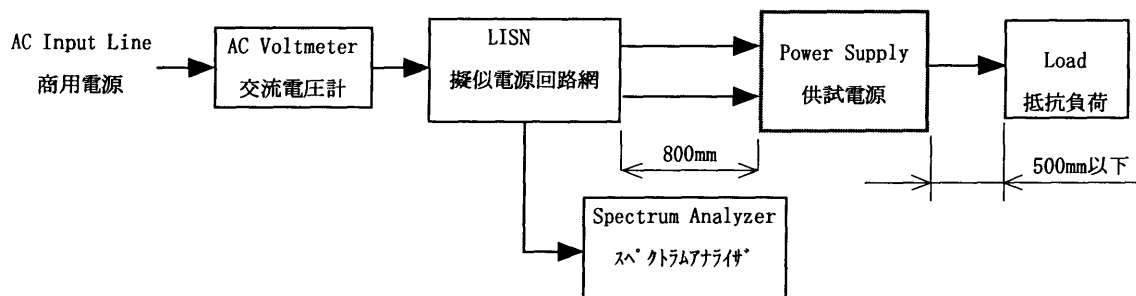


Figure D

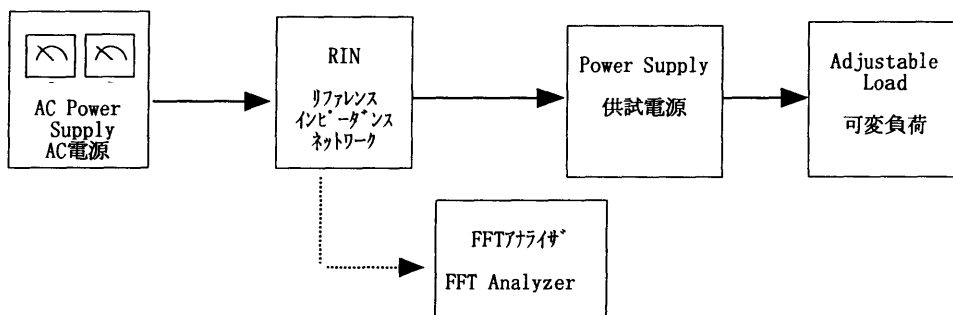


Figure E