



TEST DATA OF LDA10F-12 (200V INPUT)

Regulated DC Power Supply

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COSEL CO.,LTD.

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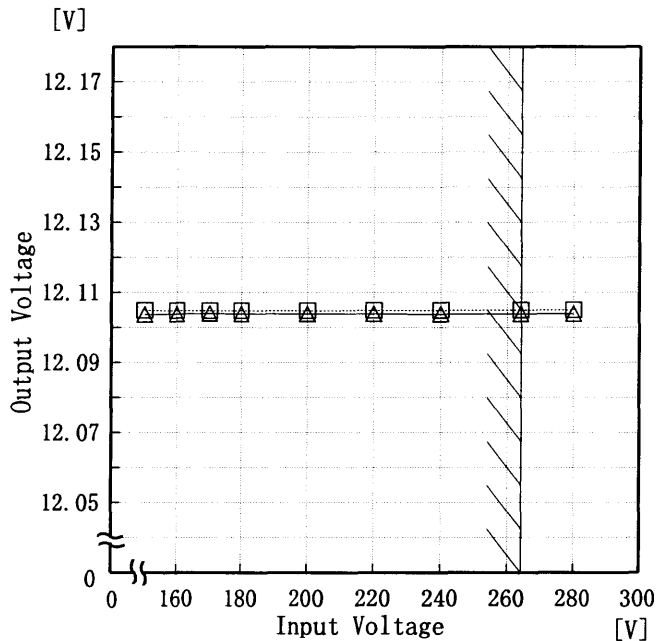
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Model	LDA10F-12
Item	Line Regulation 静的入力変動
Object	+12.0V0.9A

Temperature 25°C
 Testing Circuitry Figure A

1. Graph □ Load 50%
△ Load 100%



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
150	12.105	12.104
160	12.105	12.104
170	12.105	12.104
180	12.105	12.104
200	12.105	12.104
220	12.105	12.104
240	12.105	12.104
264	12.105	12.104
280	12.105	12.104

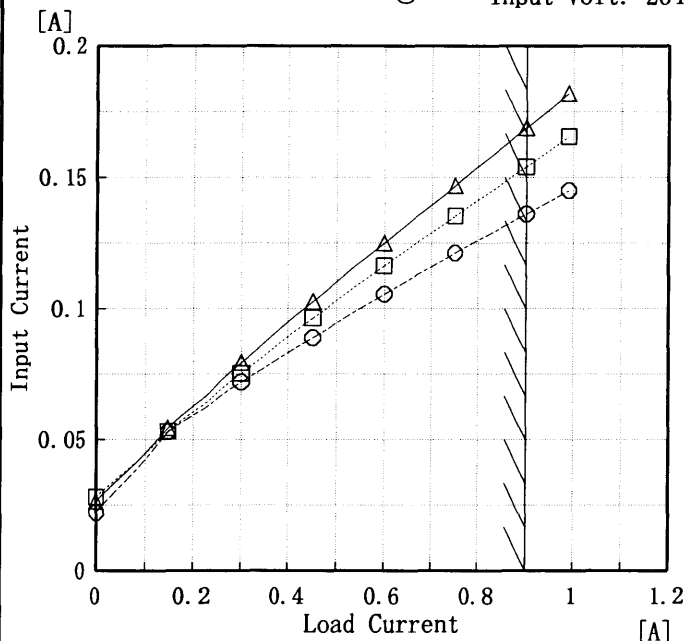


Model	LDA10F-12
Item	Input Current (by Load Current) 入力電流 (負荷特性)
Output	—————

Temperature 25°C
Testing Circuitry Figure A

1. Graph

—△— Input Volt. 170V
- - -□- - - Input Volt. 200V
- - -○- - - Input Volt. 264V



Note: Slanted line shows the range of the rated load current

(注) 斜線は定格負荷電流範囲を示す。

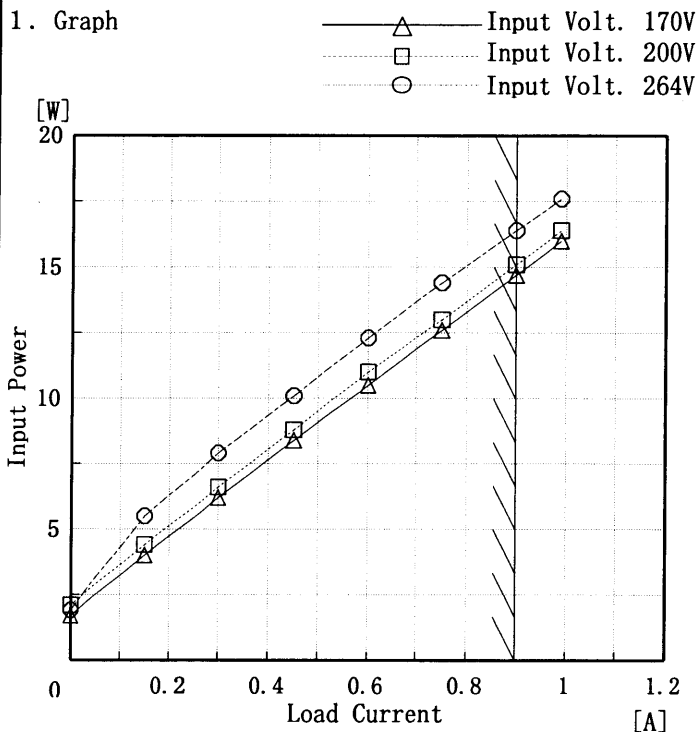
2. Values

Load Current [A]	Input Current [A]		
	Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]
0.00	0.026	0.028	0.022
0.15	0.054	0.053	0.053
0.30	0.079	0.075	0.072
0.45	0.103	0.096	0.089
0.60	0.125	0.116	0.106
0.75	0.147	0.135	0.121
0.90	0.169	0.154	0.136
0.99	0.182	0.166	0.145
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—



Model	LDA10F-12	Temperature	25°C
Item	Input Power (by Load Current) 入力電力 (負荷特性)	Testing Circuitry	Figure A
Output	_____		

1. Graph



2. Values

Load Current [A]	Input Power [W]		
	Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]
0.00	1.70	2.10	1.90
0.15	4.00	4.40	5.50
0.30	6.20	6.60	7.90
0.45	8.40	8.80	10.10
0.60	10.50	11.00	12.30
0.75	12.60	13.00	14.40
0.90	14.70	15.10	16.40
0.99	16.00	16.40	17.60
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

Note: Slanted line shows the range of the rated load current

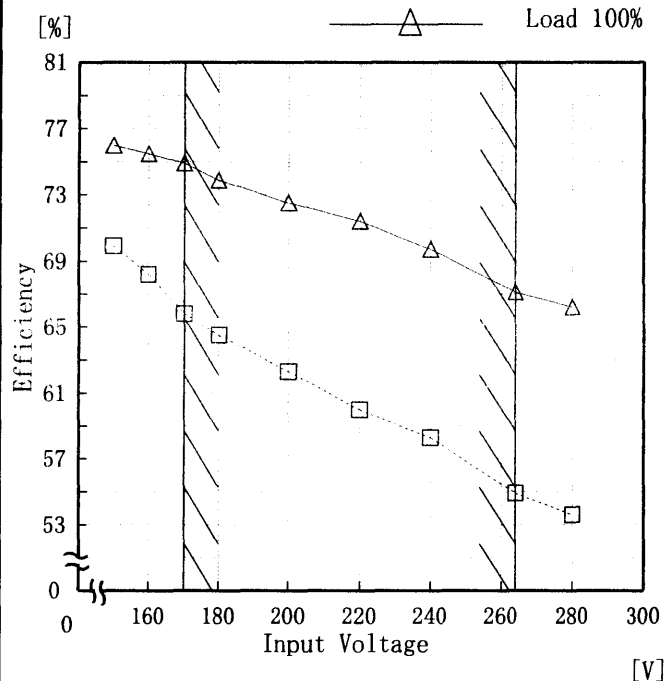
(注) 斜線は定格負荷電流範囲を示す。



Model	LDA10F-12
Item	Efficiency 効率
Object	_____

Temperature 25°C
Testing Circuitry Figure A

1. Graph □ Load 50%



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Efficiency [%]	
	Load 50%	Load 100%
150	69.9	76.0
160	68.2	75.5
170	65.8	74.9
180	64.5	73.9
200	62.3	72.5
220	60.0	71.4
240	58.3	69.7
264	54.9	67.1
280	53.6	66.2



Model		LDA10F-12		Temperature		25°C																																																								
Item		Efficiency (by Load Current) 効率 (負荷電流特性)		Testing Circuitry		Figure A																																																								
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<p> \triangle Input Volt. 170V \square Input Volt. 200V \circ Input Volt. 264V </p>				<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Efficiency [%]</th> </tr> <tr> <th>Input Volt. 170[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 264[V]</th> </tr> </thead> <tbody> <tr><td>0.15</td><td>46.4</td><td>42.3</td><td>33.9</td></tr> <tr><td>0.30</td><td>59.4</td><td>55.9</td><td>46.5</td></tr> <tr><td>0.45</td><td>65.6</td><td>62.5</td><td>54.5</td></tr> <tr><td>0.60</td><td>70.0</td><td>66.8</td><td>59.7</td></tr> <tr><td>0.75</td><td>72.8</td><td>70.6</td><td>63.7</td></tr> <tr><td>0.90</td><td>74.7</td><td>72.7</td><td>66.9</td></tr> <tr><td>0.99</td><td>75.4</td><td>73.6</td><td>68.6</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>				Load Current [A]	Efficiency [%]			Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]	0.15	46.4	42.3	33.9	0.30	59.4	55.9	46.5	0.45	65.6	62.5	54.5	0.60	70.0	66.8	59.7	0.75	72.8	70.6	63.7	0.90	74.7	72.7	66.9	0.99	75.4	73.6	68.6	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
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Input Voltage [V]	Hold-Up Time [mS]																																				
	Load 50%	Load 100%																																			
150	135	67																																			
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<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.</p> <p>Note: Slanted line shows the range of the rated input voltage.</p> <p>出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。</p> <p>(注)斜線は定格入力電圧範囲を示す。</p>																																					



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Item		Instantaneous Interruption Compensation 瞬時停電保障		Testing Circuitry		Figure A																																																				
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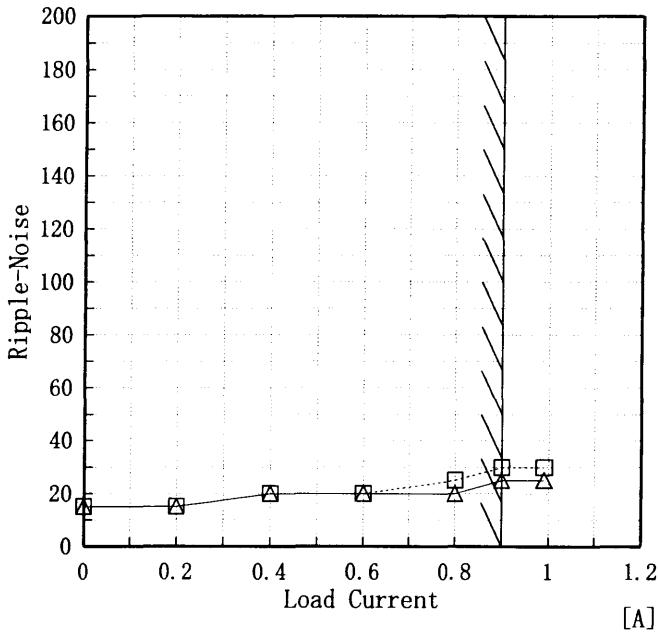


Model		LDA10F-12	Temperature		25°C																																						
Item		Ripple Voltage (by Load Current) リップル電圧 (負荷電流特性)	Testing Circuitry		Figure A																																						
Object		+12.0V0.9A																																									
<p>1. Graph</p> <p>[mV]</p> <p>□ ----- Input Volt. 170V</p> <p>△ ----- Input Volt. 264V</p> <p>Ripple Voltage</p> <p>Load Current [A]</p>			<p>2. Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th>Input Volt. 170 [V]</th> <th>Input Volt. 264 [V]</th> </tr> <tr> <th>Ripple Output Volt. [mV]</th> <th>Ripple Output Volt. [mV]</th> </tr> </thead> <tbody> <tr><td>0.00</td><td>10</td><td>10</td></tr> <tr><td>0.20</td><td>10</td><td>10</td></tr> <tr><td>0.40</td><td>10</td><td>10</td></tr> <tr><td>0.60</td><td>10</td><td>10</td></tr> <tr><td>0.80</td><td>10</td><td>10</td></tr> <tr><td>0.90</td><td>10</td><td>10</td></tr> <tr><td>0.99</td><td>10</td><td>10</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> <tr><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>			Load Current [A]	Input Volt. 170 [V]	Input Volt. 264 [V]	Ripple Output Volt. [mV]	Ripple Output Volt. [mV]	0.00	10	10	0.20	10	10	0.40	10	10	0.60	10	10	0.80	10	10	0.90	10	10	0.99	10	10	—	—	—	—	—	—	—	—	—	—	—	—
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<p>Ripple Voltage is shown as p-p in the figure below.</p> <p>Note: Slanted line shows the range of the rated load current.</p> <p>リップル電圧は、下図 p-p 値で示される。</p> <p>(注) 斜線は定格負荷電流範囲を示す。</p> <p>T1: Due to AC Input Line 入力商用周期</p> <p>T2: Due to Switching スイッチング周期</p> <p>Ripple [mVp-p]</p> <p>T1</p> <p>T2</p> <p>Fig. Complex Ripple Wave Form 図 リップル波形詳細図</p>																																											



Model	LDA10F-12	Temperature	25°C
Item	Ripple-Noise リップルノイズ	Testing Circuitry	Figure A
Object	+12.0V0.9A		

1. Graph
 [mV]
 □ Input Volt. 170V
 △ Input Volt. 264V



2. Values

Load current [A]	Input Volt. 170 [V]	Input Volt. 264 [V]
	Ripple-Noise [mV]	Ripple-Noise [mV]
0.00	15	15
0.20	15	15
0.40	20	20
0.60	20	20
0.80	25	20
0.90	30	25
0.99	30	25
—	—	—
—	—	—
—	—	—
—	—	—

Ripple-Noise is shown as p-p in the figure below.
 Note: Slanted line shows the range of the rated load current.

リップルノイズは、下図 p-p 値で示される。
 (注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
 入力商用周期
 T2: Due to Switching
 スイッチング周期

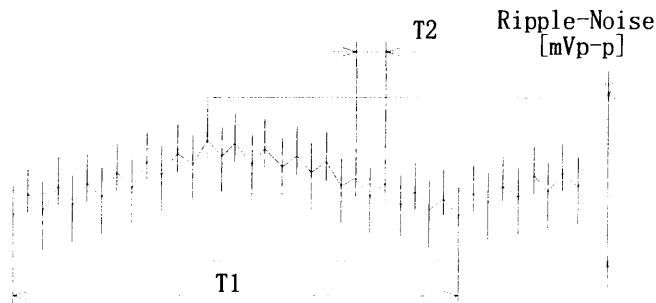


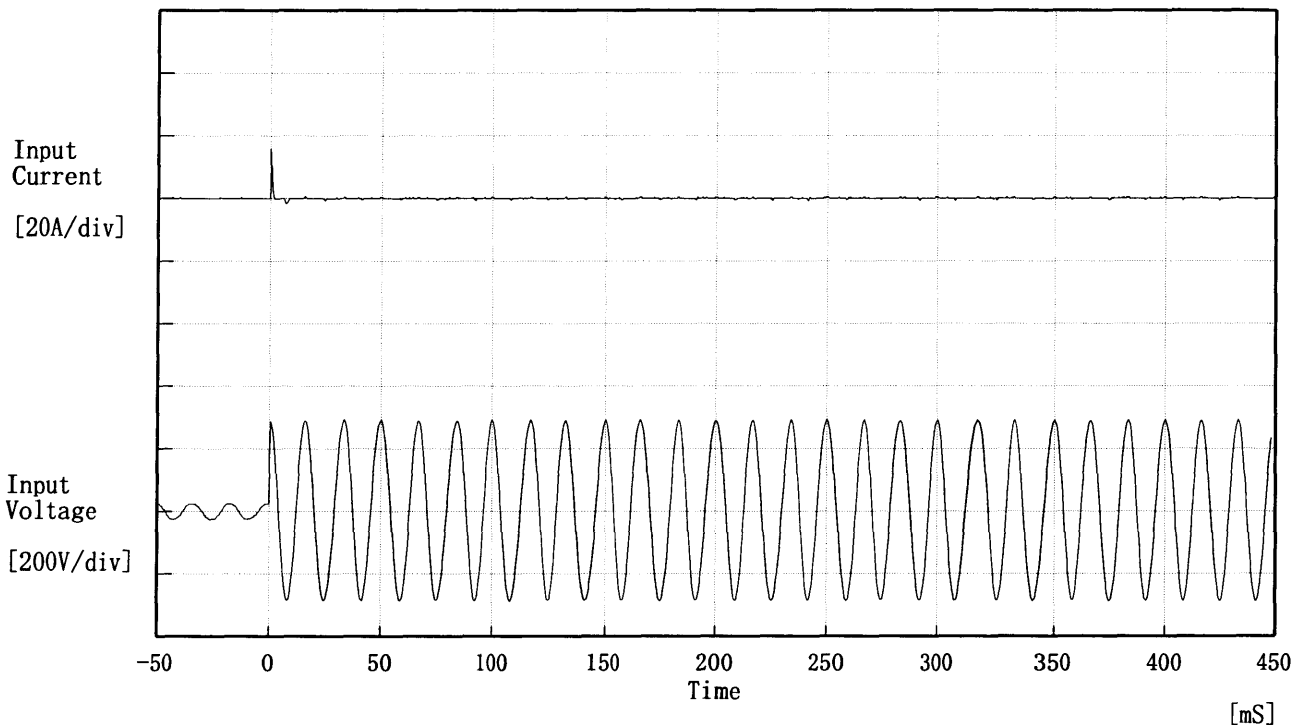
Fig. Complex Ripple Wave Form
 図 リップル波形詳細図

COSEL

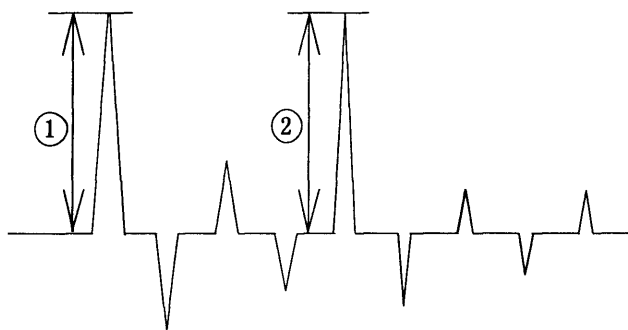
Model LDA10F-12		Temperature 25°C Testing Circuitry Figure A																																																							
Item Overcurrent Protection 過電流保護																																																									
Object +12.0V0.9A		2. Values																																																							
<p>1. Graph</p> <p>[V]</p> <p>Output Voltage</p> <p>Legend: ——— Input Volt. 170 V ——— Input Volt. 200 V ——— Input Volt. 264 V</p> <p>Load Current [A]</p>																																																									
<p>Note: Slanted line shows the range of the rated load current.</p> <p>(注)斜線は定格負荷電流範囲を示す。</p>		<table border="1"> <thead> <tr> <th rowspan="2">Output Voltage [V]</th> <th colspan="3">Load Current [A]</th> </tr> <tr> <th>Input Volt. 170[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 264[V]</th> </tr> </thead> <tbody> <tr><td>12.00</td><td>1.27</td><td>1.27</td><td>1.31</td></tr> <tr><td>11.40</td><td>1.29</td><td>1.29</td><td>1.33</td></tr> <tr><td>10.80</td><td>1.31</td><td>1.31</td><td>1.34</td></tr> <tr><td>9.60</td><td>1.35</td><td>1.35</td><td>1.38</td></tr> <tr><td>8.40</td><td>1.39</td><td>1.38</td><td>1.41</td></tr> <tr><td>7.20</td><td>1.41</td><td>1.40</td><td>1.43</td></tr> <tr><td>6.00</td><td>1.43</td><td>1.41</td><td>1.44</td></tr> <tr><td>4.80</td><td>1.43</td><td>1.41</td><td>1.44</td></tr> <tr><td>3.60</td><td>1.40</td><td>1.39</td><td>1.43</td></tr> <tr><td>2.40</td><td>1.35</td><td>1.34</td><td>1.38</td></tr> <tr><td>1.20</td><td>1.23</td><td>1.24</td><td>1.31</td></tr> <tr><td>0.00</td><td>1.01</td><td>1.04</td><td>1.15</td></tr> </tbody> </table>	Output Voltage [V]	Load Current [A]			Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]	12.00	1.27	1.27	1.31	11.40	1.29	1.29	1.33	10.80	1.31	1.31	1.34	9.60	1.35	1.35	1.38	8.40	1.39	1.38	1.41	7.20	1.41	1.40	1.43	6.00	1.43	1.41	1.44	4.80	1.43	1.41	1.44	3.60	1.40	1.39	1.43	2.40	1.35	1.34	1.38	1.20	1.23	1.24	1.31	0.00	1.01	1.04	1.15
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Model	LDA10F-12	Temperature	25°C
Item	Inrush Current 突入電流	Testing Circuitry	Figure A
Object	_____		



Input Voltage 200 V
 Frequency 60 Hz
 Load 100 %
 Inrush Current
 ① 16.00 [A]
 ② 0.80 [A]





Model		LDA10F-12	Temperature 25°C Testing Circuitry Figure A
Item		Dynamic Load Responce 動的負荷変動	
Object		+12.0V0.9A	

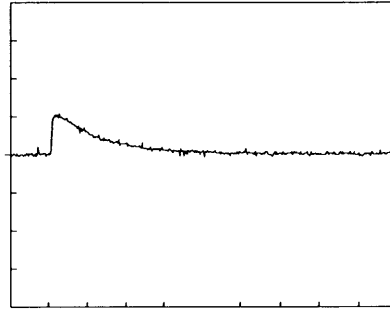
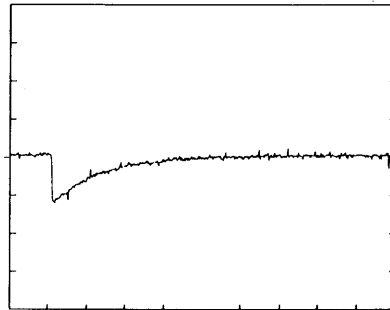
Input Volt. 200 V
Cycle 1000 mS

Load Current



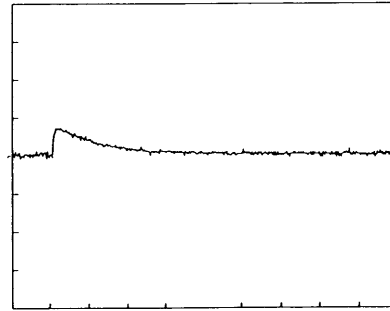
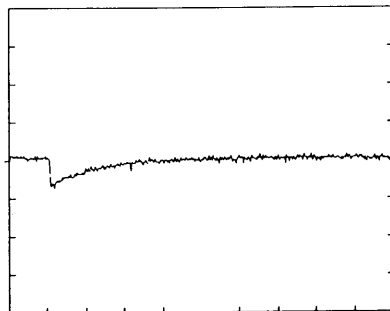
Load 0% ↔

Load 100 %



Load 0% ↔

Load 50 %



200 mV/div

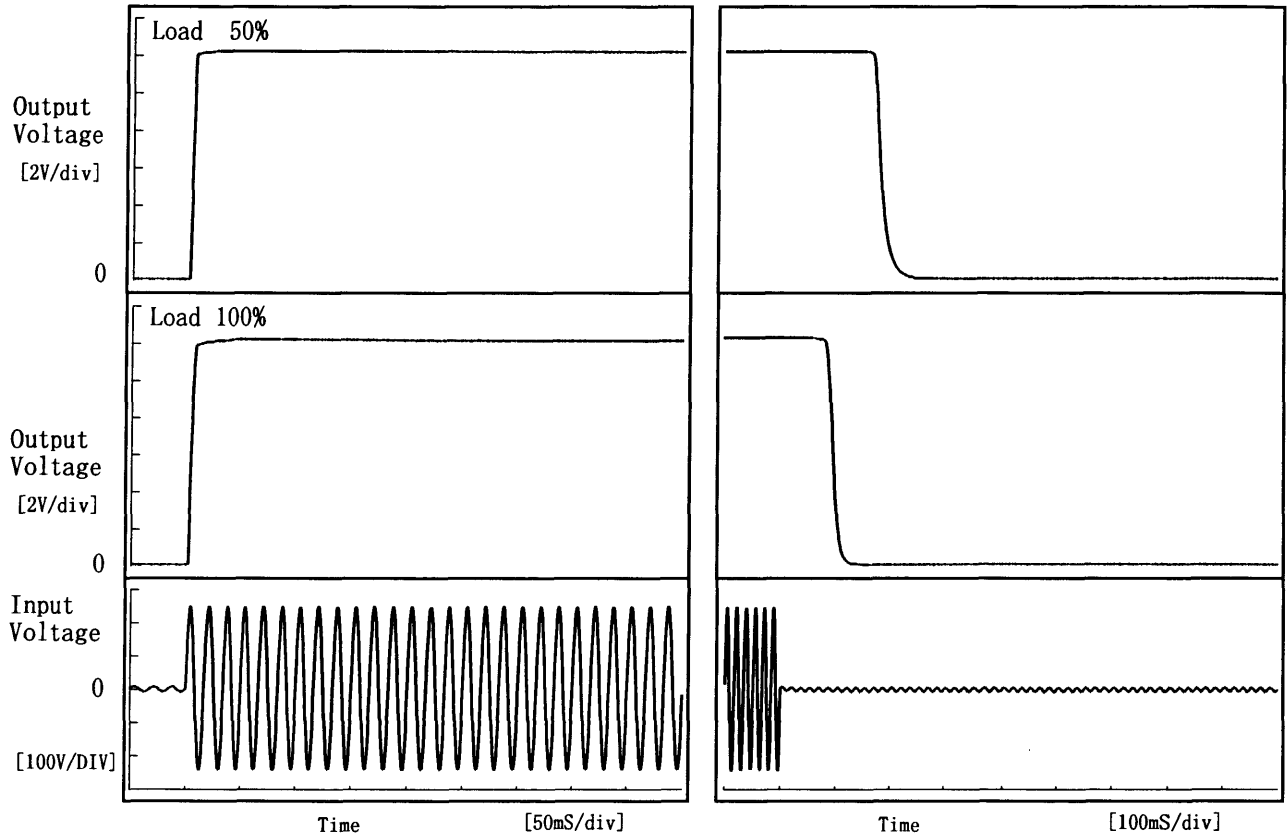
10 mS/div



Model	LDA10F-12	Temperature	25°C
Item	Rise and Fall Time 立上り、立下り時間	Testing Circuitry	Figure A
Object	+12.0V0.9A		

1. Graph

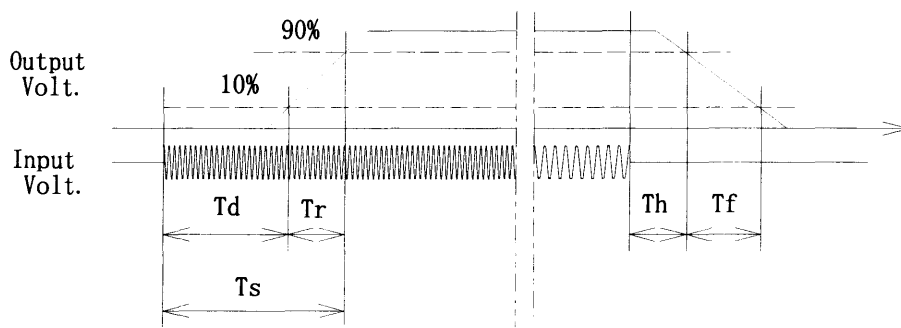
Input Volt. 170 V



2. Values

[mS]

Load \ Time	T d	T r	T s	T h	T f
50 %	2.0	4.0	6.0	174.0	30.0
100 %	2.0	5.3	7.3	89.0	20.5



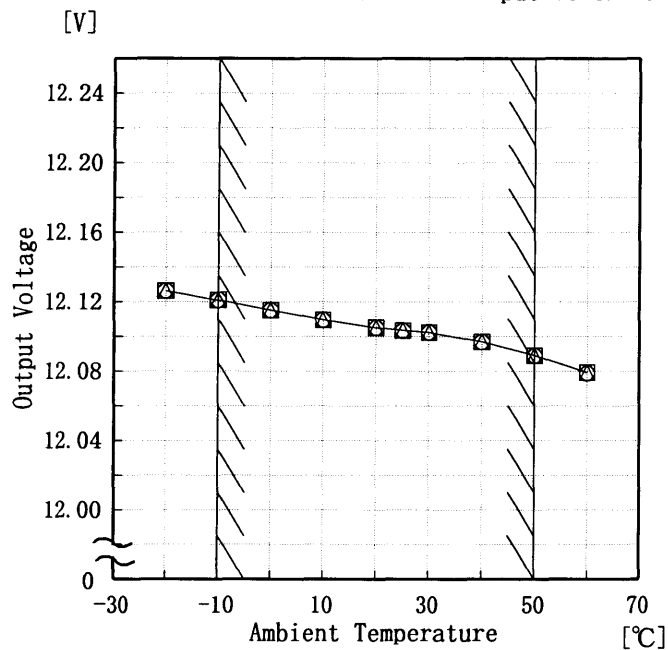


Model	LDA10F-12
Item	Ambient Temperature Drift 周囲温度変動
Object	+12.0V0.9A

Testing Circuitry Figure A

1. Graph

- △— Input Volt. 170V
- Input Volt. 200V
- Input Volt. 264V



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

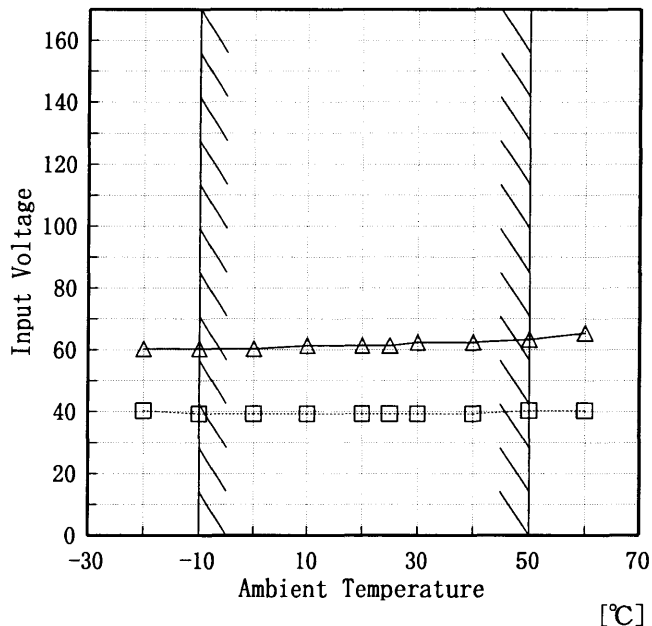
Temperature [°C]	Output Voltage [V]		
	Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]
-20	12.126	12.126	12.126
-10	12.121	12.121	12.121
0	12.115	12.115	12.115
10	12.110	12.110	12.110
20	12.105	12.105	12.105
25	12.104	12.103	12.103
30	12.102	12.102	12.102
40	12.097	12.097	12.097
50	12.089	12.089	12.089
60	12.079	12.079	12.079
—	—	—	—



Model	LDA10F-12
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+12.0V0.9A

Testing Circuitry Figure A

1. Graph
 [V] □ Load 50%
 △ Load 100%



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

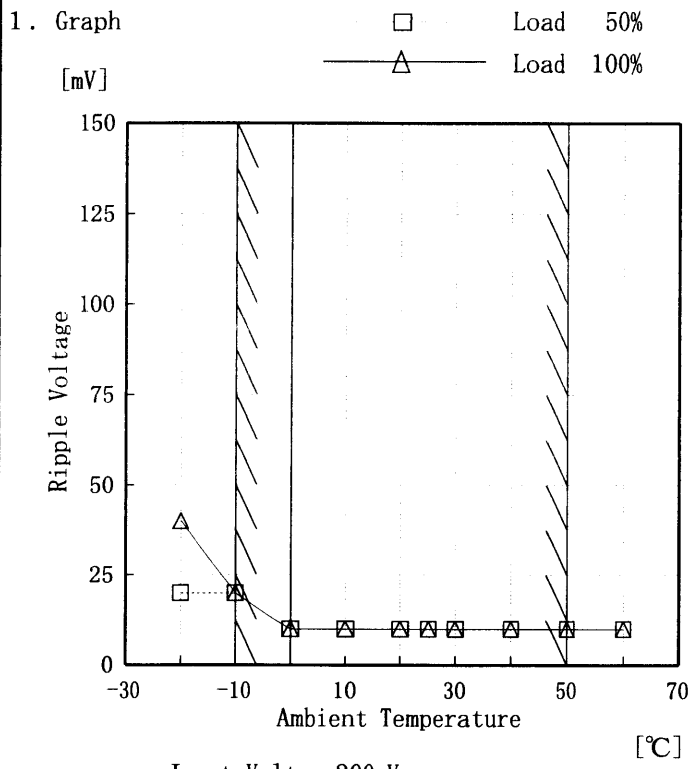
2. Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	40	60
-10	39	60
0	39	60
10	39	61
20	39	61
25	39	61
30	39	62
40	39	62
50	40	63
60	40	65
—	—	—



Model	LDA10F-12
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)
Object	+12.0V0.9A

Testing Circuitry Figure A



Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。

2. Values

Ambient Temp. [°C]	Load 50%	Load 100%
	Ripple Output Volt. [mV]	Ripple Output Volt. [mV]
-20	20	40
-10	20	20
0	10	10
10	10	10
20	10	10
25	10	10
30	10	10
40	10	10
50	10	10
60	10	10
—	—	—



COSEL																								
Model	LDA10F-12																							
Item	Time Lapse Drift 経時ドリフト	Temperature 25°C Testing Circuitry Figure A																						
Object	+12.0V0.9A																							
<p>1. Graph</p> <p style="text-align: center;">Input Volt. 200V Load 100%</p>		<p>2. Values</p> <table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>12.111</td></tr> <tr><td>0.5</td><td>12.104</td></tr> <tr><td>1.0</td><td>12.106</td></tr> <tr><td>2.0</td><td>12.106</td></tr> <tr><td>3.0</td><td>12.105</td></tr> <tr><td>4.0</td><td>12.106</td></tr> <tr><td>5.0</td><td>12.106</td></tr> <tr><td>6.0</td><td>12.106</td></tr> <tr><td>7.0</td><td>12.107</td></tr> <tr><td>8.0</td><td>12.104</td></tr> </tbody> </table>	Time since start [H]	Output Voltage [V]	0.0	12.111	0.5	12.104	1.0	12.106	2.0	12.106	3.0	12.105	4.0	12.106	5.0	12.106	6.0	12.106	7.0	12.107	8.0	12.104
Time since start [H]	Output Voltage [V]																							
0.0	12.111																							
0.5	12.104																							
1.0	12.106																							
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3.0	12.105																							
4.0	12.106																							
5.0	12.106																							
6.0	12.106																							
7.0	12.107																							
8.0	12.104																							



COSEL		
Model	LDA10F-12	
Item	Output Voltage Accuracy 定電圧精度	Testing Circuitry Figure A
Object	+12.0V0.9A	

Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature -10~50 °C

Input Voltage : 170~264 V

Load Current : 0~0.9 A

* Output Voltage Accuracy = $\pm (\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

* Output Voltage Accuracy (Ration) = $\frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$

定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 -10~50 °C

入力電圧 170~264 V

負荷電流 0~0.9 A

* 定電圧精度(変動値) = $\pm (\text{出力電圧の最高値} - \text{出力電圧の最低値}) / 2$

* 定電圧精度(変動率) = $\frac{\text{変動値}}{\text{定格出力電圧}} \times 100$

Item	Temperature [°C]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy (Ration) [%]
Maximum Voltage	-10	170	0	12.123	±19	±0.2
Minimum Voltage	50	264	0	12.086		



COSEL		
Model	LDA10F-12	
Item	Condensation 結露特性	Testing Circuitry Figure A
Object	+12.0V0.9A	

1. Condensation test

Testing procedure is as follows.

- ① Keeping and cooling the unit in a tank at -10°C for an hour with the input off.
- ② Taking it out of the tank and dewing itself in a room where the temperature is 25°C and the humidity is 40%RH.
- ③ Testing electrical characteristics of the unit to confirm there be no fault.

1. 結露特性試験

入力を切った状態で、恒温槽で-10°Cに冷却しておき、約1時間後に恒温槽から取り出し、室温25°C、湿度40%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

2. Values

Item	Data	Testing Conditions
Output Voltage [V]	12.105	Input Volt.: 200V, Load Current:0.9A
Line Regulation [mV]	3	Input Volt.: 170~264V, Load Current:0.9A
Load Regulation [mV]	8	Input Volt.: 200V, Load Current:0~0.9A



Model		LDA10F-12	Temperature		25°C
Item		Leakage Current 漏洩電流	Testing Circuitry		Figure B
Object		_____			

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DENTORI	---	---	---
(B) IEC60950	---	---	---

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B) IEC60950	0.23	0.26	0.28

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力 of 両相について測定し、その大きい方を漏洩電流測定値とする。



Model		LDA10F-12	Temperature 25°C Testing Circuitry Figure C
Item		Line Noise Tolerance 入力雑音耐量	
Object		+12.0V0.9A	

1. Results

Pulse Width [nS]	MODE	No protection failure should occur 保護回路の誤動作がない	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation
1000	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation

2. Conditions

Input Voltage : 200 V
 Pulse Voltage : 2000 V
 Pulse Cycle : 10 mS
 Pulse Input Duration : 1 min. or more
 Load : 100 %

COSEL

Model	LDA10F-12	Temperature	25°C
Item	Conducted Emission 雑音端子電圧	Testing Circuitry	Figure D
Object	_____		

1. Graph

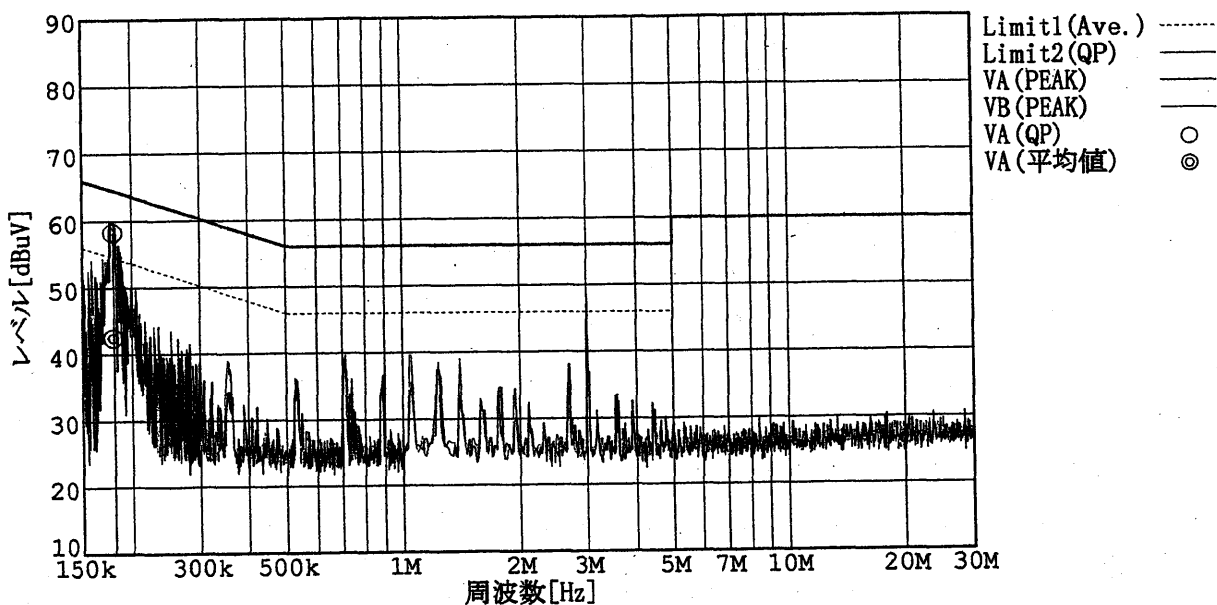
Remarks

Input Volt. 230 V

Load 100 %

規格 1: [EN 55022] Class B (平均値)

規格 2: [EN 55022] Class B (QP)



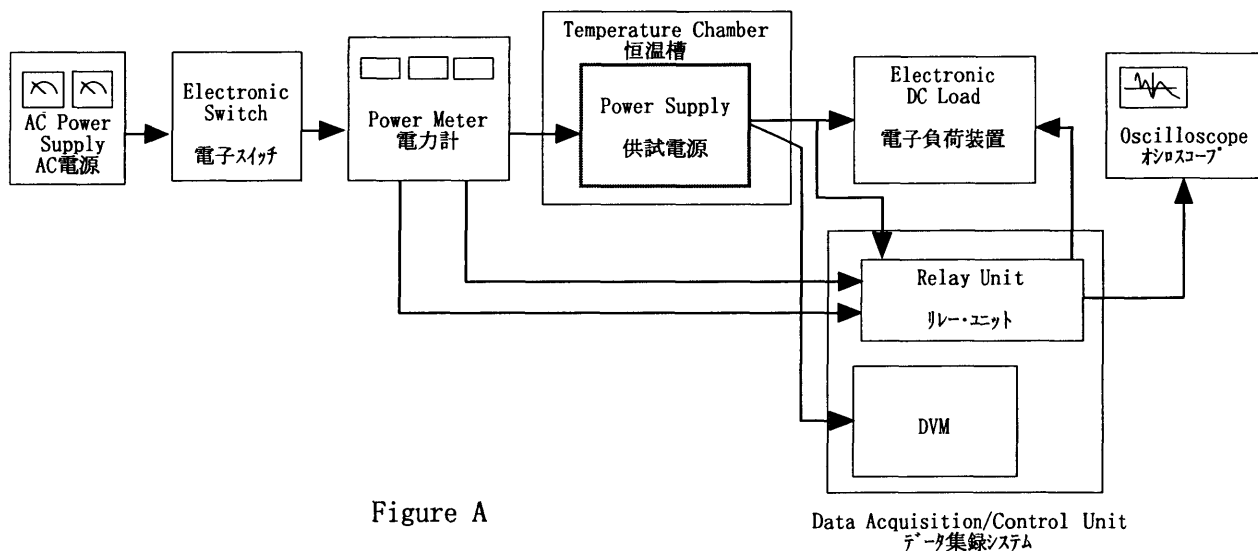


Figure A

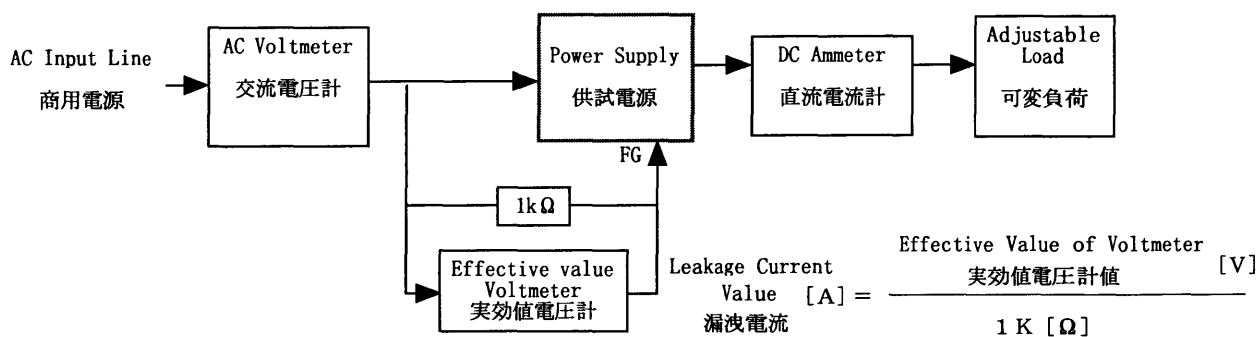


Figure B (DENTORI)

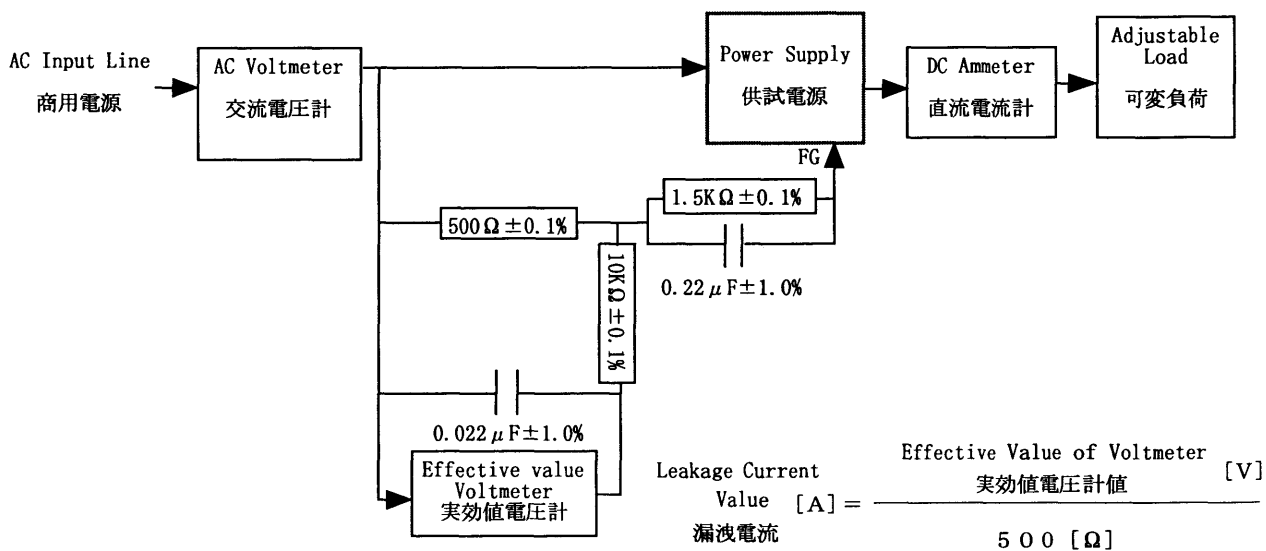


Figure B (IEC 60950)

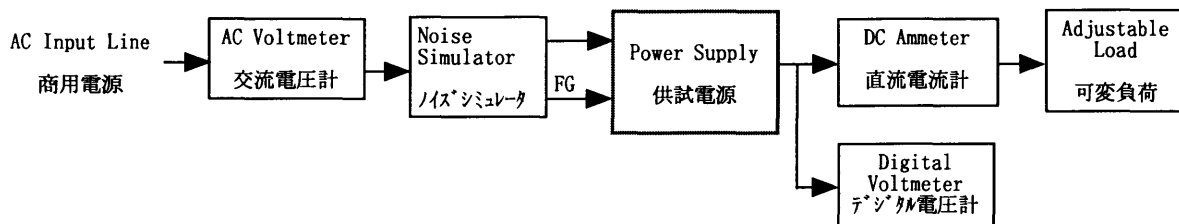


Figure C

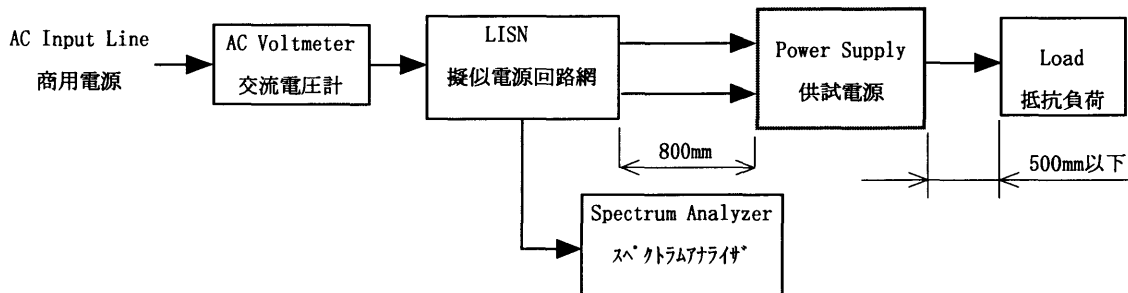


Figure D

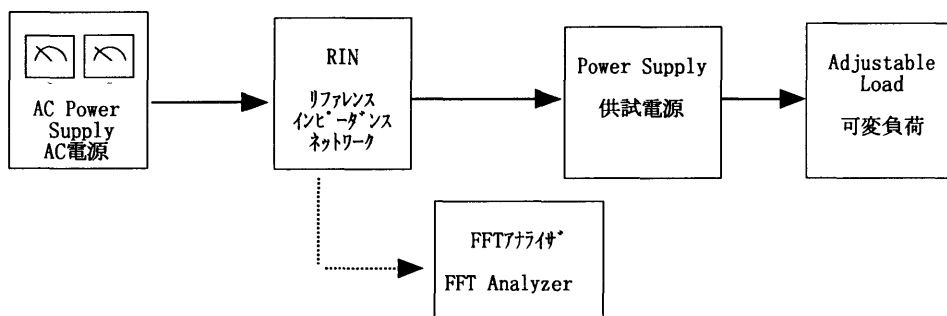


Figure E