

TEST DATA OF ADA750F**ADA750F-30
(200V INPUT)****Regulated DC power supply
Jan. 27, 2003**

Approved by : *Kuniaki Nagahara*
Kuniaki Nagahara Design Manager

Prepared by : *Katsumi Ishikawa*
Katsumi Ishikawa Design Engineer

INPUT : AC 170~264V**OUTPUT : V1: 30V 24.5A****コーセル株式会社
COSEL CO.,LTD.**

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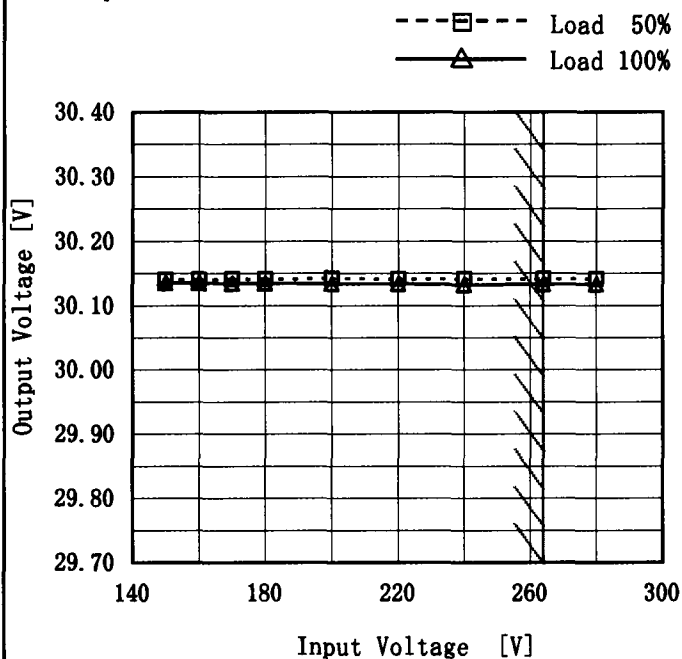
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Model	ADA750F (ADA750F-30)
Item	Line Regulation 静的入力変動
Object	V1:+30V24.5A

Temperature 25°C
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注) 斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
150	30.141	30.135
160	30.141	30.135
170	30.141	30.134
180	30.142	30.134
200	30.142	30.134
220	30.141	30.134
240	30.141	30.133
264	30.141	30.133
280	30.141	30.133



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<p> —△— Input Volt. 170V ---□--- Input Volt. 200V -·○-·- Input Volt. 264V </p> <p>Hold-Up Time [mS]</p> <p>Load Power [W]</p>			<table border="1"> <thead> <tr> <th rowspan="2">Load Power [W]</th> <th colspan="3">Hold-Up Time [mS]</th> </tr> <tr> <th>Input Volt. 170[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 264[V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>147.0</td><td>158</td><td>159</td><td>161</td></tr> <tr><td>294.0</td><td>80</td><td>82</td><td>83</td></tr> <tr><td>441.0</td><td>52</td><td>54</td><td>55</td></tr> <tr><td>588.0</td><td>38</td><td>39</td><td>40</td></tr> <tr><td>735.0</td><td>30</td><td>31</td><td>32</td></tr> <tr><td>808.5</td><td>26</td><td>27</td><td>29</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>			Load Power [W]	Hold-Up Time [mS]			Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]	0.0	—	—	—	147.0	158	159	161	294.0	80	82	83	441.0	52	54	55	588.0	38	39	40	735.0	30	31	32	808.5	26	27	29	--	—	—	—	--	—	—	—	--	—	—	—	--	—	—	—
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<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy. Note: Slanted line shows the range of the rated load power.</p> <p>出力保持時間とは、入力電圧断から出力電圧が定電圧精度の範囲を保持しているところまでの時間。 (注) 斜線は定格電力範囲を示す。</p>																																																								



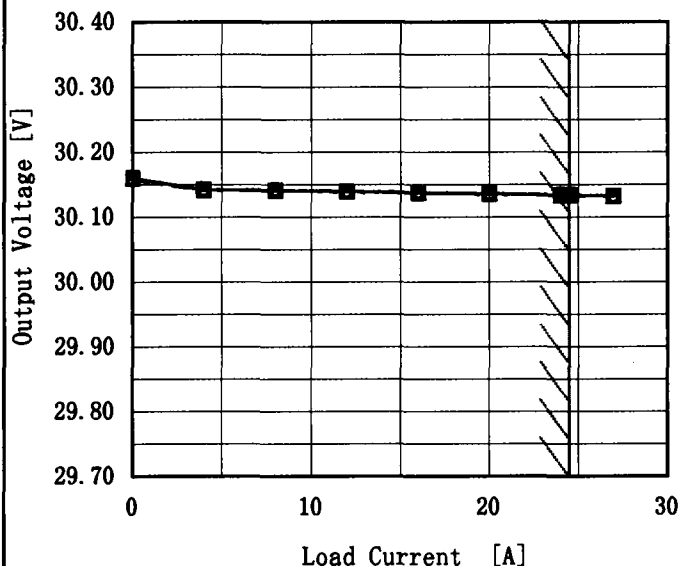
Model		ADA750F (ADA750F-30)		Temperature		25°C																																																				
Item		Instantaneous Interruption Compensation (by Load Power) 瞬時停電保障 (負荷電力特性)		Testing Circuitry		Figure A																																																				
Object		_____																																																								
1. Graph				2. Values																																																						
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Load Power [W]	Time [mS]																																																									
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Model	ADA750F (ADA750F-30)
Item	Load Regulation 静的負荷変動
Object	V1:+30V24.5A

Temperature 25°C
Testing Circuitry Figure A

1. Graph
- △— Input Volt. 170 V
 - Input Volt. 200 V
 - Input Volt. 264 V



Note: Slanted line shows the range of the rated load current.

(注) 斜線は定格負荷電流範囲を示す。

2. Values

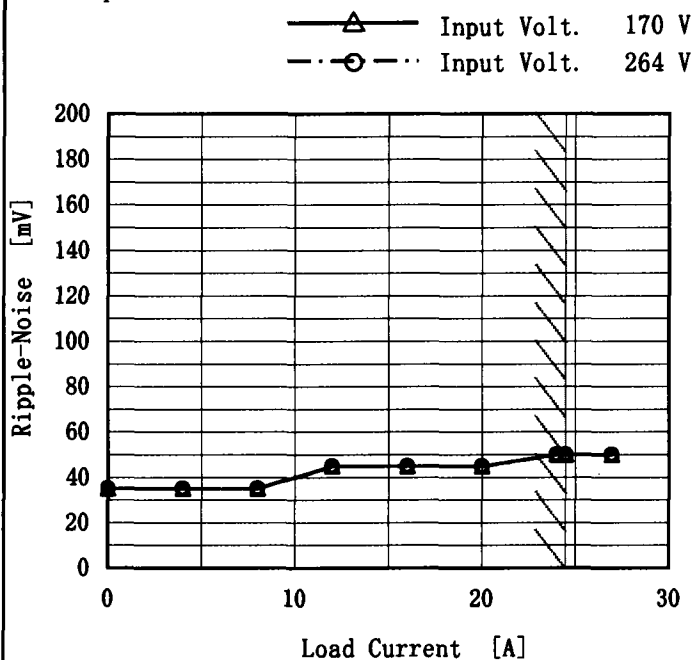
Load Current [A]	Output Voltage [V]		
	Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]
0.00	30.160	30.160	30.159
4.00	30.142	30.143	30.142
8.00	30.141	30.141	30.142
12.00	30.140	30.139	30.140
16.00	30.137	30.137	30.138
20.00	30.136	30.136	30.136
24.00	30.134	30.134	30.134
24.50	30.134	30.134	30.134
26.95	30.132	30.132	30.133
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<p>Model ADA750F (ADA750F-30)</p>		<p>Temperature 25°C</p>																																							
<p>Item Ripple Voltage (by Load Current) リップル電圧 (負荷特性)</p>		<p>Testing Circuitry Figure A</p>																																							
<p>Object V1:+30V24.5A</p>																																									
<p>1. Graph</p> <div style="text-align: right;"> <p>—△— Input Volt. 170 V</p> <p>-●- Input Volt. 264 V</p> </div> <p>Ripple Voltage [mV]</p> <p>Load Current [A]</p>		<p>2. Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="2">Ripple Output Voltage [mV]</th> </tr> <tr> <th>Input Volt. 170[V]</th> <th>Input Volt. 264[V]</th> </tr> </thead> <tbody> <tr><td>0.00</td><td>20</td><td>20</td></tr> <tr><td>4.00</td><td>30</td><td>30</td></tr> <tr><td>8.00</td><td>30</td><td>30</td></tr> <tr><td>12.00</td><td>40</td><td>40</td></tr> <tr><td>16.00</td><td>40</td><td>40</td></tr> <tr><td>20.00</td><td>40</td><td>40</td></tr> <tr><td>24.00</td><td>45</td><td>45</td></tr> <tr><td>24.50</td><td>45</td><td>45</td></tr> <tr><td>26.95</td><td>45</td><td>45</td></tr> <tr><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>		Load Current [A]	Ripple Output Voltage [mV]		Input Volt. 170[V]	Input Volt. 264[V]	0.00	20	20	4.00	30	30	8.00	30	30	12.00	40	40	16.00	40	40	20.00	40	40	24.00	45	45	24.50	45	45	26.95	45	45	--	--	--	--	--	--
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<p>Ripple Voltage is shown as p-p in the figure below.</p> <p>Note: Slanted line shows the range of the rated load current.</p>																																									
<p>リップル電圧は、下図 p-p 値で示される。 (注) 斜線は定格負荷電流範囲を示す。</p>																																									
<p>T1: Due to AC Input Line 入力商用周期</p> <p>T2: Due to Switching スイッチング周期</p>																																									
<p>Fig. Complex Ripple Wave Form 図 リップル波形詳細図</p>																																									

Model	ADA750F (ADA750F-30)
Item	Ripple-Noise リップルノイズ
Object	V1:+30V24.5A

Temperature 25°C
Testing Circuitry Figure A

1. Graph



2. Values

Load Current [A]	Ripple-Noise [mV]	
	Input Volt. 170[V]	Input Volt. 264[V]
0.00	35	35
4.00	35	35
8.00	35	35
12.00	45	45
16.00	45	45
20.00	45	45
24.00	50	50
24.50	50	50
26.95	50	50
--	--	--
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Ripple-Noise is shown as p-p in the figure below.
Note: Slanted line shows the range of the rated load current.

リップルノイズは、下図 p-p 値で示される。
(注) 斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
スイッチング周期

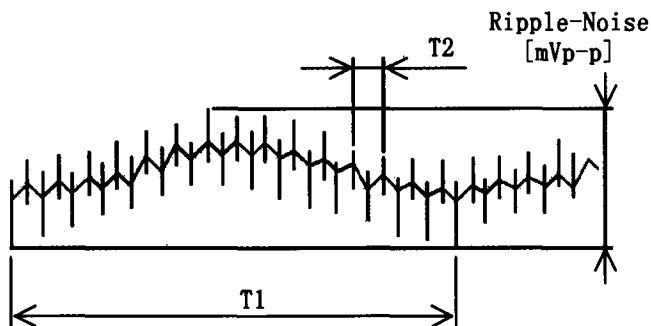


Fig. Complex Ripple Wave Form
図 リップル波形詳細図

COSEL

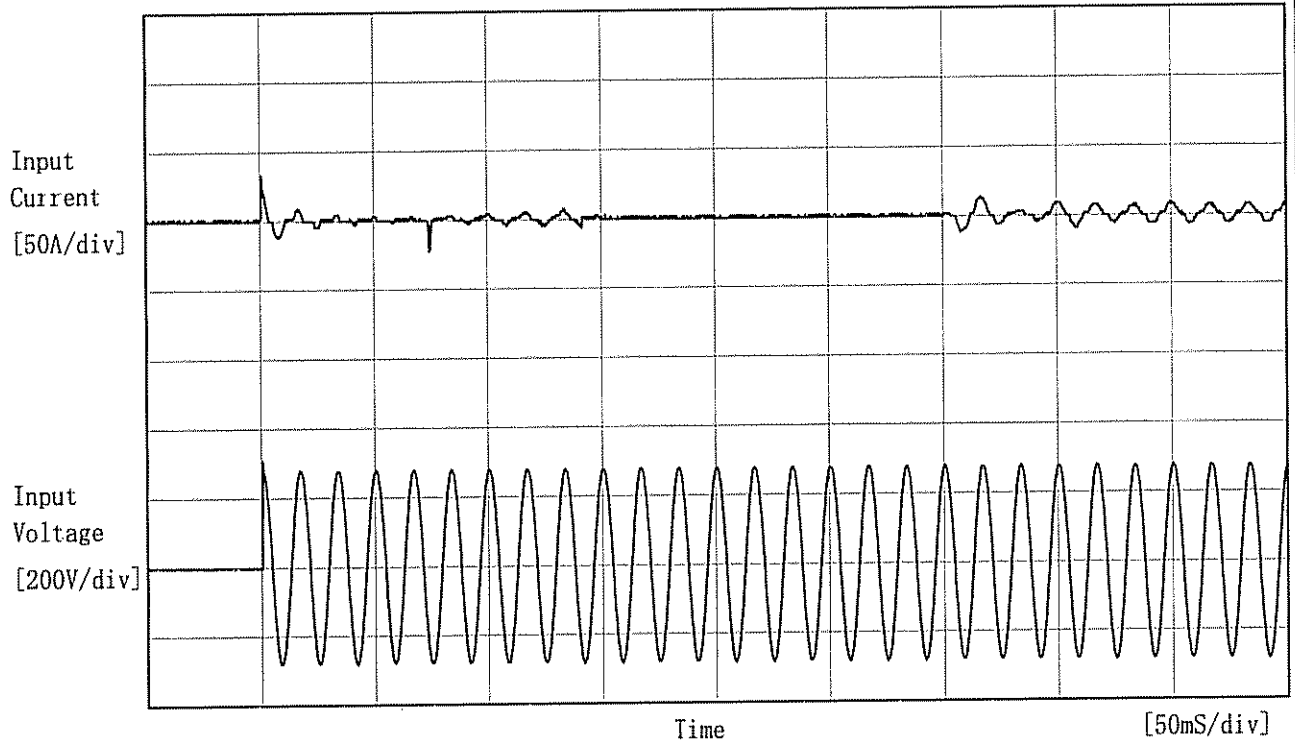
<p>Model ADA750F (ADA750F-30)</p> <p>Item Overcurrent Protection 過電流保護</p> <p>Object V1:+30V24.5A</p>		<p>Temperature 25°C</p> <p>Testing Circuitry Figure A</p>																																																											
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Model		ADA750F (ADA750F-30)		Testing Circuitry Figure A																																																		
Item		Overvoltage Protection 過電圧保護																																																				
Object		V1:+30V24.5A																																																				
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Operating Point [V] 	<table border="1"> <thead> <tr> <th rowspan="2">Ambient Temperature [°C]</th> <th colspan="3">Operating Point [V]</th> </tr> <tr> <th>Input Volt. 170[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 264[V]</th> </tr> </thead> <tbody> <tr><td>-20</td><td>43.32</td><td>43.31</td><td>43.31</td></tr> <tr><td>-10</td><td>43.72</td><td>43.72</td><td>43.72</td></tr> <tr><td>0</td><td>44.00</td><td>44.00</td><td>44.01</td></tr> <tr><td>10</td><td>44.43</td><td>44.43</td><td>44.42</td></tr> <tr><td>20</td><td>44.72</td><td>44.72</td><td>44.72</td></tr> <tr><td>25</td><td>44.90</td><td>44.90</td><td>44.90</td></tr> <tr><td>30</td><td>45.20</td><td>45.20</td><td>45.20</td></tr> <tr><td>40</td><td>45.55</td><td>45.55</td><td>45.55</td></tr> <tr><td>50</td><td>45.90</td><td>45.90</td><td>45.90</td></tr> <tr><td>60</td><td>46.19</td><td>46.19</td><td>46.19</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>			Ambient Temperature [°C]	Operating Point [V]			Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]	-20	43.32	43.31	43.31	-10	43.72	43.72	43.72	0	44.00	44.00	44.01	10	44.43	44.43	44.42	20	44.72	44.72	44.72	25	44.90	44.90	44.90	30	45.20	45.20	45.20	40	45.55	45.55	45.55	50	45.90	45.90	45.90	60	46.19	46.19	46.19	--	--	--	--
	Ambient Temperature [°C]	Operating Point [V]																																																				
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	20	44.72	44.72	44.72																																																		
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	30	45.20	45.20	45.20																																																		
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Ambient Temperature [°C] Load 0%																																																						
Note: Slanted line shows the range of the rated ambient temperature.																																																						
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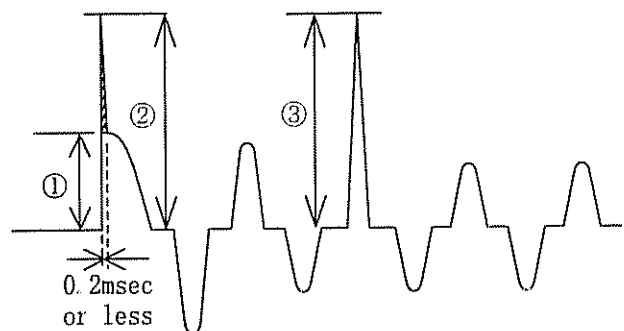
Model	ADA750F (ADA750F-30)	Temperature	25°C
Item	Inrush Current 突入電流	Testing Circuitry	Figure A
Object	_____		



Input Voltage 200 V
 Frequency 60 Hz
 Load 100 %

Inrush Current

- ① 24.0 [A]
- ② 32.9 [A] (0.2msec or less)*1
- ③ 22.5 [A]



*1 The specification of the inrush current (primary surge) means that the surge current to a built-in noise filter (0.2msec or less : waveform ②) is excluded.

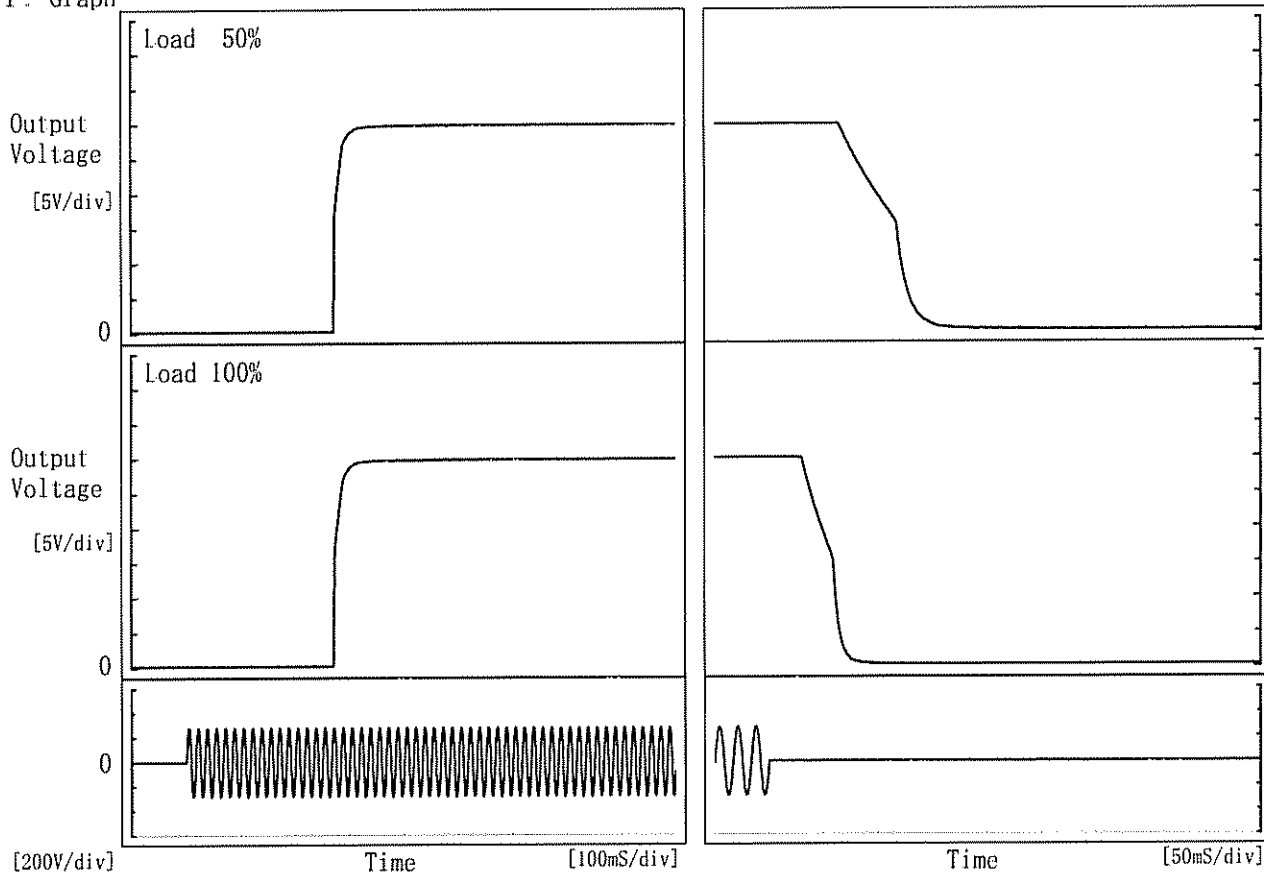
本製品の突入電流(1次サージ)の仕様は、内蔵ノイズフィルタ部へのサージ電流(0.2msec以下:波形②)を除きます。



Model	ADA750F (ADA750F-30)	Temperature	25°C
Item	Rise and Fall Time 立上り、立下り時間	Testing Circuitry	Figure A
Object	V1:+30V24.5A		

1. Graph

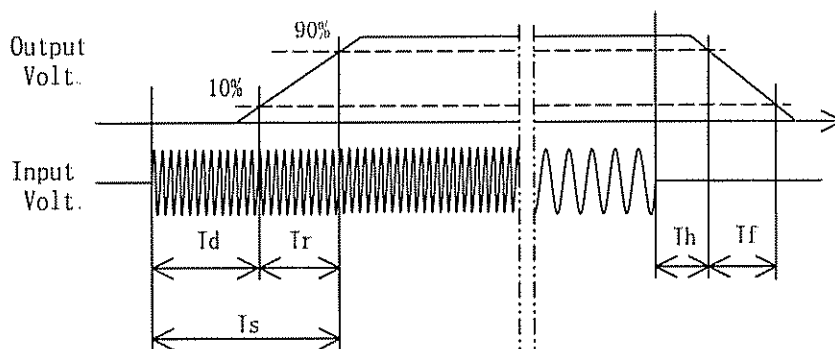
Input Volt. 200 V



2. Values

[mS]

Load \ Time	T d	T r	T s	T h	T f
50 %	271.0	18.5	289.5	72.0	62.5
100 %	271.0	18.5	289.5	34.5	33.5

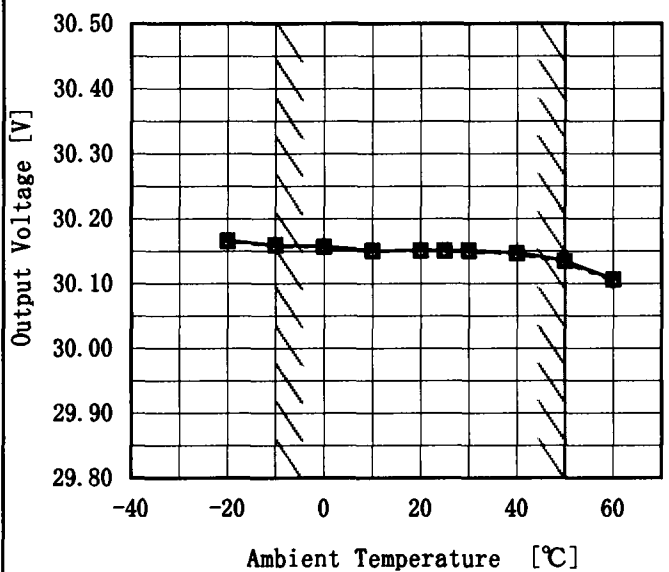




Model	ADA750F (ADA750F-30)
Item	Ambient Temperature Drift 周囲温度変動
Object	V1:+30V24.5A

Testing Circuitry Figure A

1. Graph
- △— Input Volt. 170 V
 - Input Volt. 200 V
 - Input Volt. 264 V



Load 100%

Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。

2. Values

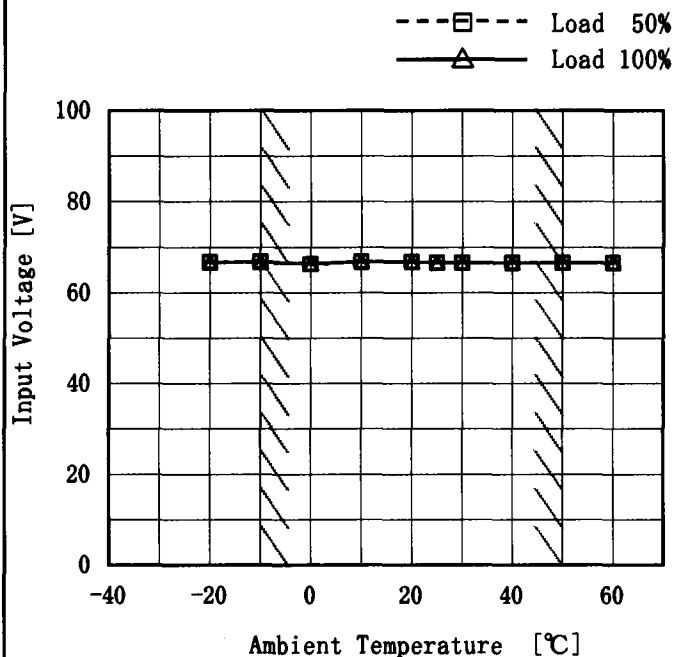
Ambient Temperature [°C]	Output Voltage [V]		
	Input Volt. 170[V]	Input Volt. 200[V]	Input Volt. 264[V]
-20	30.166	30.166	30.167
-10	30.159	30.159	30.159
0	30.157	30.157	30.157
10	30.151	30.151	30.150
20	30.151	30.151	30.151
25	30.151	30.151	30.150
30	30.151	30.151	30.150
40	30.148	30.146	30.146
50	30.136	30.135	30.134
60	30.107	30.106	30.104
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Model	ADA750F (ADA750F-30)
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	V1:+30V24.5A

Testing Circuitry Figure A

1. Graph



2. Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	67	67
-10	67	67
0	66	66
10	67	67
20	67	67
25	67	67
30	67	67
40	67	67
50	67	67
60	67	67
--	--	--

Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。



Model		ADA750F (ADA750F-30)		Testing Circuitry Figure A																										
Item		Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)																												
Object		V1:+30V24.5A																												
1. Graph		2. Values																												
<p style="text-align: center;">Ripple Voltage [mV]</p> <p style="text-align: center;">Ambient Temperature [°C]</p> <p style="text-align: center;">Input Volt. 200 V</p> <p style="text-align: center;">Load 100 %</p>		<table border="1"> <thead> <tr> <th>Ambient Temperature [°C]</th> <th>Ripple Voltage [mV]</th> </tr> </thead> <tbody> <tr><td>-10</td><td>50</td></tr> <tr><td>0</td><td>45</td></tr> <tr><td>25</td><td>45</td></tr> <tr><td>50</td><td>30</td></tr> <tr><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td></tr> </tbody> </table>			Ambient Temperature [°C]	Ripple Voltage [mV]	-10	50	0	45	25	45	50	30	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--
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<p>Note: Slanted line shows the range of the rated ambient temperature.</p> <p>(注) 斜線は定格周囲温度範囲を示す。</p>																														



Model		ADA750F (ADA750F-30)		Temperature		25°C																							
Item		Time Lapse Drift 経時ドリフト		Testing Circuitry		Figure A																							
Object		V1:+30V24.5A		2. Values <table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>30.159</td></tr> <tr><td>0.5</td><td>30.133</td></tr> <tr><td>1.0</td><td>30.133</td></tr> <tr><td>2.0</td><td>30.133</td></tr> <tr><td>3.0</td><td>30.133</td></tr> <tr><td>4.0</td><td>30.134</td></tr> <tr><td>5.0</td><td>30.134</td></tr> <tr><td>6.0</td><td>30.134</td></tr> <tr><td>7.0</td><td>30.134</td></tr> <tr><td>8.0</td><td>30.134</td></tr> </tbody> </table>				Time since start [H]	Output Voltage [V]	0.0	30.159	0.5	30.133	1.0	30.133	2.0	30.133	3.0	30.133	4.0	30.134	5.0	30.134	6.0	30.134	7.0	30.134	8.0	30.134
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1. Graph <p>Input Volt. 200V Load 100%</p>																													

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Model		ADA750F (ADA750F-30)	Testing Circuitry Figure A
Item		Output Voltage Accuracy 定電圧精度	
Object		V1:+30V24.5A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10 ~ 50°C

Input Voltage : 170 ~ 264V

Load Current : 0 ~ 24.5A

* Output Voltage Accuracy = \pm (Maximum of Output Voltage - Minimum of Output Voltage) / 2

* Output Voltage Accuracy (Ration) = $\frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$

1. 定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 : -10 ~ 50°C

入力電圧 : 170 ~ 264V

負荷電流 : 0 ~ 24.5A

* 定電圧精度(変動値) = \pm (出力電圧の最高値 - 出力電圧の最低値) / 2

* 定電圧精度(変動率) = $\frac{\text{変動値}}{\text{定格出力電圧}} \times 100$

2. Values

Item	Temperature [°C]	Input Voltage [V]	Output		Output Voltage Accuracy	
			Current [A]	Voltage [V]	Value [mV]	Ration [%]
Maximum Voltage	-10	85	0	30.185	±41	±0.1
Minimum Voltage	50	264	24.5	30.104		



Model		ADA750F (ADA750F-30)	Temperature 25°C Testing Circuitry Figure B
Item		Leakage Current 漏洩電流	
Object		_____	

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DEN-AN	—	—	—
(B) IEC60950	—	—	—

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 240 [V]	Input Volt. 264 [V]
(B) IEC60950	0.39	0.56	0.61

2. Condition

Leakage current value is concluded after measuring each phases of AC input and by choosing the larger one.

交流入力各相について測定し、その大きい方を漏洩電流測定値とする。

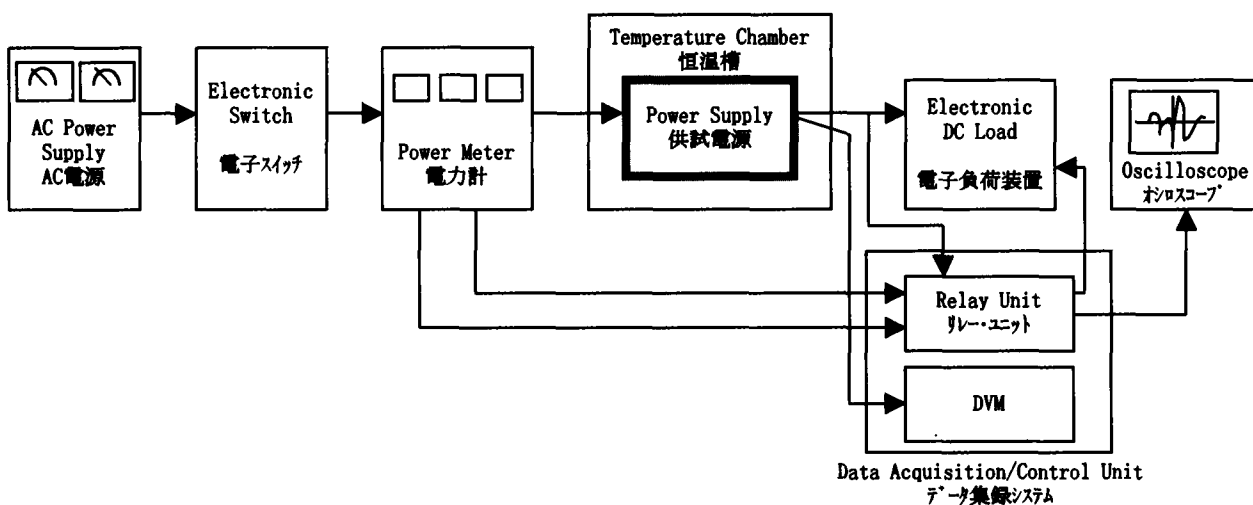


Figure A

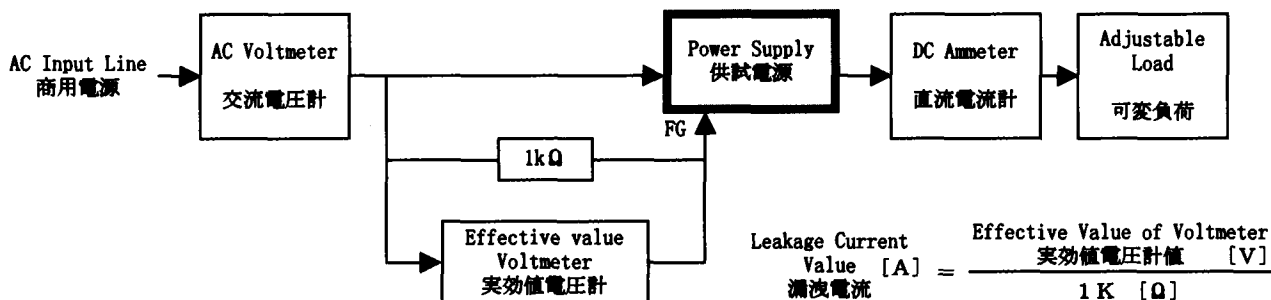


Figure B (DEN-AN)

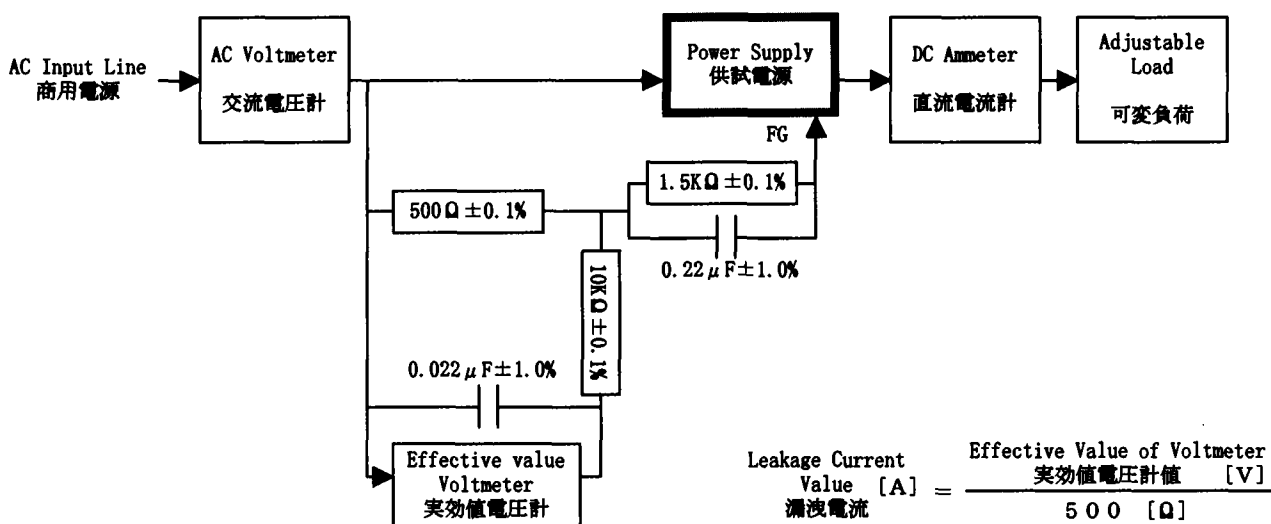


Figure B (IEC60950)