

**TEST DATA OF ADA750F****ADA750F-30  
(100V INPUT)****Regulated DC power supply  
Jan. 27, 2003**

Approved by : Kuniaki Nagahara  
Kuniaki Nagahara Design Manager

Prepared by : Katsumi Ishikawa  
Katsumi Ishikawa Design Engineer

**INPUT : AC 85~132V****OUTPUT : V1: 30V 20A****コーセル株式会社  
COSEL CO.,LTD.**

CONTENTS

1. Line Regulation . . . . .	1
静的入力変動	
2. Input Current (by Load Power) . . . . .	2
入力電流 (負荷電力特性)	
3. Input Power (by Load Power) . . . . .	3
入力電力 (負荷電力特性)	
4. Efficiency (by Input Voltage) . . . . .	4
効率 (入力電圧特性)	
5. Efficiency (by Load Power) . . . . .	5
効率 (負荷電力特性)	
6. Power Factor (by Input Voltage) . . . . .	6
力率 (入力電圧特性)	
7. Power Factor (by Load Power) . . . . .	7
力率 (負荷電力特性)	
8. Hold-Up Time (by Load Power) . . . . .	8
出力保持時間 (負荷電力特性)	
9. Instantaneous Interruption Compensation (by Load Power) . . . . .	9
瞬時停電保障 (負荷電力特性)	
10. Load Regulation . . . . .	10
静的負荷変動	
11. Ripple Voltage (by Load Current) . . . . .	11
リップル電圧 (負荷電流特性)	
12. Ripple-Noise . . . . .	12
リップルノイズ	
13. Overcurrent Protection . . . . .	13
過電流保護	
14. Overvoltage Protection . . . . .	14
過電圧保護	
15. Inrush Current . . . . .	15
突入電流	
16. Rise and Fall Time . . . . .	16
立上り、立下り時間	
17. Ambient Temperature Drift . . . . .	17
周囲温度変動	
18. Minimum Input Voltage for Regulated Output Voltage . . . . .	18
最低レギュレーション電圧	
19. Ripple Voltage (by Ambient Temperature) . . . . .	19
リップル電圧 (周囲温度特性)	
20. Time Lapse Drift . . . . .	20
経時ドリフト	
21. Output Voltage Accuracy . . . . .	21
定電圧精度	
22. Leakage Current . . . . .	22
漏洩電流	
23. Figure of Testing Circuitry . . . . .	23
測定回路図	



<p>Model ADA750F (ADA750F-30)</p>		<p>Temperature 25°C</p>																																	
<p>Item Line Regulation 静的入力変動</p>		<p>Testing Circuitry Figure A</p>																																	
<p>Object V1:+30V20A</p>																																			
<p>1. Graph</p> <p>---□--- Load 50% —△— Load 100%</p> <p>Note: Slanted line shows the range of the rated input voltage. (注) 斜線は定格入力電圧範囲を示す。</p>		<p>2. Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Input Voltage [V]</th> <th colspan="2">Output Voltage [V]</th> </tr> <tr> <th>Load 50%</th> <th>Load 100%</th> </tr> </thead> <tbody> <tr><td>75</td><td>30.150</td><td>30.139</td></tr> <tr><td>80</td><td>30.149</td><td>30.139</td></tr> <tr><td>85</td><td>30.149</td><td>30.139</td></tr> <tr><td>90</td><td>30.148</td><td>30.139</td></tr> <tr><td>100</td><td>30.148</td><td>30.139</td></tr> <tr><td>110</td><td>30.148</td><td>30.139</td></tr> <tr><td>120</td><td>30.148</td><td>30.138</td></tr> <tr><td>132</td><td>30.147</td><td>30.138</td></tr> <tr><td>140</td><td>30.146</td><td>30.138</td></tr> </tbody> </table>		Input Voltage [V]	Output Voltage [V]		Load 50%	Load 100%	75	30.150	30.139	80	30.149	30.139	85	30.149	30.139	90	30.148	30.139	100	30.148	30.139	110	30.148	30.139	120	30.148	30.138	132	30.147	30.138	140	30.146	30.138
Input Voltage [V]	Output Voltage [V]																																		
	Load 50%	Load 100%																																	
75	30.150	30.139																																	
80	30.149	30.139																																	
85	30.149	30.139																																	
90	30.148	30.139																																	
100	30.148	30.139																																	
110	30.148	30.139																																	
120	30.148	30.138																																	
132	30.147	30.138																																	
140	30.146	30.138																																	



Model		ADA750F (ADA750F-30)		Temperature		25°C																																																				
Item		Input Current (by Load Current) 入力電流 (負荷電力特性)		Testing Circuitry		Figure A																																																				
Object																																																										
1. Graph		—△— Input Volt. 85 V ---□--- Input Volt. 100 V -○- Input Volt. 132 V		2. Values																																																						
		<table border="1"> <thead> <tr> <th rowspan="2">Load Power [W]</th> <th colspan="3">Input Current [A]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0</td><td>0.300</td><td>0.280</td><td>0.240</td></tr> <tr><td>120</td><td>1.950</td><td>1.670</td><td>1.300</td></tr> <tr><td>240</td><td>3.470</td><td>2.960</td><td>2.250</td></tr> <tr><td>360</td><td>5.040</td><td>4.260</td><td>3.250</td></tr> <tr><td>480</td><td>6.570</td><td>5.610</td><td>4.240</td></tr> <tr><td>600</td><td>8.240</td><td>6.930</td><td>5.240</td></tr> <tr><td>660</td><td>9.080</td><td>7.610</td><td>5.740</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>						Load Power [W]	Input Current [A]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0	0.300	0.280	0.240	120	1.950	1.670	1.300	240	3.470	2.960	2.250	360	5.040	4.260	3.250	480	6.570	5.610	4.240	600	8.240	6.930	5.240	660	9.080	7.610	5.740	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--
Load Power [W]	Input Current [A]																																																									
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]																																																							
0	0.300	0.280	0.240																																																							
120	1.950	1.670	1.300																																																							
240	3.470	2.960	2.250																																																							
360	5.040	4.260	3.250																																																							
480	6.570	5.610	4.240																																																							
600	8.240	6.930	5.240																																																							
660	9.080	7.610	5.740																																																							
--	--	--	--																																																							
--	--	--	--																																																							
--	--	--	--																																																							
--	--	--	--																																																							
Note: Slanted line shows the range of the rated load power. (注) 斜線は定格電力範囲を示す。																																																										



<b>Model</b>	ADA750F (ADA750F-30)																																																				
<b>Item</b>	Input Power (by Load Power) 入力電力 (負荷電力特性)	Temperature 25°C Testing Circuitry Figure A																																																			
<b>Object</b>	_____																																																				
<p>1. Graph</p> <p> <span style="margin-right: 20px;">—△—</span> Input Volt. 85 V  <span style="margin-right: 20px;">---□---</span> Input Volt. 100 V  <span style="margin-right: 20px;">---○---</span> Input Volt. 132 V                 </p>		<p>2. Values</p> <table border="1" style="width:100%; border-collapse: collapse; text-align: center;"> <thead> <tr> <th rowspan="2">Load Power [W]</th> <th colspan="3">Input Power [W]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0</td><td>21.0</td><td>23.0</td><td>21.0</td></tr> <tr><td>120</td><td>158.0</td><td>158.0</td><td>159.0</td></tr> <tr><td>240</td><td>288.0</td><td>288.0</td><td>285.0</td></tr> <tr><td>360</td><td>425.0</td><td>420.0</td><td>417.0</td></tr> <tr><td>480</td><td>557.0</td><td>557.0</td><td>551.0</td></tr> <tr><td>600</td><td>701.0</td><td>689.0</td><td>683.0</td></tr> <tr><td>660</td><td>773.0</td><td>759.0</td><td>752.0</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>	Load Power [W]	Input Power [W]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0	21.0	23.0	21.0	120	158.0	158.0	159.0	240	288.0	288.0	285.0	360	425.0	420.0	417.0	480	557.0	557.0	551.0	600	701.0	689.0	683.0	660	773.0	759.0	752.0	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--
Load Power [W]	Input Power [W]																																																				
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]																																																		
0	21.0	23.0	21.0																																																		
120	158.0	158.0	159.0																																																		
240	288.0	288.0	285.0																																																		
360	425.0	420.0	417.0																																																		
480	557.0	557.0	551.0																																																		
600	701.0	689.0	683.0																																																		
660	773.0	759.0	752.0																																																		
--	--	--	--																																																		
--	--	--	--																																																		
--	--	--	--																																																		
--	--	--	--																																																		
<p>Note: Slanted line shows the range of the rated load power.</p> <p>(注) 斜線は定格電力範囲を示す。</p>																																																					



Model		ADA750F (ADA750F-30)		Temperature		25°C																																	
Item		Efficiency (by Input Voltage) 効率 (入力電圧特性)		Testing Circuitry		Figure A																																	
Object																																							
1. Graph				2. Values																																			
<p>---□--- Load 50% —△— Load 100%</p>				<table border="1"> <thead> <tr> <th rowspan="2">Input Voltage [V]</th> <th colspan="2">Efficiency [%]</th> </tr> <tr> <th>Load 50%</th> <th>Load 100%</th> </tr> </thead> <tbody> <tr><td>75</td><td>83.9</td><td>84.5</td></tr> <tr><td>80</td><td>84.1</td><td>85.1</td></tr> <tr><td>85</td><td>84.2</td><td>85.6</td></tr> <tr><td>90</td><td>84.9</td><td>86.2</td></tr> <tr><td>100</td><td>84.8</td><td>86.9</td></tr> <tr><td>110</td><td>84.8</td><td>86.9</td></tr> <tr><td>120</td><td>84.8</td><td>87.4</td></tr> <tr><td>132</td><td>84.6</td><td>87.7</td></tr> <tr><td>140</td><td>84.8</td><td>87.8</td></tr> </tbody> </table>				Input Voltage [V]	Efficiency [%]		Load 50%	Load 100%	75	83.9	84.5	80	84.1	85.1	85	84.2	85.6	90	84.9	86.2	100	84.8	86.9	110	84.8	86.9	120	84.8	87.4	132	84.6	87.7	140	84.8	87.8
Input Voltage [V]	Efficiency [%]																																						
	Load 50%	Load 100%																																					
75	83.9	84.5																																					
80	84.1	85.1																																					
85	84.2	85.6																																					
90	84.9	86.2																																					
100	84.8	86.9																																					
110	84.8	86.9																																					
120	84.8	87.4																																					
132	84.6	87.7																																					
140	84.8	87.8																																					
<p>Note: Slanted line shows the range of the rated input voltage.</p> <p>(注) 斜線は定格入力電圧範囲を示す。</p>																																							



Model		ADA750F (ADA750F-30)		Temperature		25°C																																																				
Item		Efficiency (by Load Power) 効率 (負荷電力特性)		Testing Circuitry		Figure A																																																				
Object																																																										
1. Graph		—△— Input Volt. 85 V ---□--- Input Volt. 100 V -·-○-·- Input Volt. 132 V		2. Values																																																						
		<table border="1"> <thead> <tr> <th rowspan="2">Load Power [W]</th> <th colspan="3">Efficiency [%]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>120</td><td>75.4</td><td>75.5</td><td>74.9</td></tr> <tr><td>240</td><td>83.1</td><td>83.2</td><td>84.0</td></tr> <tr><td>360</td><td>84.6</td><td>85.6</td><td>86.2</td></tr> <tr><td>480</td><td>86.1</td><td>86.2</td><td>87.1</td></tr> <tr><td>600</td><td>85.6</td><td>87.1</td><td>87.9</td></tr> <tr><td>660</td><td>85.4</td><td>87.0</td><td>87.8</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>						Load Power [W]	Efficiency [%]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0	—	—	—	120	75.4	75.5	74.9	240	83.1	83.2	84.0	360	84.6	85.6	86.2	480	86.1	86.2	87.1	600	85.6	87.1	87.9	660	85.4	87.0	87.8	--	—	—	—	--	—	—	—	--	—	—	—	--	—	—	—
Load Power [W]	Efficiency [%]																																																									
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]																																																							
0	—	—	—																																																							
120	75.4	75.5	74.9																																																							
240	83.1	83.2	84.0																																																							
360	84.6	85.6	86.2																																																							
480	86.1	86.2	87.1																																																							
600	85.6	87.1	87.9																																																							
660	85.4	87.0	87.8																																																							
--	—	—	—																																																							
--	—	—	—																																																							
--	—	—	—																																																							
--	—	—	—																																																							
Note: Slanted line shows the range of the rated load power. (注) 斜線は定格電力範囲を示す。																																																										



Model		ADA750F (ADA750F-30)	Temperature		25°C																																
Item		Power Factor (by Input Voltage) 力率 (入力電圧特性)	Testing Circuitry		Figure A																																
Object																																					
1. Graph			2. Values																																		
<p>---□--- Load 50% —△— Load 100%</p> <p>Power Factor</p> <p>Input Voltage [V]</p>			<table border="1"> <thead> <tr> <th rowspan="2">Input Voltage [V]</th> <th colspan="2">Power Factor</th> </tr> <tr> <th>Load 50%</th> <th>Load 100%</th> </tr> </thead> <tbody> <tr><td>75</td><td>0.986</td><td>0.999</td></tr> <tr><td>80</td><td>0.992</td><td>0.999</td></tr> <tr><td>85</td><td>0.986</td><td>0.999</td></tr> <tr><td>90</td><td>0.981</td><td>0.999</td></tr> <tr><td>100</td><td>0.983</td><td>0.996</td></tr> <tr><td>110</td><td>0.978</td><td>0.993</td></tr> <tr><td>120</td><td>0.975</td><td>0.993</td></tr> <tr><td>132</td><td>0.970</td><td>0.988</td></tr> <tr><td>140</td><td>0.970</td><td>0.986</td></tr> </tbody> </table>			Input Voltage [V]	Power Factor		Load 50%	Load 100%	75	0.986	0.999	80	0.992	0.999	85	0.986	0.999	90	0.981	0.999	100	0.983	0.996	110	0.978	0.993	120	0.975	0.993	132	0.970	0.988	140	0.970	0.986
Input Voltage [V]	Power Factor																																				
	Load 50%	Load 100%																																			
75	0.986	0.999																																			
80	0.992	0.999																																			
85	0.986	0.999																																			
90	0.981	0.999																																			
100	0.983	0.996																																			
110	0.978	0.993																																			
120	0.975	0.993																																			
132	0.970	0.988																																			
140	0.970	0.986																																			
<p>Note: Slanted line shows the range of the rated input voltage.</p> <p>(注) 斜線は定格入力電圧範囲を示す。</p>																																					





Model		ADA750F (ADA750F-30)		Temperature		25°C																																																				
Item		Power Factor (by Load Power) 力率 (負荷電力特性)		Testing Circuitry		Figure A																																																				
Object		_____																																																								
1. Graph				2. Values																																																						
<p> <span style="border-bottom: 1px solid black; display: inline-block; width: 1em; margin-right: 0.5em;"></span> <math>\triangle</math> — Input Volt. 85 V  <span style="border-bottom: 1px dashed black; display: inline-block; width: 1em; margin-right: 0.5em;"></span> <math>\square</math> --- Input Volt. 100 V  <span style="border-bottom: 1px dash-dot black; display: inline-block; width: 1em; margin-right: 0.5em;"></span> <math>\circ</math> - - - Input Volt. 132 V                 </p> <p style="text-align: center;">Load Power [W]</p>				<table border="1"> <thead> <tr> <th rowspan="2">Load Power [W]</th> <th colspan="3">Power Factor</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0</td><td>0.808</td><td>0.821</td><td>0.656</td></tr> <tr><td>120</td><td>0.952</td><td>0.946</td><td>0.924</td></tr> <tr><td>240</td><td>0.976</td><td>0.973</td><td>0.960</td></tr> <tr><td>360</td><td>0.993</td><td>0.986</td><td>0.972</td></tr> <tr><td>480</td><td>0.998</td><td>0.993</td><td>0.984</td></tr> <tr><td>600</td><td>0.999</td><td>0.996</td><td>0.987</td></tr> <tr><td>660</td><td>0.999</td><td>0.999</td><td>0.992</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>				Load Power [W]	Power Factor			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0	0.808	0.821	0.656	120	0.952	0.946	0.924	240	0.976	0.973	0.960	360	0.993	0.986	0.972	480	0.998	0.993	0.984	600	0.999	0.996	0.987	660	0.999	0.999	0.992	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--
Load Power [W]	Power Factor																																																									
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]																																																							
0	0.808	0.821	0.656																																																							
120	0.952	0.946	0.924																																																							
240	0.976	0.973	0.960																																																							
360	0.993	0.986	0.972																																																							
480	0.998	0.993	0.984																																																							
600	0.999	0.996	0.987																																																							
660	0.999	0.999	0.992																																																							
--	--	--	--																																																							
--	--	--	--																																																							
--	--	--	--																																																							
--	--	--	--																																																							
<p>Note: Slanted line shows the range of the rated load power.</p> <p>(注) 斜線は定格電力範囲を示す。</p>																																																										



Model		ADA750F (ADA750F-30)		Temperature		25°C																																																				
Item		Hold-Up Time (by Load Power) 出力保持時間 (負荷電力特性)		Testing Circuitry		Figure A																																																				
Object		_____																																																								
1. Graph				2. Values																																																						
<p>                 —△— Input Volt. 85V                  ---□--- Input Volt. 100V                  -·○-·- Input Volt. 132V             </p> <p>Hold-Up Time [mS]</p> <p>Load Power [W]</p>				<table border="1"> <thead> <tr> <th rowspan="2">Load Power [W]</th> <th colspan="3">Hold-Up Time [mS]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>120</td><td>173</td><td>178</td><td>185</td></tr> <tr><td>240</td><td>85</td><td>89</td><td>95</td></tr> <tr><td>360</td><td>53</td><td>57</td><td>62</td></tr> <tr><td>480</td><td>38</td><td>40</td><td>45</td></tr> <tr><td>600</td><td>28</td><td>31</td><td>35</td></tr> <tr><td>660</td><td>24</td><td>27</td><td>31</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>				Load Power [W]	Hold-Up Time [mS]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0	—	—	—	120	173	178	185	240	85	89	95	360	53	57	62	480	38	40	45	600	28	31	35	660	24	27	31	--	—	—	—	--	—	—	—	--	—	—	—	--	—	—	—
Load Power [W]	Hold-Up Time [mS]																																																									
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]																																																							
0	—	—	—																																																							
120	173	178	185																																																							
240	85	89	95																																																							
360	53	57	62																																																							
480	38	40	45																																																							
600	28	31	35																																																							
660	24	27	31																																																							
--	—	—	—																																																							
--	—	—	—																																																							
--	—	—	—																																																							
--	—	—	—																																																							
<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy. Note: Slanted line shows the range of the rated load power.</p> <p>出力保持時間とは、入力電圧断から出力電圧が定電圧精度の範囲を保持しているところまでの時間。 (注) 斜線は定格電力範囲を示す。</p>																																																										



Model		ADA750F (ADA750F-30)		Temperature	25°C																																																			
Item		Instantaneous Interruption Compensation (by Load Power) 瞬時停電保障 (負荷電力特性)				Testing Circuitry	Figure A																																																	
Object		_____																																																						
1. Graph		—△— Input Volt. 85V ---□--- Input Volt. 100V -·-○-·- Input Volt. 132V		2. Values																																																				
Instantaneous Compensation Time [mS]			<table border="1"> <thead> <tr> <th rowspan="2">Load Power [W]</th> <th colspan="3">Time [mS]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>120</td><td>132</td><td>145</td><td>162</td></tr> <tr><td>240</td><td>40</td><td>52</td><td>69</td></tr> <tr><td>360</td><td>36</td><td>48</td><td>43</td></tr> <tr><td>480</td><td>24</td><td>34</td><td>41</td></tr> <tr><td>600</td><td>17</td><td>25</td><td>33</td></tr> <tr><td>660</td><td>14</td><td>20</td><td>28</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>--</td><td>—</td><td>—</td><td>—</td></tr> </tbody> </table>			Load Power [W]	Time [mS]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0	—	—	—	120	132	145	162	240	40	52	69	360	36	48	43	480	24	34	41	600	17	25	33	660	14	20	28	--	—	—	—	--	—	—	—	--	—	—	—	--	—	—	—
			Load Power [W]	Time [mS]																																																				
				Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]																																																		
			0	—	—	—																																																		
			120	132	145	162																																																		
			240	40	52	69																																																		
			360	36	48	43																																																		
			480	24	34	41																																																		
			600	17	25	33																																																		
			660	14	20	28																																																		
--	—	—	—																																																					
--	—	—	—																																																					
--	—	—	—																																																					
--	—	—	—																																																					
Note: Slanted line shows the range of the rated load power.																																																								
(注) 斜線は定格電力範囲を示す。																																																								



Model		ADA750F (ADA750F-30)		Temperature		25°C																																																				
Item		Load Regulation 静的負荷変動		Testing Circuitry		Figure A																																																				
Object		V1:+30V20A																																																								
1. Graph				2. Values																																																						
<p> <span style="border-bottom: 1px solid black; display: inline-block; width: 1em; margin-right: 0.5em;"></span>△— Input Volt. 85 V  <span style="border-bottom: 1px dashed black; display: inline-block; width: 1em; margin-right: 0.5em;"></span>□--- Input Volt. 100 V  <span style="border-bottom: 1px dash-dot black; display: inline-block; width: 1em; margin-right: 0.5em;"></span>○-·- Input Volt. 132 V                 </p> <p>Output Voltage [V]</p> <p>Load Current [A]</p>				<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Output Voltage [V]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0</td><td>30.176</td><td>30.171</td><td>30.170</td></tr> <tr><td>4</td><td>30.155</td><td>30.153</td><td>30.153</td></tr> <tr><td>8</td><td>30.153</td><td>30.152</td><td>30.151</td></tr> <tr><td>12</td><td>30.151</td><td>30.150</td><td>30.150</td></tr> <tr><td>16</td><td>30.149</td><td>30.148</td><td>30.148</td></tr> <tr><td>20</td><td>30.146</td><td>30.145</td><td>30.146</td></tr> <tr><td>22</td><td>30.145</td><td>30.144</td><td>30.144</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>				Load Current [A]	Output Voltage [V]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0	30.176	30.171	30.170	4	30.155	30.153	30.153	8	30.153	30.152	30.151	12	30.151	30.150	30.150	16	30.149	30.148	30.148	20	30.146	30.145	30.146	22	30.145	30.144	30.144	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--
Load Current [A]	Output Voltage [V]																																																									
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]																																																							
0	30.176	30.171	30.170																																																							
4	30.155	30.153	30.153																																																							
8	30.153	30.152	30.151																																																							
12	30.151	30.150	30.150																																																							
16	30.149	30.148	30.148																																																							
20	30.146	30.145	30.146																																																							
22	30.145	30.144	30.144																																																							
--	--	--	--																																																							
--	--	--	--																																																							
--	--	--	--																																																							
--	--	--	--																																																							
<p>Note: Slanted line shows the range of the rated load current.</p> <p>(注) 斜線は定格負荷電流範囲を示す。</p>																																																										



Model		ADA750F (ADA750F-30)	Temperature		25°C																																						
Item		Ripple Voltage (by Load Current) リップル電圧 (負荷特性)	Testing Circuitry		Figure A																																						
Object		V1:+30V20A																																									
1. Graph			2. Values																																								
<p> <span style="display: inline-block; width: 20px; border-bottom: 1px solid black; margin-right: 5px;"></span> <math>\triangle</math> Input Volt. 85 V  <span style="display: inline-block; width: 20px; border-bottom: 1px dashed black; margin-right: 5px;"></span> <math>\circ</math> Input Volt. 132 V                 </p> <p>Ripple Voltage is shown as p-p in the figure below.                  Note: Slanted line shows the range of the rated load current.</p> <p>リップル電圧は、下図 p-p 値で示される。                  (注) 斜線は定格負荷電流範囲を示す。</p> <p>                     T1: Due to AC Input Line                      入力商用周期                      T2: Due to Switching                      スイッチング周期                 </p> <p>Fig. Complex Ripple Wave Form                  図 リップル波形詳細図</p>			<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="2">Ripple Output Voltage [mV]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0</td><td>20</td><td>20</td></tr> <tr><td>4</td><td>25</td><td>25</td></tr> <tr><td>8</td><td>30</td><td>30</td></tr> <tr><td>12</td><td>40</td><td>40</td></tr> <tr><td>16</td><td>40</td><td>40</td></tr> <tr><td>20</td><td>40</td><td>40</td></tr> <tr><td>22</td><td>45</td><td>45</td></tr> <tr><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>			Load Current [A]	Ripple Output Voltage [mV]		Input Volt. 85[V]	Input Volt. 132[V]	0	20	20	4	25	25	8	30	30	12	40	40	16	40	40	20	40	40	22	45	45	--	--	--	--	--	--	--	--	--	--	--	--
Load Current [A]	Ripple Output Voltage [mV]																																										
	Input Volt. 85[V]	Input Volt. 132[V]																																									
0	20	20																																									
4	25	25																																									
8	30	30																																									
12	40	40																																									
16	40	40																																									
20	40	40																																									
22	45	45																																									
--	--	--																																									
--	--	--																																									
--	--	--																																									
--	--	--																																									



Model		ADA750F (ADA750F-30)		Temperature		25°C																																							
Item		Ripple-Noise リップルノイズ		Testing Circuitry		Figure A																																							
Object		V1:+30V20A																																											
1. Graph				2. Values																																									
<p> </p>				<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="2">Ripple-Noise [mV]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0</td><td>25</td><td>25</td></tr> <tr><td>4</td><td>30</td><td>30</td></tr> <tr><td>8</td><td>35</td><td>35</td></tr> <tr><td>12</td><td>45</td><td>45</td></tr> <tr><td>16</td><td>45</td><td>45</td></tr> <tr><td>20</td><td>50</td><td>50</td></tr> <tr><td>22</td><td>55</td><td>55</td></tr> <tr><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>				Load Current [A]	Ripple-Noise [mV]		Input Volt. 85[V]	Input Volt. 132[V]	0	25	25	4	30	30	8	35	35	12	45	45	16	45	45	20	50	50	22	55	55	--	--	--	--	--	--	--	--	--	--	--	--
Load Current [A]	Ripple-Noise [mV]																																												
	Input Volt. 85[V]	Input Volt. 132[V]																																											
0	25	25																																											
4	30	30																																											
8	35	35																																											
12	45	45																																											
16	45	45																																											
20	50	50																																											
22	55	55																																											
--	--	--																																											
--	--	--																																											
--	--	--																																											
--	--	--																																											
<p>Ripple-Noise is shown as p-p in the figure below. Note: Slanted line shows the range of the rated load current.</p> <p>リップルノイズは、下図 p-p 値で示される。 (注) 斜線は定格負荷電流範囲を示す。</p>																																													
<p>T1: Due to AC Input Line 入力商用周期</p> <p>T2: Due to Switching スイッチング周期</p>																																													
<p>Fig. Complex Ripple Wave Form 図 リップル波形詳細図</p>																																													

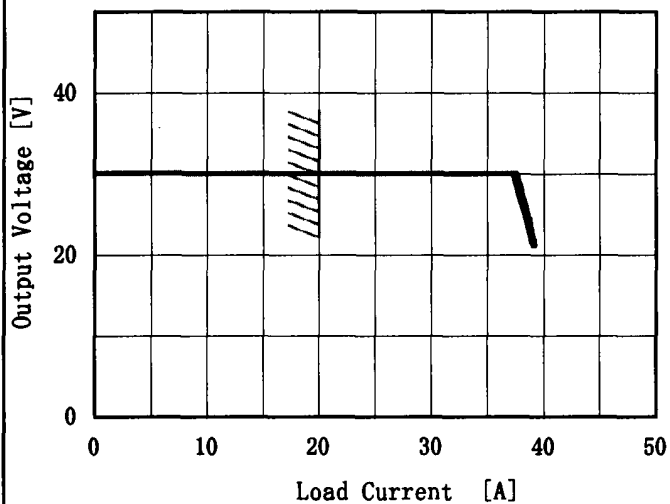
# COSEL

Model	ADA750F (ADA750F-30)
Item	Overcurrent Protection 過電流保護
Object	V1:+30V20A

Temperature 25°C  
Testing Circuitry Figure A

1. Graph

\_\_\_\_\_ Input Volt. 85 V  
 \_\_\_\_\_ Input Volt. 100 V  
 \_\_\_\_\_ Input Volt. 132 V



Note: Slanted line shows the range of the rated load current.

(注) 斜線は定格負荷電流範囲を示す。

Intermittent operation occurs when the output voltage is from 21V to 0V.

21V~0V間は、間欠モードとなる。

2. Values

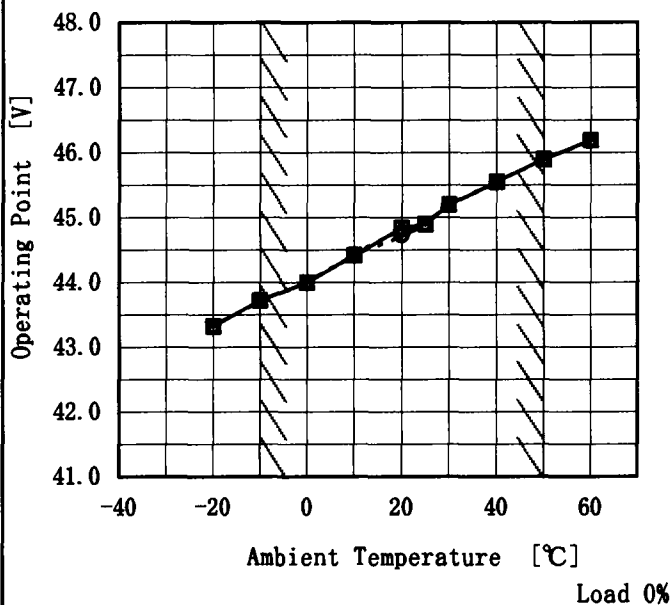
Output Voltage [V]	Load Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
30.0	37.20	37.39	37.62
28.5	37.45	37.64	37.87
27.0	37.76	37.98	38.21
24.0	38.40	38.48	38.71
21.0	38.90	38.96	39.19
--	--	--	--
--	--	--	--
--	--	--	--
--	--	--	--
--	--	--	--
--	--	--	--
--	--	--	--
--	--	--	--



Model	ADA750F (ADA750F-30)
Item	Overvoltage Protection 過電圧保護
Object	V1:+30V20A

Testing Circuitry Figure A

1. Graph
- △— Input Volt. 85 V
  - Input Volt. 100 V
  - Input Volt. 132 V



2. Values

Ambient Temperature [°C]	Operating Point [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
-20	43.32	43.32	43.32
-10	43.72	43.72	43.72
0	44.00	44.00	44.00
10	44.42	44.42	44.43
20	44.84	44.84	44.72
25	44.90	44.90	44.90
30	45.20	45.20	45.20
40	45.55	45.55	45.55
50	45.90	45.90	45.90
60	46.19	46.19	46.19
--	--	--	--

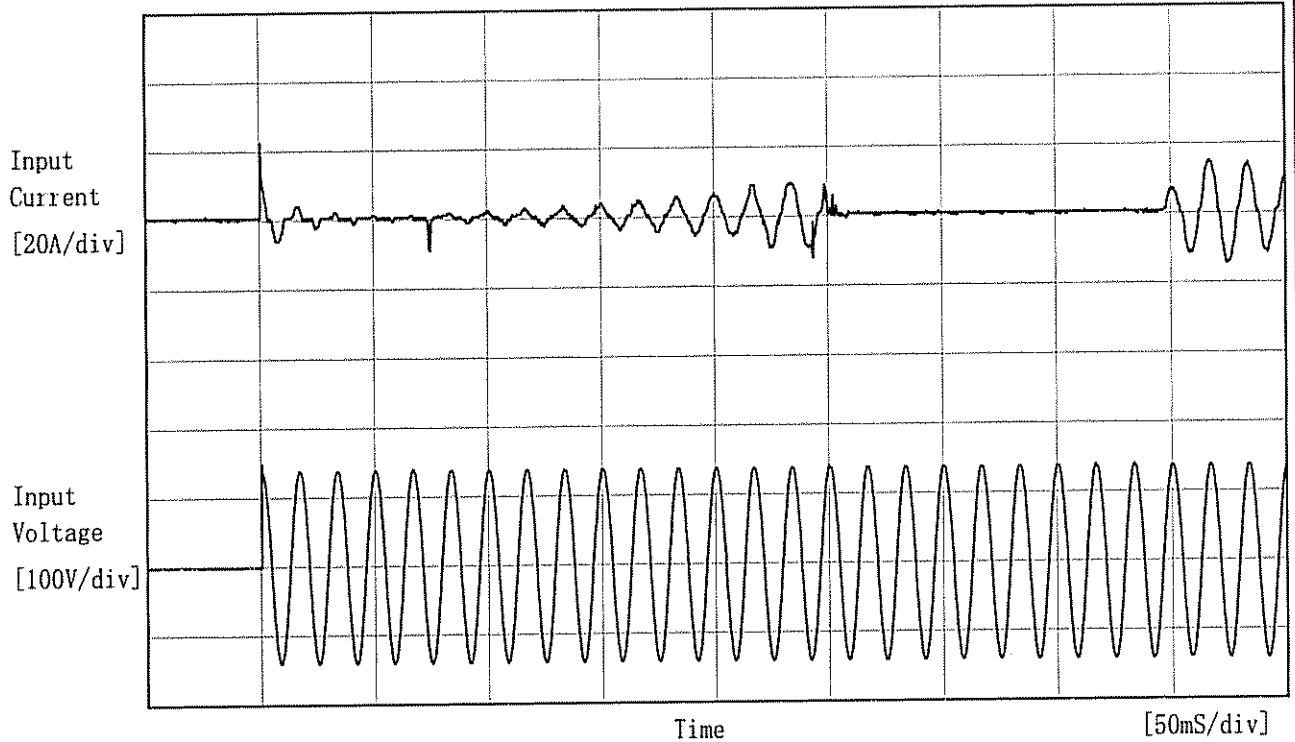
Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。

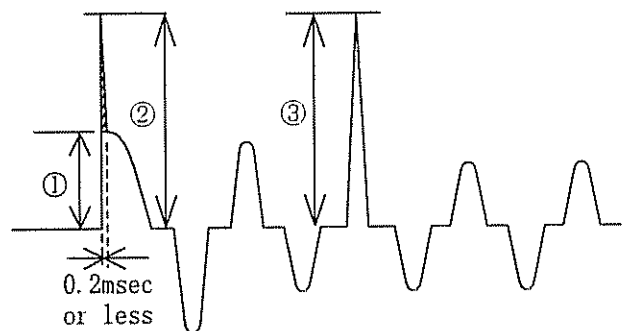


# COSEL

Model	ADA750F (ADA750F-30)	Temperature	25°C
Item	Inrush Current 突入電流	Testing Circuitry	Figure A
Object	_____		



- Input Voltage    100 V
- Frequency        60 Hz
- Load             100 %
- Inrush Current
- ①    13.8 [A]
- ②    22.3 [A] (0.2msec or less)\*1
- ③    8.4 [A]



\*1 The specification of the inrush current (primary surge) means that the surge current to a built-in noise filter (0.2msec or less : waveform ②) is excluded.

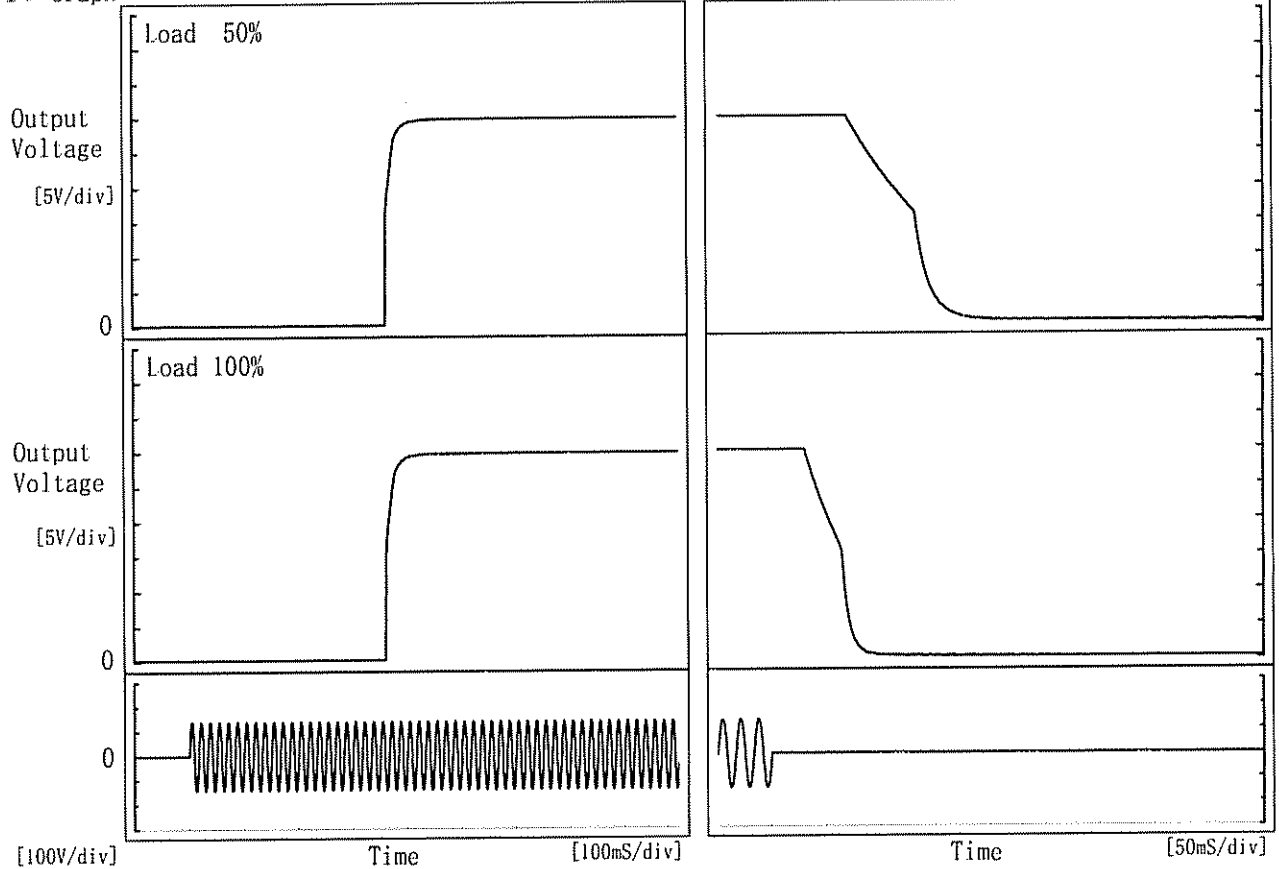
本製品の突入電流(1次サージ)の仕様は、内蔵ノイズフィルタ部へのサージ電流(0.2msec以下:波形②)を除きます。



Model	ADA750F (ADA750F-30)	Temperature	25°C
Item	Rise and Fall Time 立上り、立下り時間	Testing Circuitry	Figure A
Object	V1:+30V20A		

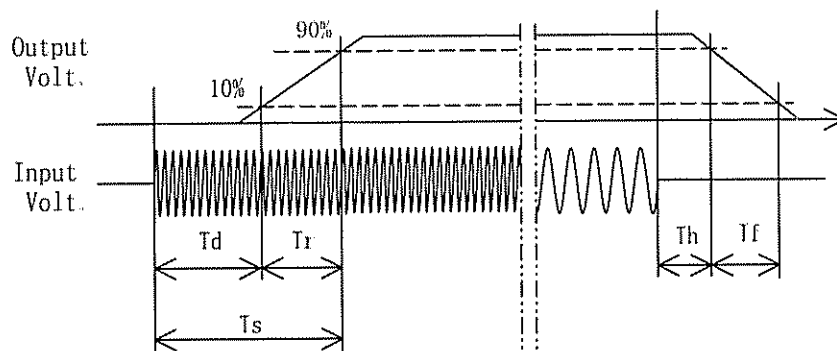
1. Graph

Input Volt. 100 V



2. Values

		[mS]				
Load	Time	T d	T r	T s	T h	T f
	50 %		361.5	18.5	380.0	79.0
100 %		358.0	22.0	380.0	35.8	40.0





Model		ADA750F (ADA750F-30)		Testing Circuitry Figure A																																																				
Item		Ambient Temperature Drift 周囲温度変動																																																						
Object		V1:+30V20A																																																						
1. Graph		—△— Input Volt. 85 V ---□--- Input Volt. 100 V -·○-·- Input Volt. 132 V		2. Values																																																				
		<table border="1"> <thead> <tr> <th rowspan="2">Ambient Temperature [°C]</th> <th colspan="3">Output Voltage [V]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>-20</td><td>30.173</td><td>30.173</td><td>30.173</td></tr> <tr><td>-10</td><td>30.165</td><td>30.164</td><td>30.164</td></tr> <tr><td>0</td><td>30.163</td><td>30.162</td><td>30.162</td></tr> <tr><td>10</td><td>30.155</td><td>30.155</td><td>30.156</td></tr> <tr><td>20</td><td>30.157</td><td>30.157</td><td>30.157</td></tr> <tr><td>25</td><td>30.156</td><td>30.156</td><td>30.156</td></tr> <tr><td>30</td><td>30.156</td><td>30.157</td><td>30.157</td></tr> <tr><td>40</td><td>30.156</td><td>30.155</td><td>30.154</td></tr> <tr><td>50</td><td>30.145</td><td>30.144</td><td>30.143</td></tr> <tr><td>60</td><td>30.117</td><td>30.115</td><td>30.115</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>				Ambient Temperature [°C]	Output Voltage [V]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	-20	30.173	30.173	30.173	-10	30.165	30.164	30.164	0	30.163	30.162	30.162	10	30.155	30.155	30.156	20	30.157	30.157	30.157	25	30.156	30.156	30.156	30	30.156	30.157	30.157	40	30.156	30.155	30.154	50	30.145	30.144	30.143	60	30.117	30.115	30.115	--	--	--	--
Ambient Temperature [°C]	Output Voltage [V]																																																							
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]																																																					
-20	30.173	30.173	30.173																																																					
-10	30.165	30.164	30.164																																																					
0	30.163	30.162	30.162																																																					
10	30.155	30.155	30.156																																																					
20	30.157	30.157	30.157																																																					
25	30.156	30.156	30.156																																																					
30	30.156	30.157	30.157																																																					
40	30.156	30.155	30.154																																																					
50	30.145	30.144	30.143																																																					
60	30.117	30.115	30.115																																																					
--	--	--	--																																																					
Note: Slanted line shows the range of the rated ambient temperature. (注) 斜線は定格周囲温度範囲を示す。																																																								

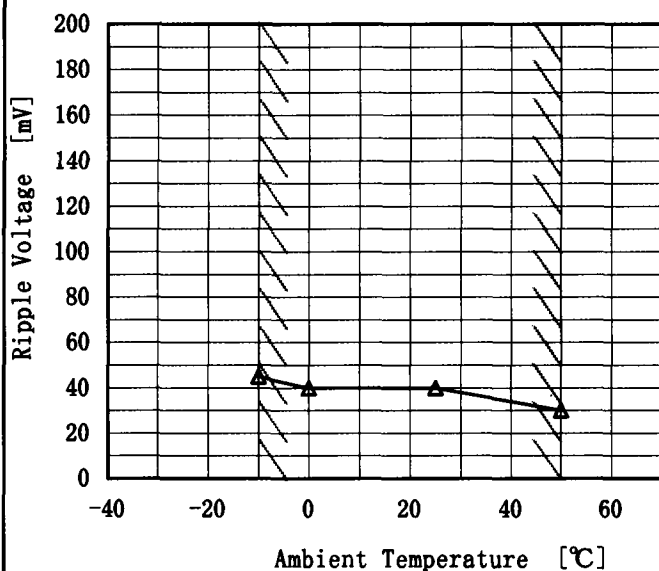
Model		ADA750F (ADA750F-30)																																							
Item		Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧																																							
Object		V1:+30V20A																																							
1. Graph		Testing Circuitry Figure A																																							
<p style="text-align: right;">             ---□--- Load 50%              —△— Load 100%         </p>		2. Values																																							
<p>Note: Slanted line shows the range of the rated ambient temperature.</p> <p>(注) 斜線は定格周囲温度範囲を示す。</p>		<table border="1"> <thead> <tr> <th rowspan="2">Ambient Temperature [°C]</th> <th colspan="2">Input Voltage [V]</th> </tr> <tr> <th>Load 50%</th> <th>Load 100%</th> </tr> </thead> <tbody> <tr><td>-20</td><td>67</td><td>67</td></tr> <tr><td>-10</td><td>67</td><td>66</td></tr> <tr><td>0</td><td>67</td><td>67</td></tr> <tr><td>10</td><td>67</td><td>67</td></tr> <tr><td>20</td><td>67</td><td>67</td></tr> <tr><td>25</td><td>67</td><td>67</td></tr> <tr><td>30</td><td>67</td><td>67</td></tr> <tr><td>40</td><td>67</td><td>67</td></tr> <tr><td>50</td><td>67</td><td>67</td></tr> <tr><td>60</td><td>67</td><td>67</td></tr> <tr><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>		Ambient Temperature [°C]	Input Voltage [V]		Load 50%	Load 100%	-20	67	67	-10	67	66	0	67	67	10	67	67	20	67	67	25	67	67	30	67	67	40	67	67	50	67	67	60	67	67	--	--	--
Ambient Temperature [°C]	Input Voltage [V]																																								
	Load 50%	Load 100%																																							
-20	67	67																																							
-10	67	66																																							
0	67	67																																							
10	67	67																																							
20	67	67																																							
25	67	67																																							
30	67	67																																							
40	67	67																																							
50	67	67																																							
60	67	67																																							
--	--	--																																							



Model	ADA750F (ADA750F-30)
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)
Object	V1:+30V20A

Testing Circuitry Figure A

1. Graph



Input Volt. 100 V  
Load 100 %

Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。

2. Values

Ambient Temperature [°C]	Ripple Voltage [mV]
-10	45
0	40
25	40
50	30
--	--
--	--
--	--
--	--
--	--
--	--
--	--



<b>COSEL</b>																									
Model	ADA750F (ADA750F-30)	Temperature	25°C																						
Item	Time Lapse Drift 経時ドリフト	Testing Circuitry	Figure A																						
Object	V1:+30V20A																								
1. Graph		2. Values																							
<p style="text-align: center;">Time [H]</p> <p>Input Volt. 100V Load 100%</p>		<table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>30.158</td></tr> <tr><td>0.5</td><td>30.134</td></tr> <tr><td>1.0</td><td>30.135</td></tr> <tr><td>2.0</td><td>30.136</td></tr> <tr><td>3.0</td><td>30.136</td></tr> <tr><td>4.0</td><td>30.137</td></tr> <tr><td>5.0</td><td>30.136</td></tr> <tr><td>6.0</td><td>30.137</td></tr> <tr><td>7.0</td><td>30.137</td></tr> <tr><td>8.0</td><td>30.136</td></tr> </tbody> </table>		Time since start [H]	Output Voltage [V]	0.0	30.158	0.5	30.134	1.0	30.135	2.0	30.136	3.0	30.136	4.0	30.137	5.0	30.136	6.0	30.137	7.0	30.137	8.0	30.136
Time since start [H]	Output Voltage [V]																								
0.0	30.158																								
0.5	30.134																								
1.0	30.135																								
2.0	30.136																								
3.0	30.136																								
4.0	30.137																								
5.0	30.136																								
6.0	30.137																								
7.0	30.137																								
8.0	30.136																								

# COSEL

Model	ADA750F (ADA750F-30)	Testing Circuitry Figure A
Item	Output Voltage Accuracy 定電圧精度	
Object	V1:+30V20A	

## 1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10 ~ 50°C

Input Voltage : 85 ~ 132V

Load Current : 0 ~ 20A

\* Output Voltage Accuracy =  $\pm (\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

\* Output Voltage Accuracy (Ration) =  $\frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$

## 1. 定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 : -10 ~ 50°C

入力電圧 : 85 ~ 132V

負荷電流 : 0 ~ 20A

\* 定電圧精度(変動値) =  $\pm (\text{出力電圧の最高値} - \text{出力電圧の最低値}) / 2$

\* 定電圧精度(変動率) =  $\frac{\text{変動値}}{\text{定格出力電圧}} \times 100$

## 2. Values

Item	Temperature [°C]	Input Voltage [V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ration [%]
Maximum Voltage	-10	85	0	30.195	±26	±0.1
Minimum Voltage	50	132	20	30.143		

# COSEL

Model	ADA750F (ADA750F-30)	Temperature	25°C
Item	Leakage Current 漏洩電流	Testing Circuitry	Figure B
Object	_____		

## 1. Results

Standards	Leakage Current [mA]		
	Input Volt.	Input Volt.	Input Volt.
	85 [V]	100 [V]	132 [V]
(A) DEN-AN	0.19	0.22	0.28
(B) IEC60950	0.19	0.22	0.28

Standards	Leakage Current [mA]		
	Input Volt.	Input Volt.	Input Volt.
	170 [V]	240 [V]	264 [V]
(B) IEC60950	—	—	—

## 2. Condition

Leakage current value is concluded after measuring each phases of AC input and by choosing the larger one.

交流入力各相について測定し、その大きい方を漏洩電流測定値とする。



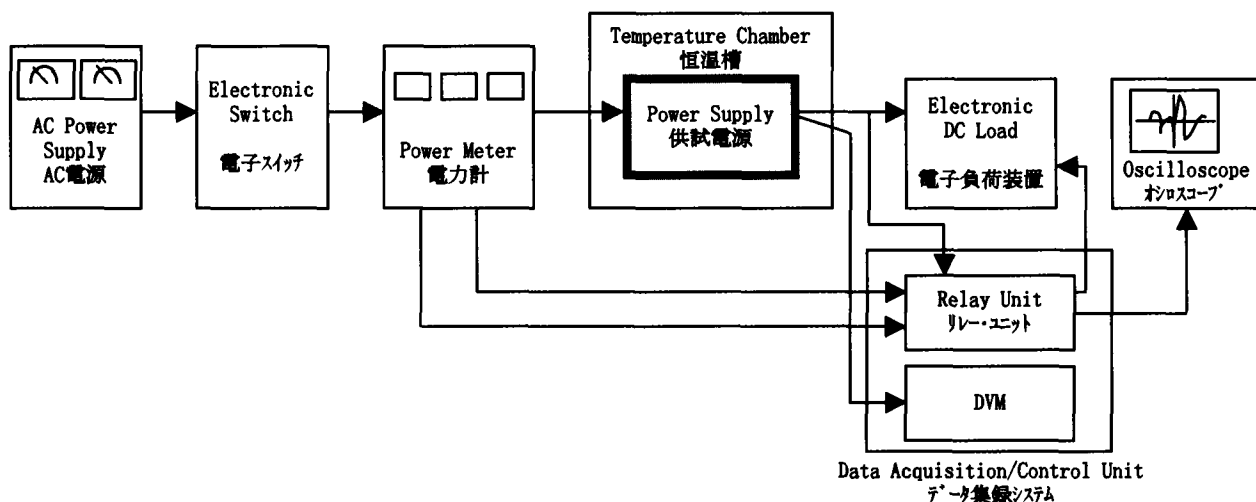


Figure A

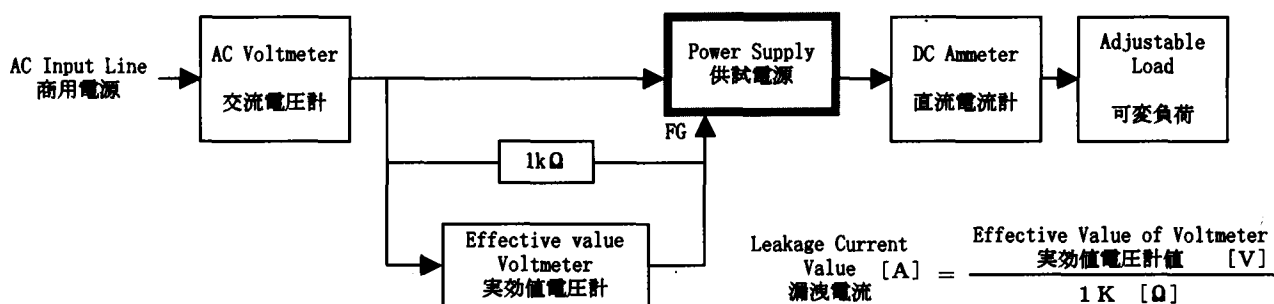


Figure B ( DEN-AN )

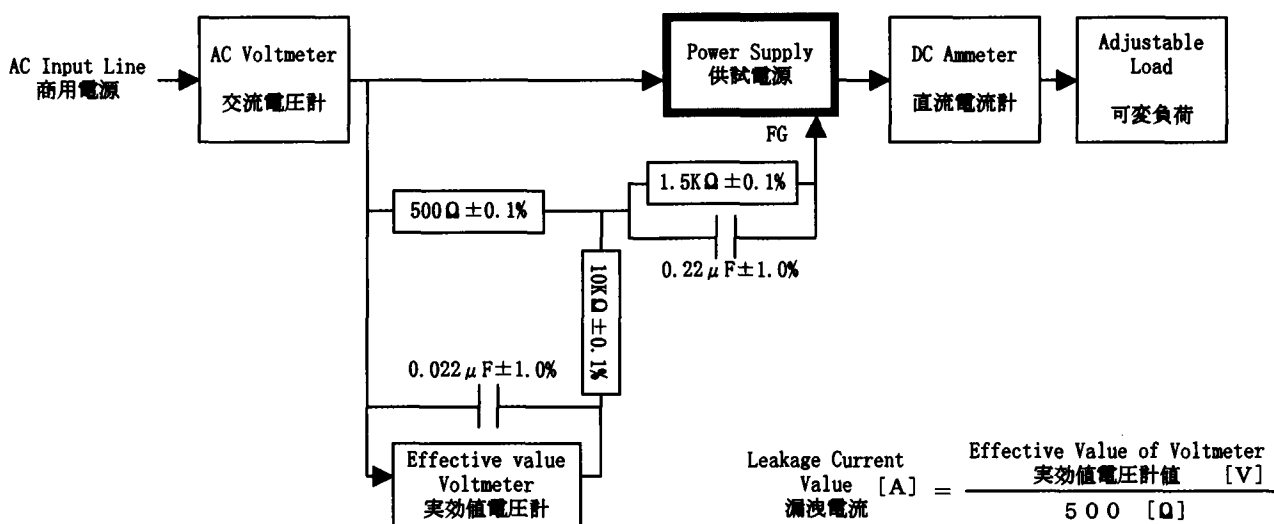


Figure B ( IEC60950 )