

TEST DATA OF ADA750FADA750F-24
(100V INPUT)Regulated DC power supply
Jan. 21, 2003Approved by : Kuniaki Nagahara
Kuniaki Nagahara Design ManagerPrepared by : Katsumi Ishikawa
Katsumi Ishikawa Design Engineer

INPUT : AC 85~132V

OUTPUT : V1: 24V 25A

コーセル株式会社
COSEL CO.,LTD.

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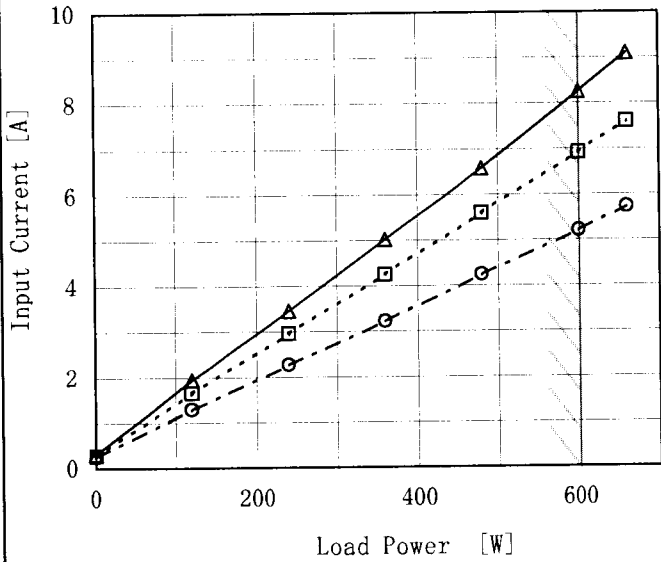


Model		ADA750F (ADA750F-24)		Temperature		25°C																																	
Item		Line Regulation 静的入力変動		Testing Circuitry		Figure A																																	
Object		V1:+24V25A																																					
1. Graph				2. Values																																			
<p>---□--- Load 50%</p> <p>—△— Load 100%</p> <p>Output Voltage [V]</p> <p>Input Voltage [V]</p>				<table border="1"> <thead> <tr> <th rowspan="2">Input Voltage [V]</th> <th colspan="2">Output Voltage [V]</th> </tr> <tr> <th>Load 50%</th> <th>Load 100%</th> </tr> </thead> <tbody> <tr><td>75</td><td>23.969</td><td>23.947</td></tr> <tr><td>80</td><td>23.968</td><td>23.948</td></tr> <tr><td>85</td><td>23.968</td><td>23.949</td></tr> <tr><td>90</td><td>23.968</td><td>23.949</td></tr> <tr><td>100</td><td>23.967</td><td>23.949</td></tr> <tr><td>110</td><td>23.967</td><td>23.949</td></tr> <tr><td>120</td><td>23.967</td><td>23.949</td></tr> <tr><td>132</td><td>23.967</td><td>23.950</td></tr> <tr><td>140</td><td>23.967</td><td>23.952</td></tr> </tbody> </table>				Input Voltage [V]	Output Voltage [V]		Load 50%	Load 100%	75	23.969	23.947	80	23.968	23.948	85	23.968	23.949	90	23.968	23.949	100	23.967	23.949	110	23.967	23.949	120	23.967	23.949	132	23.967	23.950	140	23.967	23.952
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<p>Note: Slanted line shows the range of the rated input voltage.</p> <p>(注) 斜線は定格入力電圧範囲を示す。</p>																																							

Model	ADA750F (ADA750F-24)	Temperature	25°C
Item	Input Current (by Load Current) 入力電流 (負荷電力特性)	Testing Circuitry	Figure A
Object	_____		

1. Graph

- △— Input Volt. 85 V
- - -□- - - Input Volt. 100 V
- · - ○ - · - Input Volt. 132 V



Note: Slanted line shows the range of the rated load power.

(注) 斜線は定格電力範囲を示す。

2. Values

Load Power [W]	Input Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	0.300	0.270	0.250
120	1.940	1.660	1.290
240	3.460	2.960	2.270
360	5.020	4.250	3.220
480	6.580	5.600	4.240
600	8.260	6.920	5.210
660	9.100	7.600	5.730
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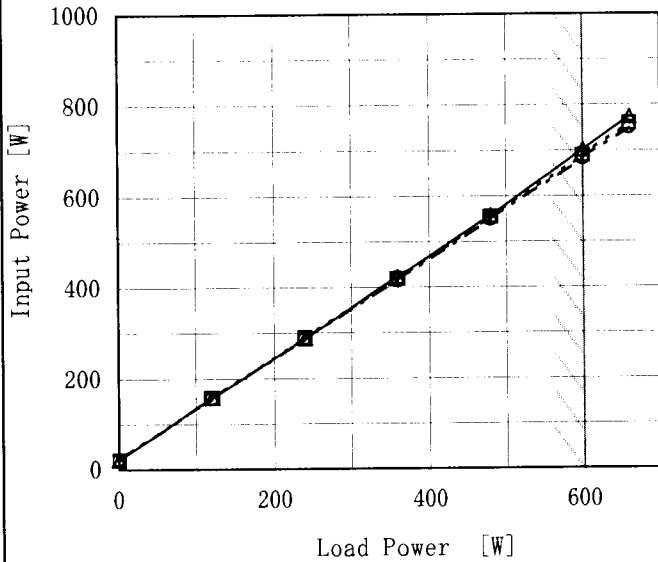


Model	ADA750F (ADA750F-24)
Item	Input Power (by Load Power) 入力電力 (負荷電力特性)
Object	_____

Temperature 25°C
Testing Circuitry Figure A

1. Graph

—△— Input Volt. 85 V
 ---□--- Input Volt. 100 V
 -·-○-·- Input Volt. 132 V



2. Values

Load Power [W]	Input Power [W]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0	21.0	21.0	21.0
120	158.0	158.0	156.0
240	288.0	290.0	288.0
360	423.0	419.0	416.0
480	558.0	555.0	549.0
600	702.0	689.0	683.0
660	774.0	759.0	750.0
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--	--	--	--
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Note: Slanted line shows the range of the rated load power.

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Item		Efficiency (by Input Voltage) 効率 (入力電圧特性)																																	
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<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy. Note: Slanted line shows the range of the rated load power.</p>																																																										
<p>出力保持時間とは、入力電圧断から出力電圧が定電圧精度の範囲を保持しているところまでの時間。 (注) 斜線は定格電力範囲を示す。</p>																																																										

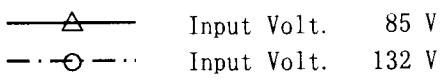
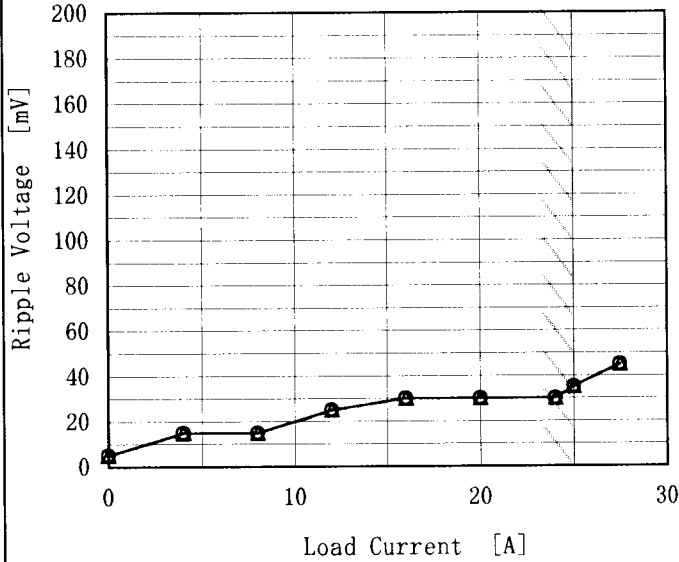
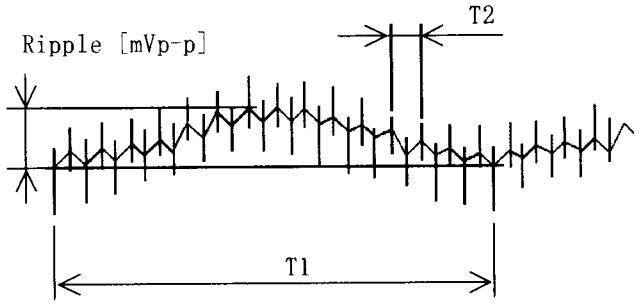


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Item		Instantaneous Interruption Compensation (by Load Power) 瞬時停電保障 (負荷電力特性)		Testing Circuitry		Figure A																																																				
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<p>1. Graph</p> <p>—△— Input Volt. 85V ---□--- Input Volt. 100V -·-○-·- Input Volt. 132V</p> <p>Instantaneous Compensation Time [mS]</p> <p>Load Power [W]</p>				<p>2. Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Load Power [W]</th> <th colspan="3">Time [mS]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0</td><td>—</td><td>—</td><td>—</td></tr> <tr><td>120</td><td>124</td><td>140</td><td>155</td></tr> <tr><td>240</td><td>47</td><td>52</td><td>63</td></tr> <tr><td>360</td><td>40</td><td>49</td><td>40</td></tr> <tr><td>480</td><td>24</td><td>35</td><td>39</td></tr> <tr><td>600</td><td>18</td><td>25</td><td>30</td></tr> <tr><td>660</td><td>17</td><td>21</td><td>27</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>				Load Power [W]	Time [mS]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	0	—	—	—	120	124	140	155	240	47	52	63	360	40	49	40	480	24	35	39	600	18	25	30	660	17	21	27	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--
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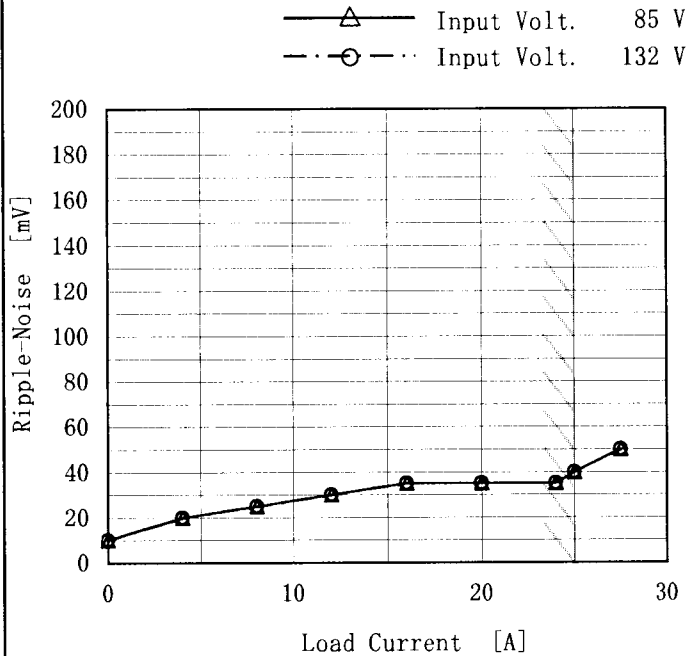
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Item		Load Regulation 静的負荷変動		Testing Circuitry Figure A																																																				
Object		V1:+24V25A																																																						
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Model		ADA750F (ADA750F-24)	Temperature		25°C																																						
Item		Ripple Voltage (by Load Current) リップル電圧 (負荷特性)	Testing Circuitry		Figure A																																						
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<p>  </p>  <p>Ripple Voltage is shown as p-p in the figure below. Note: Slanted line shows the range of the rated load current.</p> <p>リップル電圧は、下図 p-p 値で示される。 (注) 斜線は定格負荷電流範囲を示す。</p> <p> T1: Due to AC Input Line 入力商用周期 T2: Due to Switching スイッチング周期 </p>  <p>Fig. Complex Ripple Wave Form 図 リップル波形詳細図</p>			<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="2">Ripple Output Voltage [mV]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>5</td><td>5</td></tr> <tr><td>4.0</td><td>15</td><td>15</td></tr> <tr><td>8.0</td><td>15</td><td>15</td></tr> <tr><td>12.0</td><td>25</td><td>25</td></tr> <tr><td>16.0</td><td>30</td><td>30</td></tr> <tr><td>20.0</td><td>30</td><td>30</td></tr> <tr><td>24.0</td><td>30</td><td>30</td></tr> <tr><td>25.0</td><td>35</td><td>35</td></tr> <tr><td>27.5</td><td>45</td><td>45</td></tr> <tr><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>			Load Current [A]	Ripple Output Voltage [mV]		Input Volt. 85[V]	Input Volt. 132[V]	0.0	5	5	4.0	15	15	8.0	15	15	12.0	25	25	16.0	30	30	20.0	30	30	24.0	30	30	25.0	35	35	27.5	45	45	--	--	--	--	--	--
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Model	ADA750F (ADA750F-24)	Temperature	25°C
Item	Ripple-Noise リップルノイズ	Testing Circuitry	Figure A
Object	V1:+24V25A		

1. Graph



2. Values

Load Current [A]	Ripple-Noise [mV]	
	Input Volt. 85 [V]	Input Volt. 132 [V]
0.0	10	10
4.0	20	20
8.0	25	25
12.0	30	30
16.0	35	35
20.0	35	35
24.0	35	35
25.0	40	40
27.5	50	50
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Ripple-Noise is shown as p-p in the figure below.
Note: Slanted line shows the range of the rated load current.

リップルノイズは、下図 p-p 値で示される。
(注) 斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
スイッチング周期

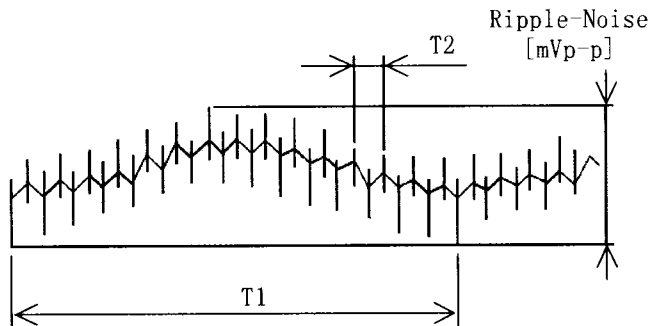


Fig. Complex Ripple Wave Form
図 リップル波形詳細図



<p>Model ADA750F (ADA750F-24)</p> <p>Item Overcurrent Protection 過電流保護</p> <p>Object V1:+24V25A</p>		<p>Temperature 25°C</p> <p>Testing Circuitry Figure A</p>																																																							
<p>1. Graph</p> <p>— Input Volt. 85 V</p> <p>— Input Volt. 100 V</p> <p>⋯ Input Volt. 132 V</p> <p>Output Voltage [V]</p> <p>Load Current [A]</p> <p>Note: Slanted line shows the range of the rated load current. (注) 斜線は定格負荷電流範囲を示す。</p> <p>Intermittent operation occurs when the output voltage is from 14.4V to 0V. 14.4V~0V間は、間欠モードとなる。</p>		<p>2. Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Output Voltage [V]</th> <th colspan="3">Load Current [A]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>24.0</td><td>52.35</td><td>52.68</td><td>52.99</td></tr> <tr><td>22.8</td><td>52.73</td><td>53.05</td><td>53.35</td></tr> <tr><td>21.6</td><td>53.16</td><td>53.51</td><td>53.80</td></tr> <tr><td>19.2</td><td>54.16</td><td>54.55</td><td>54.89</td></tr> <tr><td>16.8</td><td>55.51</td><td>55.80</td><td>56.13</td></tr> <tr><td>14.4</td><td>56.70</td><td>56.92</td><td>57.27</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>	Output Voltage [V]	Load Current [A]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	24.0	52.35	52.68	52.99	22.8	52.73	53.05	53.35	21.6	53.16	53.51	53.80	19.2	54.16	54.55	54.89	16.8	55.51	55.80	56.13	14.4	56.70	56.92	57.27	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--
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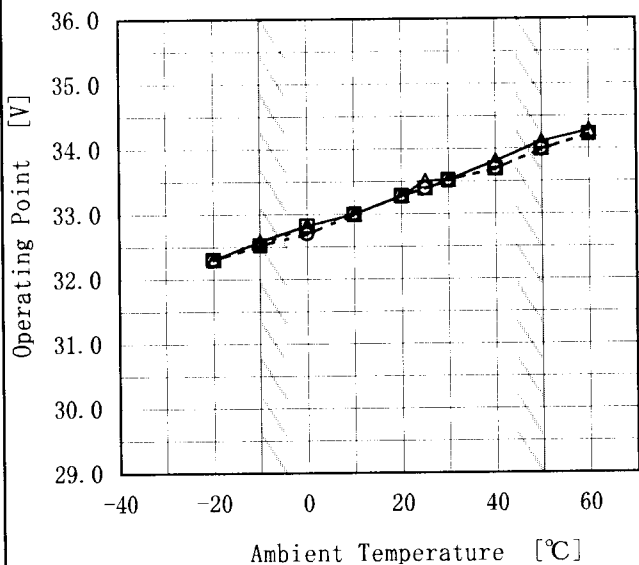


Model	ADA750F (ADA750F-24)
Item	Overvoltage Protection 過電圧保護
Object	V1:+24V25A

Testing Circuitry Figure A

1. Graph

—△— Input Volt. 85 V
 ---□--- Input Volt. 100 V
 -·-○-·- Input Volt. 132 V



Load 0%

Note: Slanted line shows the range of the rated ambient temperature.

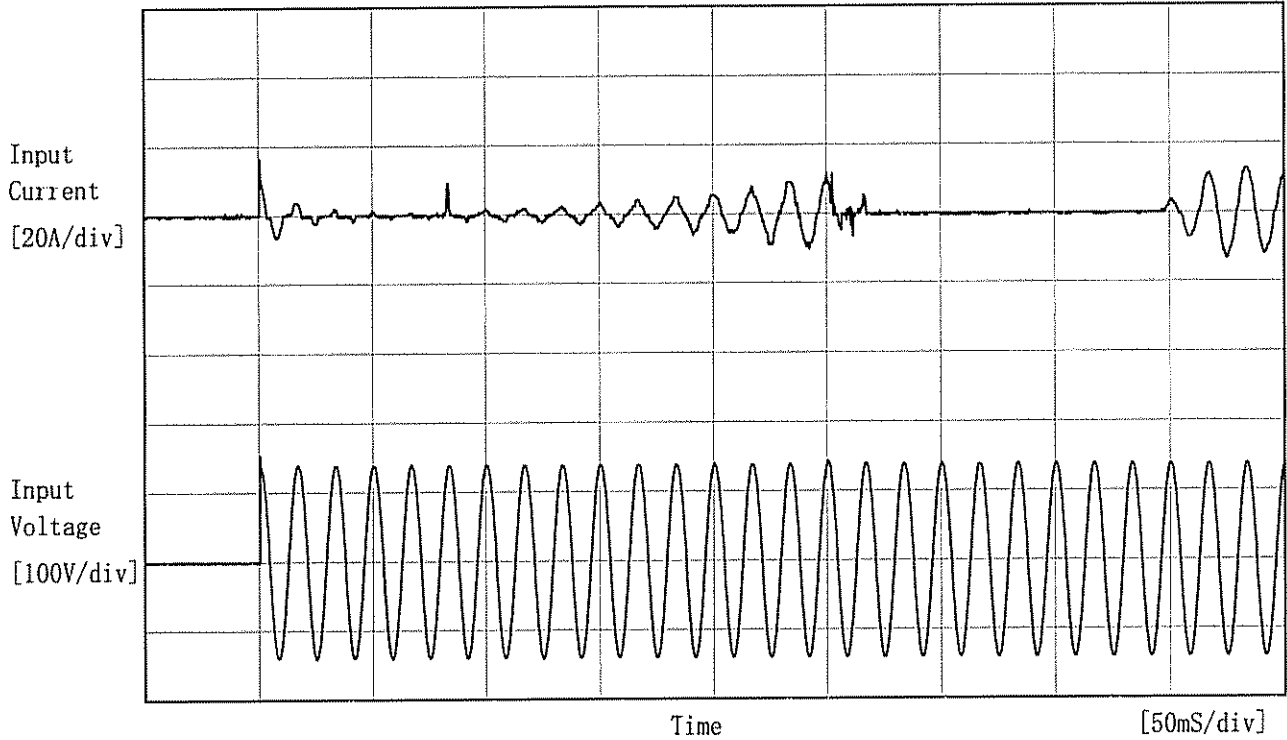
(注) 斜線は定格周囲温度範囲を示す。

2. Values

Ambient Temperature [°C]	Operating Point [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
-20	32.30	32.30	32.30
-10	32.58	32.52	32.52
0	32.82	32.82	32.70
10	33.00	33.00	33.00
20	33.28	33.28	33.28
25	33.51	33.39	33.39
30	33.52	33.52	33.52
40	33.81	33.69	33.69
50	34.11	33.99	33.99
60	34.28	34.22	34.22
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COSEL

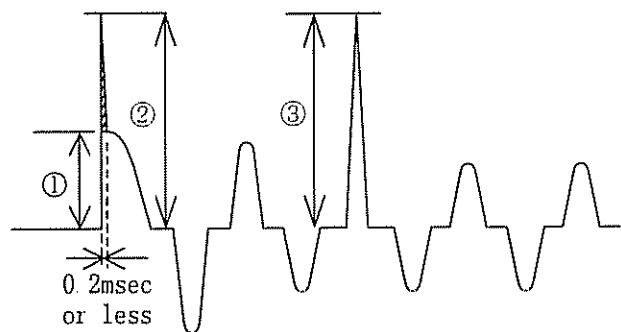
Model	ADA750F (ADA750F-24)	Temperature	25°C
Item	Inrush Current 突入電流	Testing Circuitry	Figure A
Object	_____		



Input Voltage 100 V
 Frequency 60 Hz
 Load 100 %

Inrush Current

- ① 11.4 [A]
- ② 16.2 [A] (0.2msec or less)*1
- ③ 7.2 [A]



*1 The specification of the inrush current (primary surge) means that the surge current to a built-in noise filter (0.2msec or less : waveform ②) is excluded.

本製品の突入電流(1次サージ)の仕様は、内蔵ノイズフィルタ部へのサージ電流(0.2msec以下:波形②)を除きます。

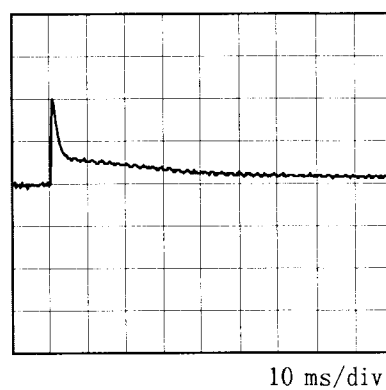
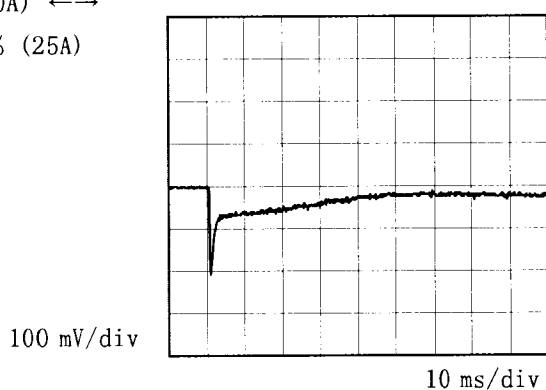


Model	ADA750F (ADA750F-24)	Temperature	25°C
Item	Dynamic Load Response 動的負荷変動	Testing Circuitry	Figure A
Object	V1:+24V25A		

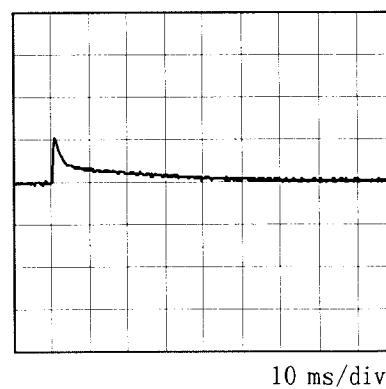
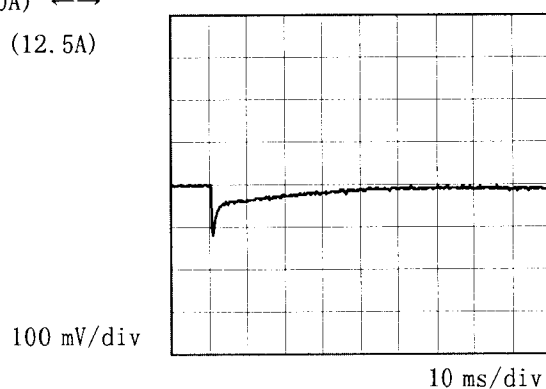
Input Volt. AC100 V
Cycle 1000 ms



Min. Load (0A) ←→
Load 100% (25A)



Min. Load (0A) ←→
Load 50% (12.5A)

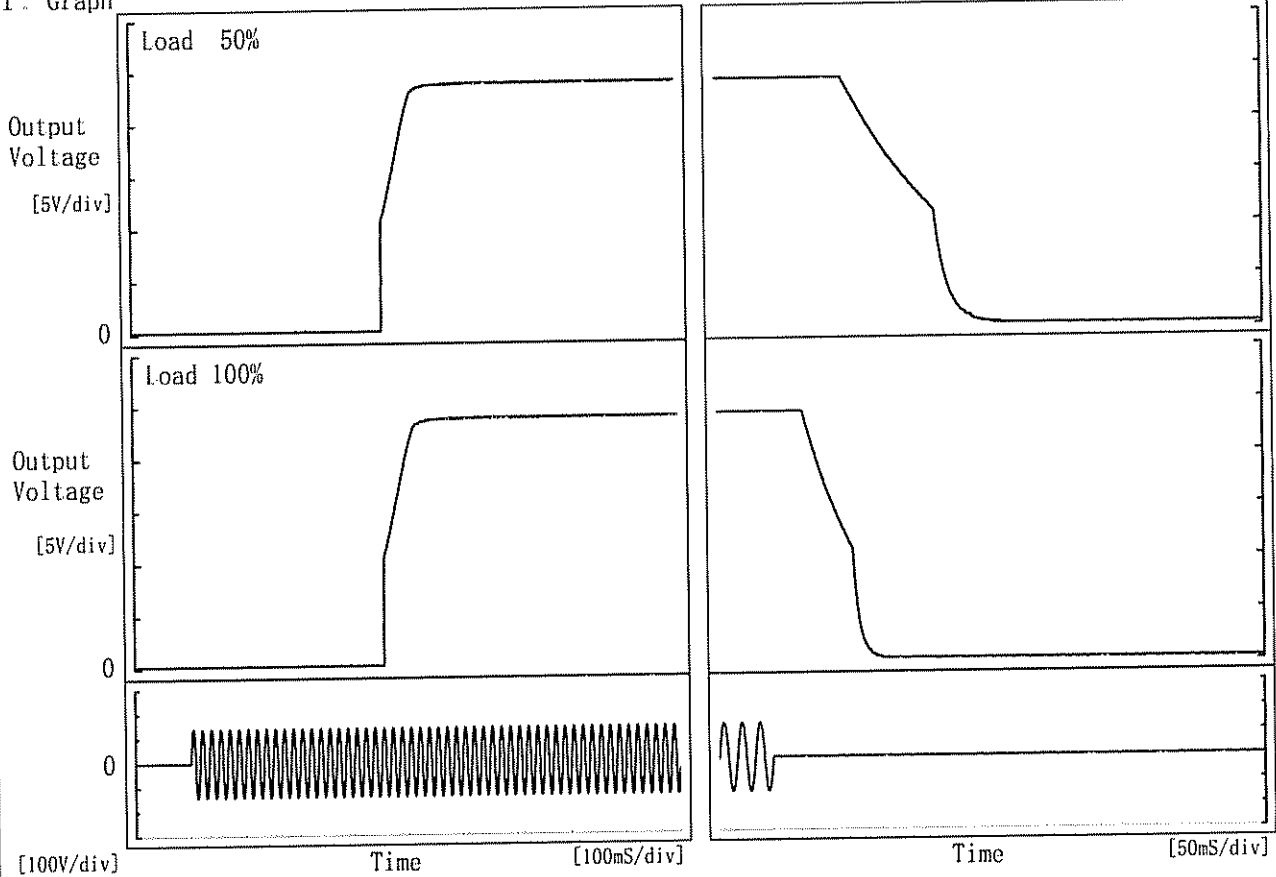




Model	ADA750F (ADA750F-24)	Temperature	25°C
Item	Rise and Fall Time 立上り、立下り時間	Testing Circuitry	Figure A
Object	VI:+24V25A		

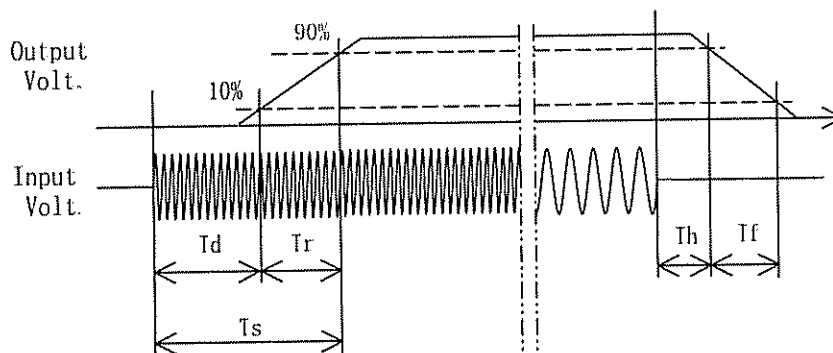
1. Graph

Input Volt. 100 V



2. Values

		[mS]				
Load	Time	T _d	T _r	T _s	T _h	T _f
50 %		357.0	49.5	406.5	77.0	92.5
100 %		356.5	50.0	406.5	34.3	49.3

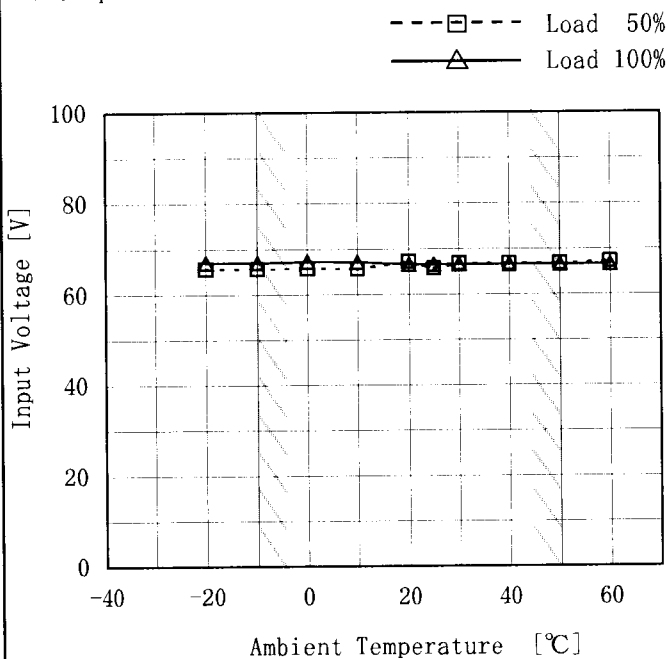


Model		ADA750F (ADA750F-24)		Testing Circuitry Figure A																																																				
Item		Ambient Temperature Drift 周囲温度変動																																																						
Object		V1:+24V25A																																																						
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Model	ADA750F (ADA750F-24)
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	V1:+24V25A

Testing Circuitry Figure A

1. Graph



2. Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	66	67
-10	66	67
0	66	67
10	66	67
20	67	67
25	66	67
30	67	67
40	67	67
50	67	67
60	67	67
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Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。



COSEL																												
Model	ADA750F (ADA750F-24)																											
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)	Testing Circuitry Figure A																										
Object	V1:+24V25A																											
<p>1. Graph</p> <p style="text-align: center;">Ripple Voltage [mV]</p> <p style="text-align: center;">Ambient Temperature [°C]</p> <p>Input Volt. 100 V</p> <p>Load 100 %</p> <p>Note: Slanted line shows the range of the rated ambient temperature.</p> <p>(注) 斜線は定格周囲温度範囲を示す。</p>		<p>2. Values</p> <table border="1"> <thead> <tr> <th>Ambient Temperature [°C]</th> <th>Ripple Voltage [mV]</th> </tr> </thead> <tbody> <tr><td>-30</td><td>105</td></tr> <tr><td>-10</td><td>40</td></tr> <tr><td>0</td><td>30</td></tr> <tr><td>25</td><td>30</td></tr> <tr><td>50</td><td>25</td></tr> <tr><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td></tr> </tbody> </table>	Ambient Temperature [°C]	Ripple Voltage [mV]	-30	105	-10	40	0	30	25	30	50	25	--	--	--	--	--	--	--	--	--	--	--	--	--	--
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<p>Model ADA750F (ADA750F-24)</p>		<p>Temperature 25°C Testing Circuitry Figure A</p>																						
<p>Item Time Lapse Drift 経時ドリフト</p>																								
<p>Object VI:+24V25A</p>																								
<p>1. Graph</p> <p style="text-align: center;">Time [H]</p> <p>Input Volt. 100V Load 100%</p>		<p>2. Values</p> <table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>23.971</td></tr> <tr><td>0.5</td><td>23.956</td></tr> <tr><td>1.0</td><td>23.956</td></tr> <tr><td>2.0</td><td>23.957</td></tr> <tr><td>3.0</td><td>23.957</td></tr> <tr><td>4.0</td><td>23.957</td></tr> <tr><td>5.0</td><td>23.958</td></tr> <tr><td>6.0</td><td>23.958</td></tr> <tr><td>7.0</td><td>23.958</td></tr> <tr><td>8.0</td><td>23.959</td></tr> </tbody> </table>	Time since start [H]	Output Voltage [V]	0.0	23.971	0.5	23.956	1.0	23.956	2.0	23.957	3.0	23.957	4.0	23.957	5.0	23.958	6.0	23.958	7.0	23.958	8.0	23.959
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COSEL		
Model	ADA750F (ADA750F-24)	
Item	Output Voltage Accuracy 定電圧精度	Testing Circuitry Figure A
Object	V1:+24V25A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10 ~ 50°C

Input Voltage : 85 ~ 132V

Load Current : 0 ~ 25A

* Output Voltage Accuracy = $\pm (\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

* Output Voltage Accuracy (Ration) = $\frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$

1. 定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 : -10 ~ 50°C

入力電圧 : 85 ~ 132V

負荷電流 : 0 ~ 25A

* 定電圧精度(変動値) = $\pm (\text{出力電圧の最高値} - \text{出力電圧の最低値}) / 2$

* 定電圧精度(変動率) = $\frac{\text{変動値}}{\text{定格出力電圧}} \times 100$

2. Values

Item	Temperature [°C]	Input Voltage [V]	Output		Output Voltage Accuracy	
			Current [A]	Voltage [V]	Value [mV]	Ration [%]
Maximum Voltage	25	85	0	24.000	±25	±0.1
Minimum Voltage	-10	85	25	23.950		

COSEL

Model	ADA750F (ADA750F-24)	Temperature	25°C
Item	Leakage Current 漏洩電流	Testing Circuitry	Figure B
Object			

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DEN-AN	0.19	0.22	0.28
(B) IEC60950	0.19	0.22	0.28

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 240 [V]	Input Volt. 264 [V]
(B) IEC60950	—	—	—

2. Condition

Leakage current value is concluded after measuring each phases of AC input and by choosing the larger one.

交流入力各相について測定し、その大きい方を漏洩電流測定値とする。

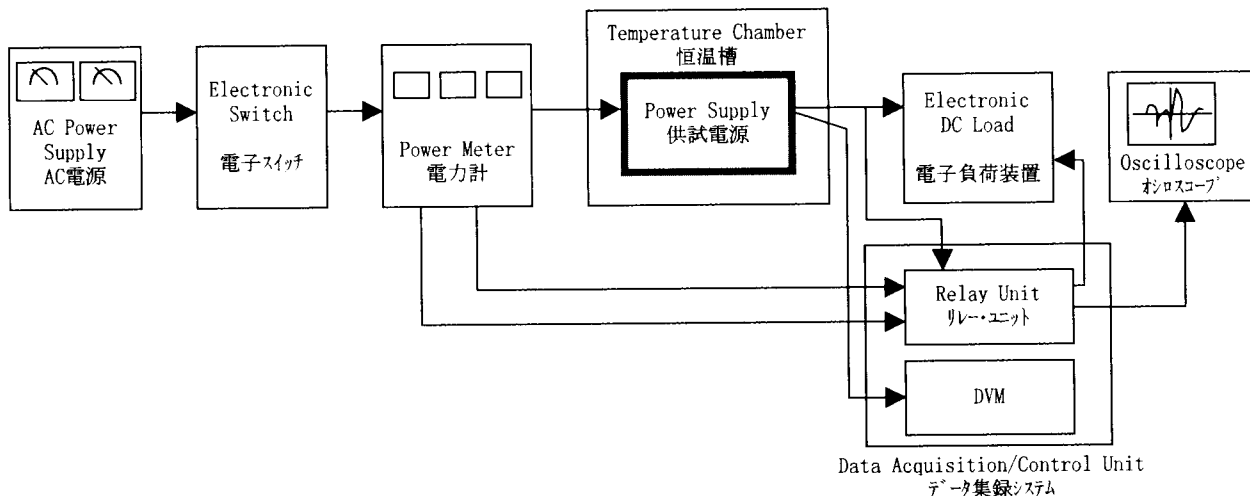


Figure A

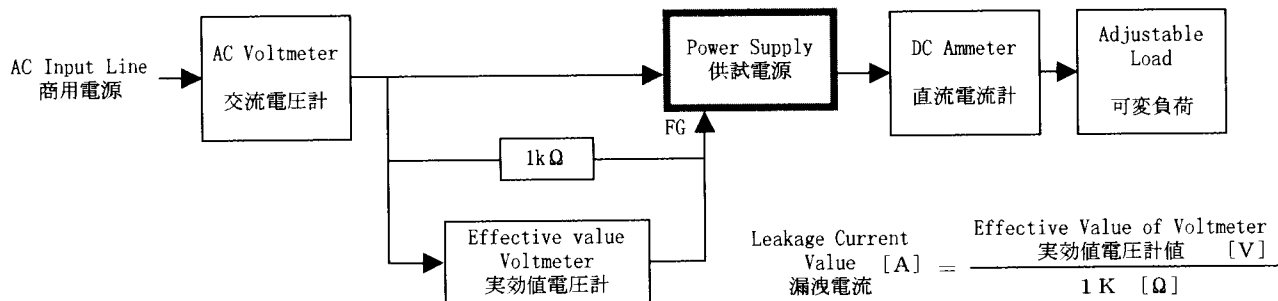


Figure B (DEN-AN)

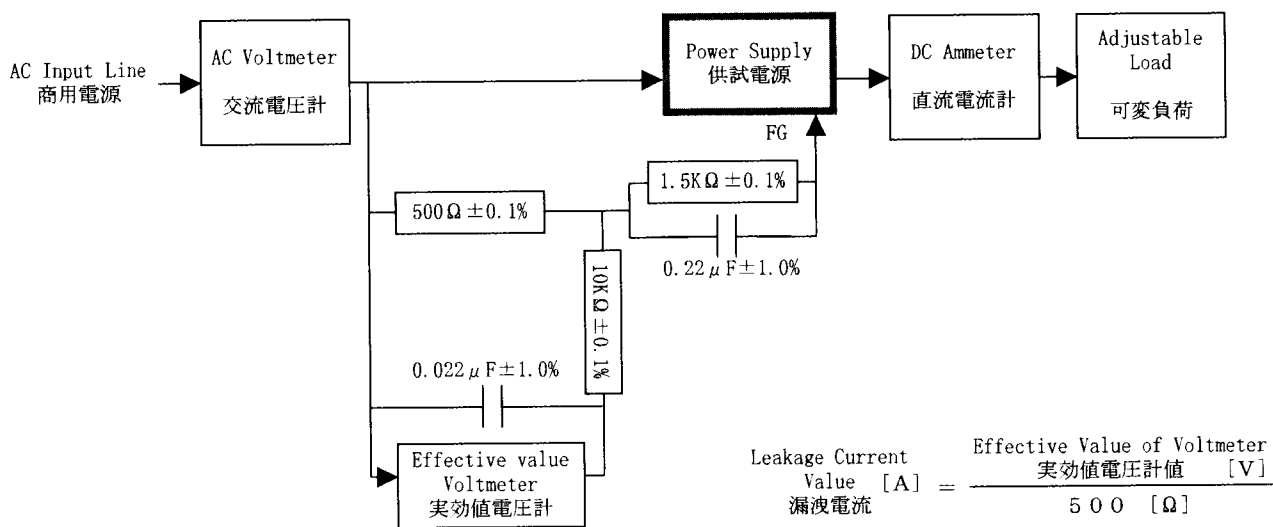


Figure B (IEC60950)